PIC16(L)F1773/6/7/8/9

28/40/44-Pin, 8-Bit Flash Microcontroller Product Brief

Description

PIC16(L)F177X microcontrollers feature a high level of integration of intelligent analog and digital peripherals for a wide range of applications, such as lighting, power supplies, battery charging, motor control and other general purpose applications. These devices deliver multiple op amps, 5-/10-bit DACs, high-speed comparators, 10-bit ADC, 10-/16-bit PWMs, programmable ramp generator (PRG) and other peripherals that can be connected internally to create closed loop systems without using pins or PCB area. The 10-/16-bit PWMs, digital signal modulators and tri-state output op amp can be used together to create a LED dimming engine for lighting applications. The peripheral pin select (PPS) functionality provides flexibility, eases PCB layout and peripheral utilization by allowing digital peripheral pin mapping to an I/O.

Core Features

- C Compiler Optimized RISC Architecture
- Only 49 Instructions
- Operating Speed:
  - DC – 32 MHz clock input
  - 125 ns minimum instruction cycle
- Interrupt Capability
- 16-Level Deep Hardware Stack
- Five 8-Bit Timers
- Three 16-Bit Timers
- Low-Current Power-on Reset (POR)
- Configurable Power-up Timer (PWRT)
- Brown-out Reset (BOR) with Selectable Trip Point
- Extend Watchdog Timer (EWDT):
  - Low-power 31 kHz WDT
  - Software selectable pre-scaler
  - Software selectable enable

Memory

- Up to 28 Kbytes Program Flash Memory (PFM)
- Up to 2 Kbytes Data RAM
- Direct, Indirect and Relative Addressing modes
- High-Endurance Flash (HEF):
  - 128B of nonvolatile data storage
  - 100K Erase/Write cycles

Operating Characteristics

- Operating Voltage Range:
  - 1.8V to 3.6V (PIC16LF1773/6/7/8/9)
  - 2.3V to 5.5V (PIC16F1773/6/7/8/9)
- Temperature Range:
  - Industrial: -40°C to 85°C
  - Extended: -40°C to 125°C

eXtreme Low-Power (XLP) Features

- Sleep mode: 50 nA @ 1.8V, typical
- Watchdog Timer: 500 nA @ 1.8V, typical
- Secondary Oscillator: 500 nA @ 32 kHz
- Operating Current:
  - 8 uA @ 31 kHz, 1.8V, typical
  - 32 uA/MHz @ 1.8V, typical

Intelligent Analog Peripherals

- 10-Bit Analog-to-Digital Converter (ADC):
  - Up to 28 external channels
  - Conversion available during Sleep
- Up to Four Operational Amplifiers (OPA):
  - Selectable internal and external channels
  - Tri-state output
  - Part of LED dimming engine
  - Selectable internal and external channels
- Up to Eight High-Speed Comparators (HS Comp):
  - Up to nine external inverting inputs
  - Up to 12 external noninverting inputs
  - Fixed voltage reference at inverting and noninverting input(s)
  - Comparator outputs externally accessible
- Digital-to-Analog Converters (DAC):
  - Up to four 10-bit resolution DACs
  - 10-bit resolution, rail-to-rail
  - Conversion during Sleep
  - Internal connections to ADCs and HS Comparators
- Voltage Reference:
  - Fixed Voltage Reference (FVR)
  - 1.024V, 2.048V and 4.096V output levels
- Zero-Cross Detector (ZCD):
  - Detect high-voltage AC signal
- Up to Four Programmable Ramp Generators (PRG):
  - Slope compensation
  - Ramp generation
- Two High-Current Drive I/Os:
  - Up to 100 mA sink or source @ 5V

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Digital Peripherals

- Up to Four Configurable Logic Cells (CLC):
  - Integrated combinational and state logic
- Up to Four Complementary Output Generators (COG):
  - Push-pull, Full-Bridge and Steering modes
- Up to Four Capture/Compare/PWM (CCP) Modules
- Pulse-Width Modulator (PWM):
  - Up to four 16-bit PWMs
  - Independent timers
  - Multiple output modes (edge-, center-aligned, set and toggle on register match)
  - User settings for phase, duty cycle, period, offset and polarity
  - 16-bit timer capability
  - Up to four 10-bit PWMs
- Up to Four Data Signal Modulators (DSM):
  - Modulates a carrier signal with a digital data to create custom carrier synchronized output waveforms
  - Part of LED dimming engine
- Peripheral Pin Select (PPS):
  - I/O remapping of digital peripherals
- Serial Communications:
  - Enhanced USART (EUSART)
  - SPI, I^2C™, RS-232, RS-485, LIN compatible
  - Auto-Baud Detect, auto-wake-up on start
- Up to 38 I/O Pins:
  - Individually programmable pull-ups
  - Slew rate control
  - Interrupt-on-change with edge-select

Clocking Structure

- Precision Internal Oscillator:
  - ±1% at calibration
  - Selectable frequency range 32 MHz to 31 kHz
- 31 kHz Low-Power Internal Oscillator
- 4x Phase-Locked Loop (PLL) for up to 32 MHz Internal Operation
- External Oscillator Block with Three External Clock modes up to 32 MHz

TABLE 1: PIC16(L)F1773/6/7/8/9 FAMILY TYPES

Device	Data Sheet Index	Program Flash Memory (bytes)	Program Flash Memory (word)	High Endurance Flash (B)	Data SRAM (Bytes)	I/O Pins(1)	8-Bit/16-Bit Timers	High-Speed Comparator	10-bit ADC (ch)	5/10-bit DAC	CCP	10-bit/16-bit PWM	COG	CLC	Op Amp	Zero Cross Detect	Data Signal Modulator	Programmable Ramp Gen	High-Current I/Os	Peripheral Pin Select	EUSART	I^2C™/SPI	Debug(2)
PIC16(L)F1773	(A)	7K	4K	128	512	25	5/3	6	17	3/3	3	3/3	3	4	3	1	3	3	2	Y	1	1	I
PIC16(L)F1776	(A)	14K	8K	128	1K	25	5/3	6	17	3/3	3	3/3	3	4	3	1	3	3	2	Y	1	1	I
PIC16(L)F1777	(B)	14K	8K	128	1K	36	5/3	8	28	4/4	4	4/4	4	4	4	1	4	4	2	Y	1	1	I
PIC16(L)F1778	(B)	28K	16K	128	2K	25	5/3	6	17	3/3	3	3/3	3	4	3	1	3	3	2	Y	1	1	I
PIC16(L)F1779	(B)	28K	16K	128	2K	36	5/3	8	28	4/4	4	4/4	4	4	4	1	4	4	2	Y	1	1	I

Note 1: One pin is input-only.
2: I – Debugging integrated on chip

Data Sheet Index:

A: Future Release PIC16(L)F1773/6 Data Sheet, 24-Pin, 8-bit Flash Microcontrollers
B: Future Release PIC16(L)F1777/8/9 Data Sheet, 24/40/44-Pin, 8-bit Flash Microcontrollers

Note: For other small form-factor package availability and marking information, please visit **http://www.microchip.com/packaging** or contact your local sales office.

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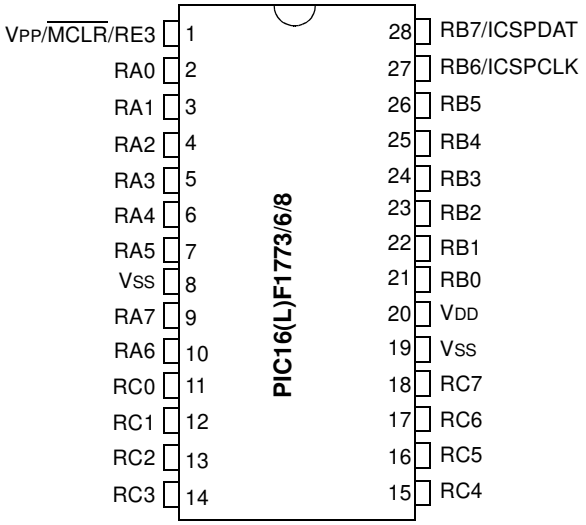
TABLE 2: PACKAGES

Packages	SPDIP	PDIP	SOIC	SSOP	UQFN	TQFP
PIC16(L)F1773	X		X	X	X	
PIC16(L)F1776	X		X	X	X	
PIC16(L)F1777		X			X	X
PIC16(L)F1778	X		X	X	X	
PIC16(L)F1779		X			X	X

Note: Pin details are subject to change.

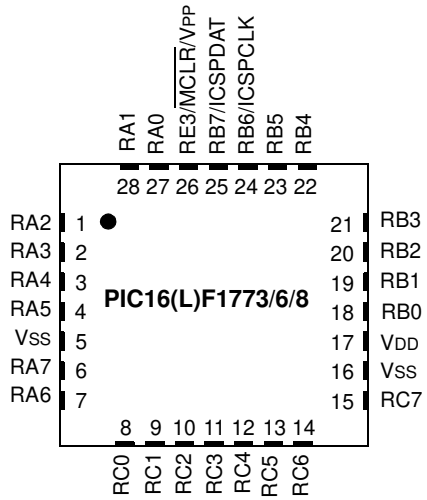
PIN DIAGRAMS

FIGURE 1: 28-PIN SPDIP, SOIC, SSOP



Note: See Table 3 for location of all peripheral functions.

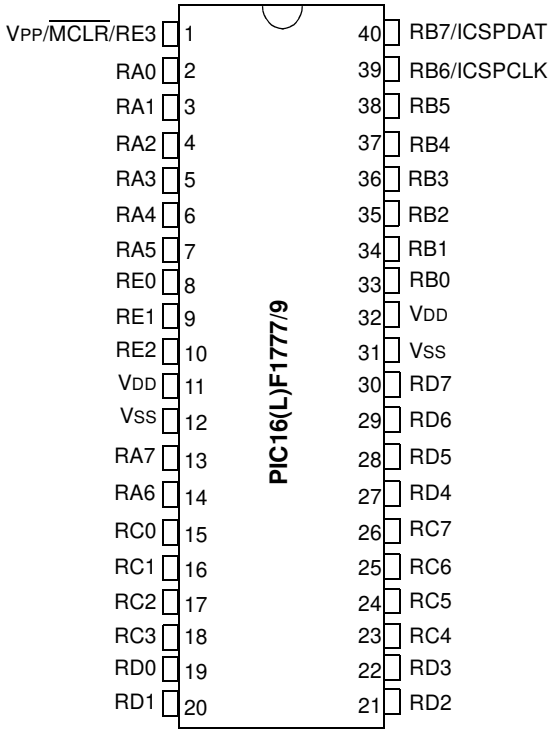
FIGURE 2: 28-PIN UQFN (6x6x0.5 mm)



Note: See Table 3 for location of all peripheral functions.

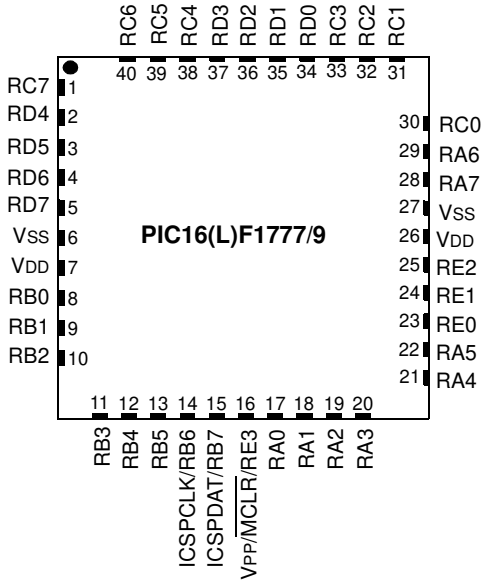
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FIGURE 3: 40-PIN PDIP



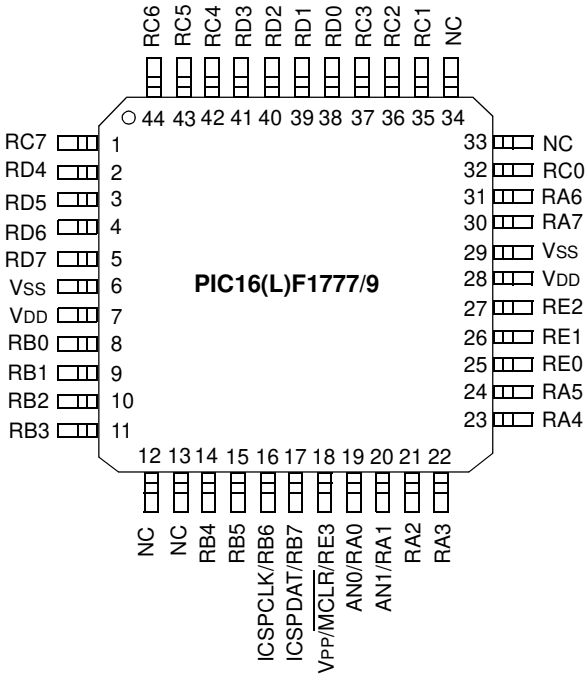
Note: See Table 4 for location of all peripheral functions.

FIGURE 4: 40-PIN UQFN (5x5x0.5 mm)



Note: See Table 4 for location of all peripheral functions.

FIGURE 5: 44-PIN TQFP (4x4 mm)



Note: See Table 4 for location of all peripheral functions.

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PIN ALLOCATION TABLES

TABLE 3: 28-PIN ALLOCATION TABLE (PIC16(L)F1773/6/8)

I/O	28-Pin SPDIP/SOIC/SSOP	28-Pin UQFN	ADC	Voltage Reference	DAC	Op Amp	Comparator	Zero Cross	Programmable Ramp Generator	Timer	PWM	CCP	COG	CLC	Modulator	EUSART	MSSP	Interrupts	Pull-ups	High Current	Basic
RA0	2	27	AN0	—	—	—	C1IN0- C2IN0- C3IN0- C4IN0- C5IN0- C6IN0-	—	—	—	—	—	—	CLCIN1(1)	—	—	—	IOC	—	—	—
RA1	3	28	AN1	—	—	OPA1OUT OPA2IN1+ OPA2IN1-	C1IN1- C2IN1- C3IN1- C4IN1-	—	PRG1IN0 PRG2IN1	—	—	—	—	CLCIN2(1)	—	—	—	IOC	—	—	—
RA2	4	1	AN2 VREF-	DAC1REF0- DAC2REF0- DAC3REF0- DAC4REF0- DAC5REF0- DAC7REF0-	DAC1OUT1	—	C1IN0+ C2IN0+ C3IN0+ C4IN0+ C5IN0+ C6IN0+	—	—	—	—	—	—	—	—	—	—	IOC	—	—	—
RA3	5	2	AN3 VREF+	DAC1REF0+ DAC2REF0+ DAC3REF0+ DAC4REF0+ DAC5REF0+ DAC7REF0+	—	—	C1IN1+	—	—	—	—	—	—	—	MD1CL(1)	—	—	IOC	—	—	—
RA4	6	3	—	—	DAC4OUT1	OPA1IN0+	—	—	PRG1R(1)	T0CKI(1)	—	—	—	—	MD1CH(1)	—	—	IOC	—	—	—
RA5	7	4	AN4	—	DAC2OUT1	OPA1IN0-	—	—	PRG1F(1)	—	—	—	—	—	MD1MOD(1)	—	$\overline{SS}$	IOC	—	—	—
RA6	10	7	—	—	—	—	CLKOUT C6IN1+	—	—	—	—	—	—	—	—	—	—	IOC	—	—	OSC2
RA7	9	6	—	—	—	—	CLKIN	—	—	—	—	—	—	—	—	—	—	IOC	—	—	OSC1

Note 1: Default peripheral input. Input can be moved to any other pin with the PPS input selection register.
2: All pin outputs default to PORT latch data. Any pin can be selected as a digital peripheral output with the PPS output selection registers.
3: These peripheral functions are bidirectional. The output pin selections must be the same as the input pin selections.

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TABLE 3: 28-PIN ALLOCATION TABLE (PIC16(L)F1773/6/8) (CONTINUED)

I/O	28-Pin SPDIP/SOIC/SSOP	28-Pin UQFN	ADC	Voltage Reference	DAC	Op Amp	Comparator	Zero Cross	Programmable Ramp Generator	Timer	PWM	CCP	COG	CLC	Modulator	EUSART	MSSP	Interrupts	Pull-ups	High Current	Basic
RB0	21	18	AN12	HIB0	—	—	C2IN1+	ZCD	—	—	—	—	COG1IN(1)	—	—	—	—	IOC	—	—	—
RB1	22	19	AN10	HIB1	—	OPA2OUT OPA1IN1+ OPA1IN1-	C1IN3- C2IN3- C3IN3- C4IN3-	—	PRG1IN1 PRG2IN0	—	—	—	COG2IN(1)	—	—	—	—	IOC	—	—	—
RB2	23	20	AN8	—	DAC3OUT1	OPA2IN0-	—	—	—	—	—	—	COG3IN(1)	—	—	—	—	IOC	—	—	—
RB3	24	21	AN9	—	—	OPA2IN0+	C1IN2- C2IN2- C3IN2-	—	—	—	—	—	—	—	MD3CL(1)	—	—	IOC	—	—	—
RB4	25	22	ADCACT	—	—	—	C3IN1+	—	—	T5G(1)	—	—	—	—	MD3CH(1)	—	—	IOC	—	—	—
RB5	26	23	AN13	DAC5REF1- DAC7REF1-	—	—	C4IN2-	—	—	T1G(1)	—	CCP3(1)	—	—	MD3MOD(1)	—	—	IOC	—	—	—
RB6	27	24	—	DAC5REF1+ DAC7REF1+	—	—	C4IN1+	—	—	—	—	—	—	CLCIN3(1)	—	—	—	IOC	—	—	ICSPCLK ICDCLK
RB7	28	25	—	—	DAC1OUT2 DAC2OUT2 DAC5OUT2 DAC3OUT2 DAC4OUT2 DAC7OUT2	—	C5IN1+	—	—	T6CKI(1)	—	—	—	CLCIN4(1)	—	—	—	IOC	—	—	ICSPDAT ICDDAT

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I/O	28-Pin SPDIP/SOIC/SSOP	28-Pin UQFN	ADC	Voltage Reference	DAC	Op Amp	Comparator	Zero Cross	Programmable Ramp Generator	Timer	PWM	CCP	COG	CLC	Modulator	EUSART	MSSP	Interrupts	Pull-ups	High Current	Basic
RC0	11	8	—	—	DAC5OUT1	—	—	—	—	T1CKI(1) T3CKI(1) T3G(1)	—	—	—	—	—	—	—	IOC	—	—	—
RC1	12	9	—	—	DAC7OUT1	—	—	—	PRG2R(2)	—	—	CCP2(1)	—	—	—	—	—	IOC	—	—	—
RC2	13	10	AN14	—	—	—	C5IN2- C6IN2-	—	PRG2F(1)	T5CKI(1)	—	CCP1(1)	—	—	—	—	—	IOC	—	—	—
RC3	14	11	AN15	—	—	—	C1IN4- C2IN4- C3IN4- C4IN4- C5IN4- C6IN4-	—	—	T2CKI(1)	—	—	—	—	MD2CL(1)	—	SDI(1) SDA(1,3)	IOC	—	—	—
RC4	15	12	AN16	—	—	—	C5IN3- C6IN3-	—	PRG3R(1)	T8CKI(1)	—	—	—	—	MD2CH(1)	—	SCL(1) SCK(1,3)	IOC	—	—	—
RC5	16	13	AN17	—	—	OPA3IN0+	—	—	PRG3F(1)	T4CKI(1)	—	—	—	—	MD2MOD(1)	—	—	IOC	—	—	—
RC6	17	14	AN18	—	—	OPA3OUT	C5IN1- C6IN1-	—	PRG3IN0	—	—	—	—	—	—	CK(1)	—	IOC	—	—	—
RC7	18	15	AN19	—	—	OPA3IN0-	—	—	—	—	—	—	—	—	—	RX(1,3)	—	IOC	—	—	—
RE3	1	26	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	IOC	—	—	$\overline{\text{MCLR}}$ ICD
VDD	20	17	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	VDD
VSS	8	5	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	VSS
VSS	19	16	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	VSS
OUT(2)	—	—	—	—	—	—	C1OUT C2OUT C3OUT C4OUT C5OUT C6OUT	—	—	—	PWM1 PWM2 PWM3 PWM4 PWM5 PWM6	CCP1 CCP2 CCP3	COG1A COG1B COG1C COG1D COG2A COG2B COG2C COG2D COG3A COG3B COG3C COG3D	CLC1OUT CLC2OUT CLC3OUT CLC4OUT	MD1OUT MD2OUT MD3OUT	DT(3) TX CK	SDO SDA(3) SCK SCL(3)	—	—	—	—

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3: These peripheral functions are bidirectional. The output pin selections must be the same as the input pin selections.

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TABLE 4: 40/44-PIN ALLOCATION TABLE (PIC16(L)F1777/9)

I/O	40-Pin PDIP	40-Pin UQFN	44-Pin TQFP	ADC	Voltage Reference	DAC	Op Amp	Comparator	Zero Cross	Programmable Ramp Generator	Timers	PWM	CCP	COG	CLC	Modulator	EUSART	MSSP	Interrupt	Pull-ups	High Current	Basic
RA0	2	17	19	AN0	—	—	—	C1IN0- C2IN0- C3IN0- C4IN0- C5IN0- C6IN0- C7IN0- C8IN0-	—	—	—	—	—	—	—	—	—	—	IOC	—	—	—
RA1	3	18	20	AN1	—	—	OPA1OUT OPA2IN1+ OPA2IN1-	C1IN1- C2IN1- C3IN1- C4IN1-	—	PRG1IN0 PRG2IN1	—	—	—	—	—	—	—	—	IOC	—	—	—
RA2	4	19	21	AN2	DAC1REF0- DAC2REF0- DAC3REF0- DAC4REF0- DAC5REF0- DAC6REF0- DAC7REF0- DAC8REF0-	DAC1OUT1	—	C1IN0+ C2IN0+ C3IN0+ C4IN0+ C5IN0+ C6IN0+ C7IN0+ C8IN0+	—	—	—	—	—	—	—	—	—	—	IOC	—	—	—
RA3	5	20	22	AN3	DAC1REF0+ DAC2REF0+ DAC3REF0+ DAC4REF0+ DAC5REF0+ DAC6REF0+ DAC7REF0+ DAC8REF0+	—	—	C1IN1+	—	—	—	—	—	—	—	MD1CL(1)	—	—	IOC	—	—	—
RA4	6	21	23	—	—	—	OPA1IN0+	—	—	PRG1R(1)	T0CKI(1)	—	—	—	—	MD1CH(1)	—	—	IOC	—	—	—
RA5	7	22	24	AN4	—	DAC2OUT1	OPA1IN0-	—	—	PRG1F(1)	—	—	—	—	—	MD1MOD(1)	—	$\overline{SS}$	IOC	—	—	—
RA6	14	29	31	—	—	—	—	CLKOUT C6IN1+	—	—	—	—	—	—	—	—	—	—	IOC	—	—	OSC2
RA7	13	28	30	—	—	—	—	CLKIN	—	—	—	—	—	—	—	—	—	—	IOC	—	—	OSC1

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I/O	40-Pin PDIP	40-Pin UQFN	44-Pin TQFP	ADC	Voltage Reference	DAC	Op Amp	Comparator	Zero Cross	Programmable Ramp Generator	Timers	PWM	CCP	COG	CLC	Modulator	EUSART	MSSP	Interrupt	Pull-ups	High Current	Basic
RB0	33	8	8	AN12	HIB0	—	—	C2IN1+	ZCD	—	—	—	CCP4(1)	COG1IN(1)	—	MD4CL(1)	—	—	IOC INT	—	—	—
RB1	34	9	9	AN10	HIB1	—	OPA2OUT OPA1IN1+ OPA1IN1-	C1IN3- C2IN3- C3IN3- C4IN3-	—	PRG4R(1)	—	—	—	COG2IN(1)	—	MD4CH(1)	—	—	IOC	—	—	—
RB2	35	10	10	AN8	—	—	OPA2IN0-	—	—	PRG4F(1)	—	—	—	COG3IN(1)	—	MD4MOD(1)	—	—	IOC	—	—	—
RB3	36	11	11	AN9	—	—	OPA2IN0+	C1IN2- C2IN2- C3IN2-	—	—	—	—	—	COG4IN(1)	—	MD3CL(1)	—	—	IOC	—	—	—
RB4	37	12	14	ADCACT	—	—	—	C3IN1+	—	—	T5G(1)	—	—	—	—	MD3CH(1)	—	—	IOC	—	—	—
RB5	38	13	15	AN13	DAC5REF1- DAC7REF1-	—	—	C4IN2-	—	—	T1G(1)	—	CCP3(1)	—	—	MD3MOD(1)	—	—	IOC	—	—	—
RB6	39	14	16	—	DAC5REF1+ DAC7REF1+	—	—	C4IN1+	—	—	—	—	—	—	—	—	—	—	IOC	—	—	ICSPCLK ICDCLK
RB7	40	15	17	—	—	DACxOUT2	—	C5IN1+	—	—	T6CKI(1)	—	—	—	—	—	—	—	IOC	—	—	ICSPDAT ICDDAT
RC0	15	30	32	—	—	DAC5OUT1	—	—	—	—	T1CKI(1) T3CKI(1) T3G(1)	—	—	—	—	—	—	—	IOC	—	—	—
RC1	16	31	35	—	—	DAC7OUT1	—	—	—	PRG2R(1)	SOSCO	—	CCP2(1)	—	—	—	—	—	IOC	—	—	—
RC2	17	32	36	AN14	—	—	—	C5IN2- C6IN2-	—	PRG2F(1)	T5CKI(1)	—	CCP1(1)	—	—	—	—	—	IOC	—	—	—
RC3	18	33	37	AN15	—	—	—	—	—	—	T2CKI(1)	—	—	—	—	MD2CL(1)	—	SDI(1) SDA(1,3)	IOC	—	—	—
RC4	23	38	42	AN16	—	—	—	C5IN3- C6IN3-	—	PRG3R(1)	T8CKI(1)	—	—	—	—	MD2CH(1)	—	SCL(1) SCK(1,3)	IOC	—	—	—

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I/O	40-Pin PDIP	40-Pin UQFN	44-Pin TQFP	ADC	Voltage Reference	DAC	Op Amp	Comparator	Zero Cross	Programmable Ramp Generator	Timers	PWM	CCP	COG	CLC	Modulator	EUSART	MSSP	Interrupt	Pull-ups	High Current	Basic
RC5	24	39	43	AN17	—	—	OPA3IN0+	—	—	PRG3F(1)	T4CKI(1)	—	—	—	—	MD2MOD(1)	—	—	IOC	—	—	—
RC6	25	40	44	AN18	—	—	OPA3OUT OPA4IN1+ OPA4IN1-	C5IN1- C6IN1- C7IN1- C8IN1-	—	PRG3IN0 PRG4IN1	—	—	—	—	—	—	—	CK(1)	IOC	—	—	—
RC7	26	1	1	AN19	—	—	OPA3IN0-	—	—	—	—	—	—	—	—	—	—	RX(1,3)	IOC	—	—	—
RD0	19	34	38	AN20	—	—	OPA4IN0+	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—
RD1	20	35	39	AN21	—	—	OPA4OUT OPA3IN1+ OPA3IN1-	C1IN4- C2IN4- C3IN4- C4IN4- C5IN4- C6IN4- C7IN4- C8IN4-	—	PRG3IN1 PRG4IN0	—	—	—	—	—	—	—	—	—	—	—	—
RD2	21	36	40	AN22	—	—	OPA4IN0-	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—
RD3	22	37	41	AN23	—	—	—	C8IN2-	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—
RD4	27	2	2	AN24	—	—	—	C7IN2-	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—
RD5	28	3	3	AN25	—	—	—	C7IN3- C8IN3-	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—
RD6	29	4	4	AN26	—	—	—	C7IN1+	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—
RD7	30	5	5	AN27	—	—	—	C8IN1+	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—
RE0	8	23	25	AN5	DAC6REF1+ DAC8REF1+	—	—	—	—	—	—	—	—	—	—	—	—	—	IOC	—	—	—
RE1	9	24	26	AN6	DAC6REF1- DAC8REF1-	—	—	—	—	—	—	—	—	—	—	—	—	—	IOC	—	—	—
RE2	10	25	27	AN7	—	DAC6OUT1	—	—	—	—	—	—	—	—	—	—	—	—	IOC	—	—	—
RE3	1	16	18	—	—	DAC8OUT1	—	—	—	—	—	—	—	—	—	—	—	—	IOC	—	—	$\overline{MCLR}$ $\overline{ICD}$
VDD	11	7	7	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	VDD
VDD	32	26	28	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	VDD
VSS	12	6	6	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	VSS
VSS	31	27	29	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	VSS

Note 1: Default peripheral input. Input can be moved to any other pin with the PPS input selection register.
2: All pin outputs default to PORT latch data. Any pin can be selected as a digital peripheral output with the PPS output selection registers.
3: These peripheral functions are bidirectional. The output pin selections must be the same as the input pin selections.

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TABLE 4: 40/44-PIN ALLOCATION TABLE (PIC16(L)F1777/9) (CONTINUED)

I/O	40-Pin PDIP	40-Pin UQFN	44-Pin TQFP	ADC	Voltage Reference	DAC	Op Amp	Comparator	Zero Cross	Programmable Ramp Generator	Timers	PWM	CCP	COG	CLC	Modulator	EUSART	MSSP	Interrupt	Pull-ups	High Current	Basic
OUT(2)	—	—	—	—	—	—	—	C1OUT C2OUT C3OUT C4OUT C5OUT C6OUT C7OUT C8OUT	—	—	—	PWM1 PWM2 PWM3 PWM4 PWM5 PWM6 PWM7 PWM8	CCP1 CCP2 CCP3 CCP4	COG1A COG1B COG1C COG1D COG2A COG2B COG2C COG2D COG3A COG3B COG3C COG3D COG4A COG4B COG4C COG4D	CLC1OUT CLC2OUT CLC3OUT CLC4OUT	MD1OUT MD2OUT MD3OUT MD4OUT	DT(3) TX CK	SDO SDA(3) SCK SCL(3)	—	—	—	—

Note 1: Default peripheral input. Input can be moved to any other pin with the PPS input selection register.
2: All pin outputs default to PORT latch data. Any pin can be selected as a digital peripheral output with the PPS output selection registers.
3: These peripheral functions are bidirectional. The output pin selections must be the same as the input pin selections.

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ISBN: 978-1-63277-450-7

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