BLF7G27L-150P; BLF7G27LS-150P

Power LDMOS transistor

Rev. 3 — 1 September 2015

AMPLEON

Product data sheet

1. Product profile

1.1 General description

150 W LDMOS power transistor for base station applications at frequencies from 2500 MHz to 2700 MHz.

Table 1. Typical performance

Typical RF performance at T_{case} = 25 °C in a common source class-AB production test circuit.

Mode of operation	f	I_{Dq}	V_{DS}	$P_{L(AV)}$	G_p	η_D	$ACPR_{885k}$	$ACPR_{5M}$
	(MHz)	(mA)	(V)	(W)	(dB)	(%)	(dBc)	(dBc)
IS-95	2500 to 2700	1200	28	30	16.5	26	–47[1]	-
Single carrier W-CDMA	2500 to 2700	1200	28	45	16.5	31	-	–38[2]

[1] Single carrier IS-95 with pilot, paging, sync and 6 traffic channels (Walsh codes 8 - 13). PAR = 9.7 dB at 0.01 % probability on the CCDF. Channel bandwidth is 1.2288 MHz.

[2] 3GPP; test model 1; 64 DPCH; PAR = 7.2 dB at 0.01 % probability on CCDF. Channel bandwidth is 3.84 MHz.

1.2 Features and benefits

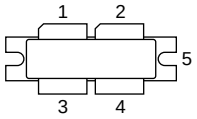
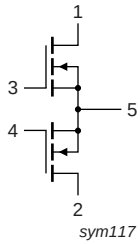
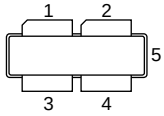
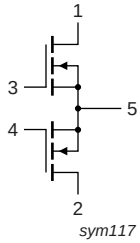
- Excellent ruggedness
- High efficiency
- Low R_{th} providing excellent thermal stability
- Designed for broadband operation (2500 MHz to 2700 MHz)
- Lower output capacitance for improved performance in Doherty applications
- Designed for low memory effects providing excellent pre-distortability
- Internally matched for ease of use
- Integrated ESD protection
- Compliant to Directive 2002/95/EC, regarding Restriction of Hazardous Substances (RoHS)

1.3 Applications

- RF power amplifiers for base stations and multi carrier applications in the 2500 MHz to 2700 MHz frequency range

2. Pinning information

Table 2. Pinning

Pin	Description		Simplified outline	Graphic symbol
BLF7G27L-150P (SOT539A)				
1	drain1			
2	drain2			
3	gate1			
4	gate2			
5	source	[1]		
BLF7G27LS-150P (SOT539B)				
1	drain1			
2	drain2			
3	gate1			
4	gate2			
5	source	[1]		

[1] Connected to flange.

3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BLF7G27L-150P	-	flanged balanced LDMOST ceramic package; 2 mounting holes; 4 leads	SOT539A
BLF7G27LS-150P	-	earless flanged balanced LDMOST ceramic package; 4 leads	SOT539B

4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DS}	drain-source voltage		-	65	V
V_{GS}	gate-source voltage		−0.5	+13	V
I_D	drain current		-	37	A
T_{stg}	storage temperature		−65	+150	°C
T_j	junction temperature		-	225	°C

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5. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Typ	Unit
$R_{th(j-c)}$	thermal resistance from junction to case	T_{case} = 80 °C; P_L = 30 W	0.25	K/W

6. Characteristics

Table 6. Characteristics

T_j = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{(BR)DSS}$	drain-source breakdown voltage	V_{GS} = 0 V; I_D = 1 mA	65	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	V_{DS} = 10 V; I_D = 100 mA	1.3	1.9	2.3	V
I_{DSS}	drain leakage current	V_{GS} = 0 V; V_{DS} = 28 V	-	-	5	μA
I_{DSX}	drain cut-off current	V_{GS} = $V_{GS(th)}$ + 3.75 V; V_{DS} = 10 V	16.75	19	-	A
I_{GSS}	gate leakage current	V_{GS} = 11 V; V_{DS} = 0 V	-	-	500	nA
g_{fs}	forward transconductance	V_{DS} = 10 V; I_D = 3.57 A	-	0.86	-	S
$R_{DS(on)}$	drain-source on-state resistance	V_{GS} = $V_{GS(th)}$ + 3.75 V; I_D = 3.5 A	-	0.14	-	Ω

7. Test information

Remark: All testing performed in a class-AB production test circuit.

Table 7. Functional test information

Mode of operation: 1-carrier N-CDMA, single carrier IS-95 with pilot, paging, sync and 6 traffic channels (Walsh codes 8 - 13). PAR = 9.7 dB at 0.01 % probability on the CCDF, channel bandwidth is 1.2288 MHz; f_1 = 2500 MHz; f_2 = 2700 MHz; RF performance at V_{DS} = 28 V; I_{Dq} = 1200 mA; T_{case} = 25 °C; unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$P_{L(AV)}$	average output power		-	30	-	W
G_p	power gain		14.8	16.5	-	dB
RL_{in}	input return loss		-	−10	-	dB
η_D	drain efficiency		22	26	-	%
$ACPR_{885k}$	adjacent channel power ratio (885 kHz)		−43	−47	-	dBc

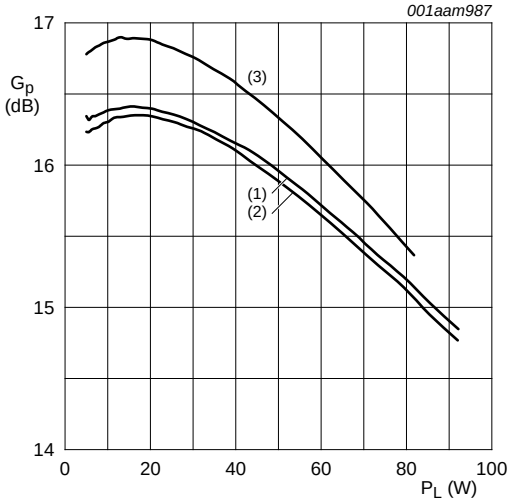
7.1 Ruggedness in class-AB operation

The BLF7G27L-150P and BLF7G27LS-150P are capable of withstanding a load mismatch corresponding to VSWR = 20 : 1 through all phases under the following conditions: V_{DS} = 28 V; I_{Dq} = 1200 mA; P_L = 35 W (IS-95); f = 2500 MHz.

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7.2 Single carrier IS-95

Single carrier IS-95 with pilot, paging, sync and 6 traffic channels (Walsh codes 8 - 13). PAR = 9.7 dB at 0.01 % probability on the CCDF. Channel bandwidth is 1.2288 MHz.



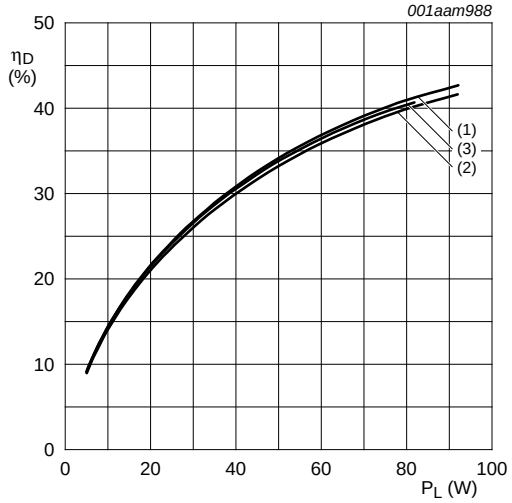
V_{DS} = 28 V; I_{Dq} = 1200 mA.

(1) f = 2500 MHz

(2) f = 2600 MHz

(3) f = 2700 MHz

Fig 1. Single carrier IS-95 power gain as a function of load power; typical values



V_{DS} = 28 V; I_{Dq} = 1200 mA.

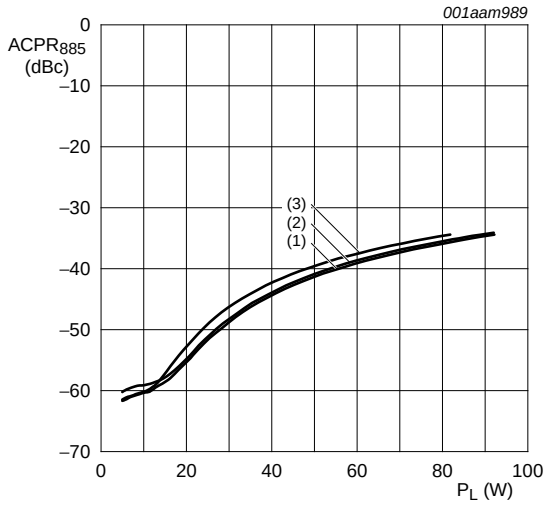
(1) f = 2500 MHz

(2) f = 2600 MHz

(3) f = 2700 MHz

Fig 2. Single carrier IS-95 drain efficiency as a function of load power; typical values

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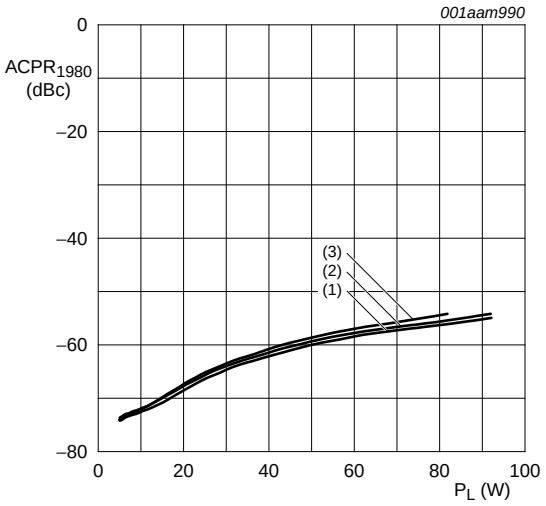
V_{DS} = 28 V; I_{Dq} = 1200 mA.

(1) f = 2500 MHz

(2) f = 2600 MHz

(3) f = 2700 MHz

Fig 3. Single carrier IS-95 ACPR at 885 kHz as a function of load power; typical values



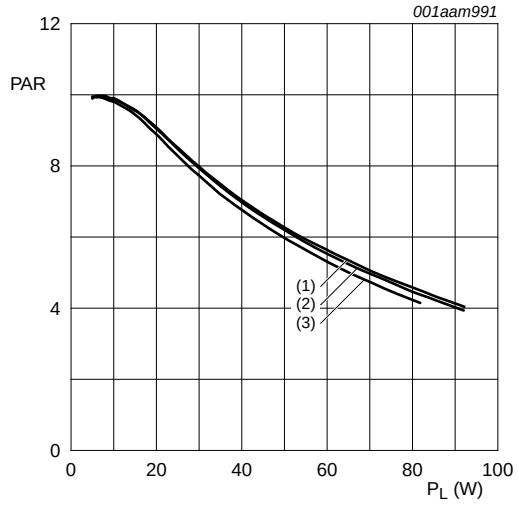
V_{DS} = 28 V; I_{Dq} = 1200 mA.

(1) f = 2500 MHz

(2) f = 2600 MHz

(3) f = 2700 MHz

Fig 4. Single carrier IS-95 ACPR at 1980 kHz as a function of load power; typical values



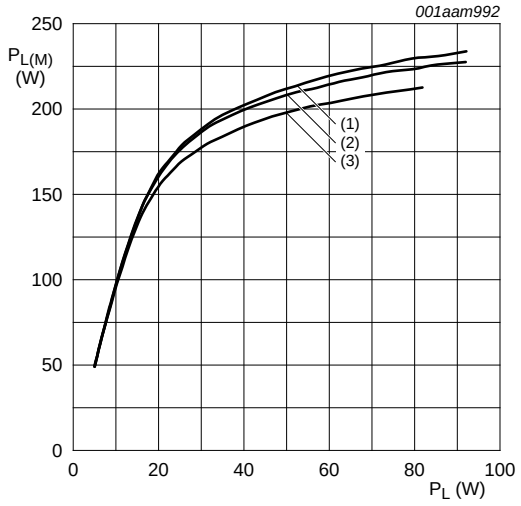
V_{DS} = 28 V; I_{Dq} = 1200 mA.

(1) f = 2500 MHz

(2) f = 2600 MHz

(3) f = 2700 MHz

Fig 5. Single carrier IS-95 peak-to-average power ratio as a function of load power; typical values



V_{DS} = 28 V; I_{Dq} = 1200 mA.

(1) f = 2500 MHz

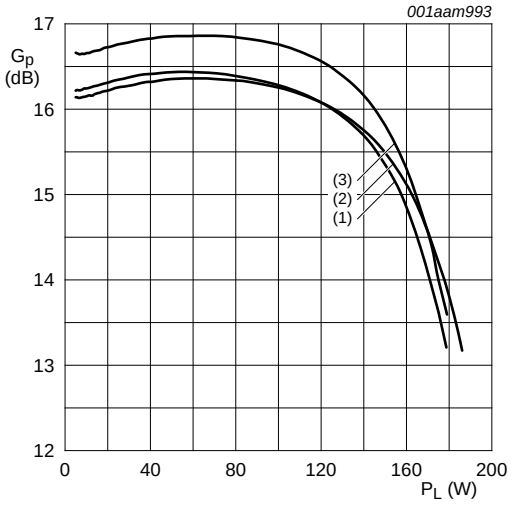
(2) f = 2600 MHz

(3) f = 2700 MHz

Fig 6. Single carrier IS-95 peak power as a function of load power; typical values

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7.3 Pulsed CW



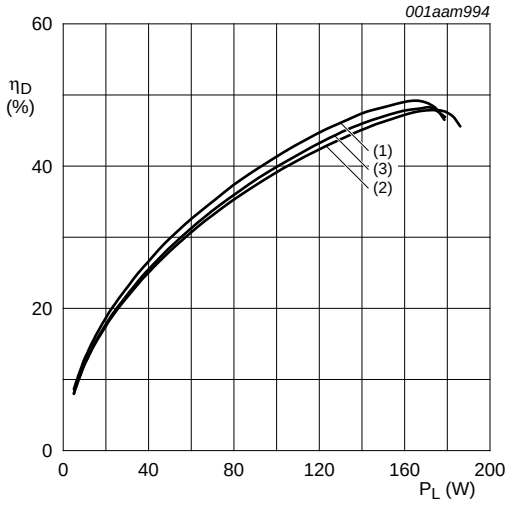
V_{DS} = 28 V; I_{Dq} = 1200 mA.

(1) f = 2500 MHz

(2) f = 2600 MHz

(3) f = 2700 MHz

Fig 7. Pulsed CW power gain as a function of load power; typical values



V_{DS} = 28 V; I_{Dq} = 1200 mA.

(1) f = 2500 MHz

(2) f = 2600 MHz

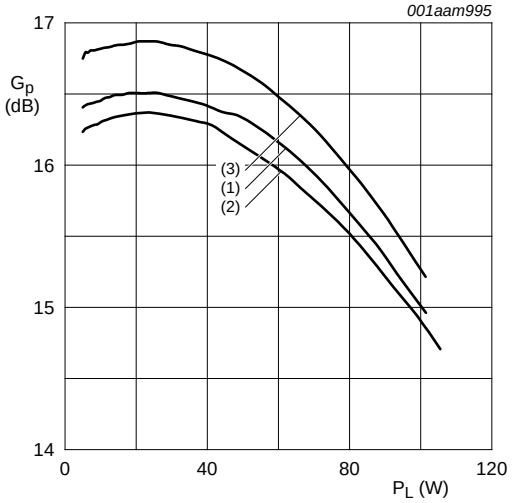
(3) f = 2700 MHz

Fig 8. Pulsed CW drain efficiency as a function of load power; typical values

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7.4 Single carrier W-CDMA

3GPP; test model 1; 64 DPCH; PAR = 7.2 dB at 0.01 % probability on CCDF. Channel bandwidth is 3.84 MHz.



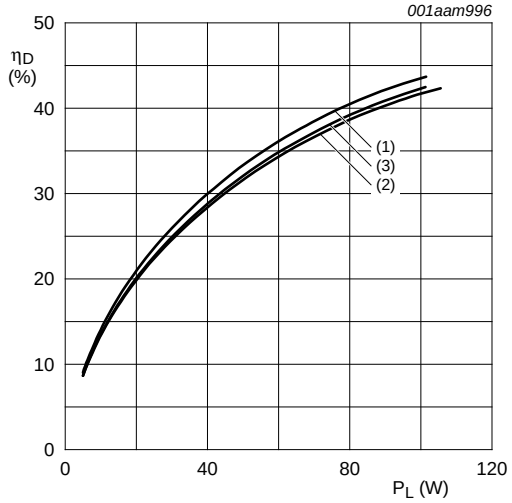
V_{DS} = 28 V; I_{Dq} = 1200 mA.

(1) f = 2500 MHz

(2) f = 2600 MHz

(3) f = 2700 MHz

Fig 9. Single carrier W-CDMA power gain as a function of load power; typical values



V_{DS} = 28 V; I_{Dq} = 1200 mA.

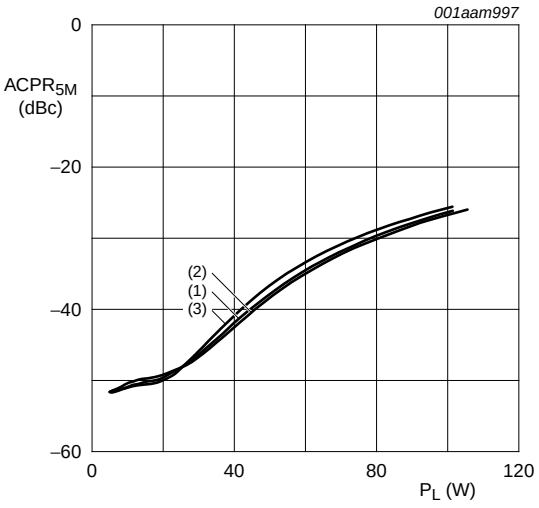
(1) f = 2500 MHz

(2) f = 2600 MHz

(3) f = 2700 MHz

Fig 10. Single carrier W-CDMA drain efficiency as a function of load power; typical values

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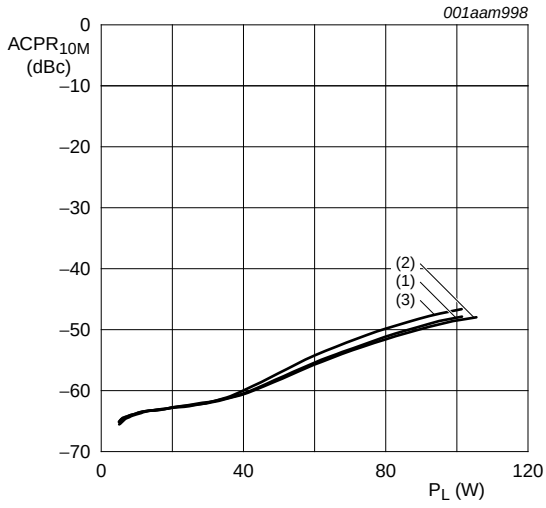
V_{DS} = 28 V; I_{Dq} = 1200 mA.

(1) f = 2500 MHz

(2) f = 2600 MHz

(3) f = 2700 MHz

Fig 11. Single carrier W-CDMA ACPR at 5 MHz as a function of load power; typical values



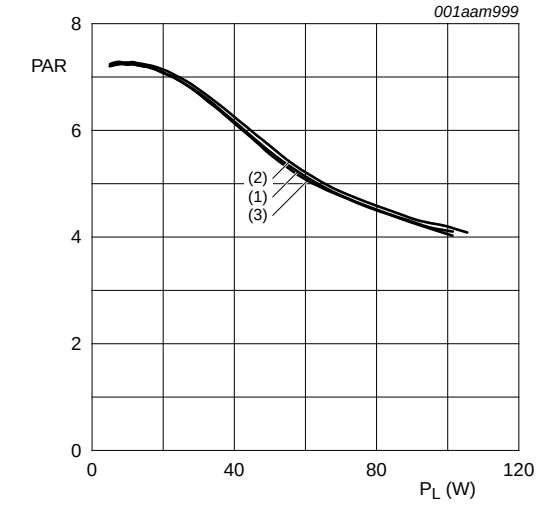
V_{DS} = 28 V; I_{Dq} = 1200 mA.

(1) f = 2500 MHz

(2) f = 2600 MHz

(3) f = 2700 MHz

Fig 12. Single carrier W-CDMA ACPR at 10 MHz as a function of load power; typical values



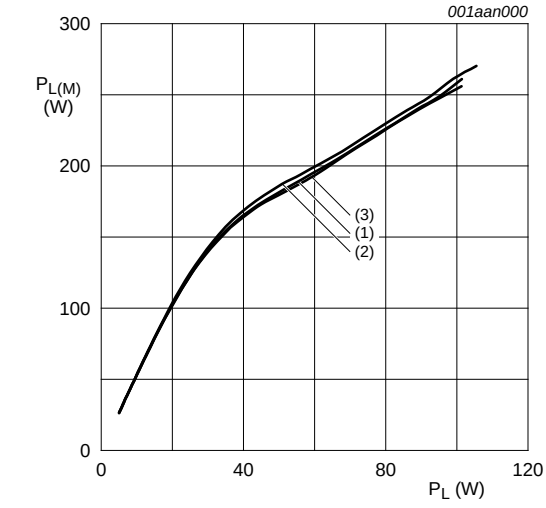
V_{DS} = 28 V; I_{Dq} = 1200 mA.

(1) f = 2500 MHz

(2) f = 2600 MHz

(3) f = 2700 MHz

Fig 13. Single carrier W-CDMA peak-to-average power ratio as a function of load power; typical values



V_{DS} = 28 V; I_{Dq} = 1200 mA.

(1) f = 2500 MHz

(2) f = 2600 MHz

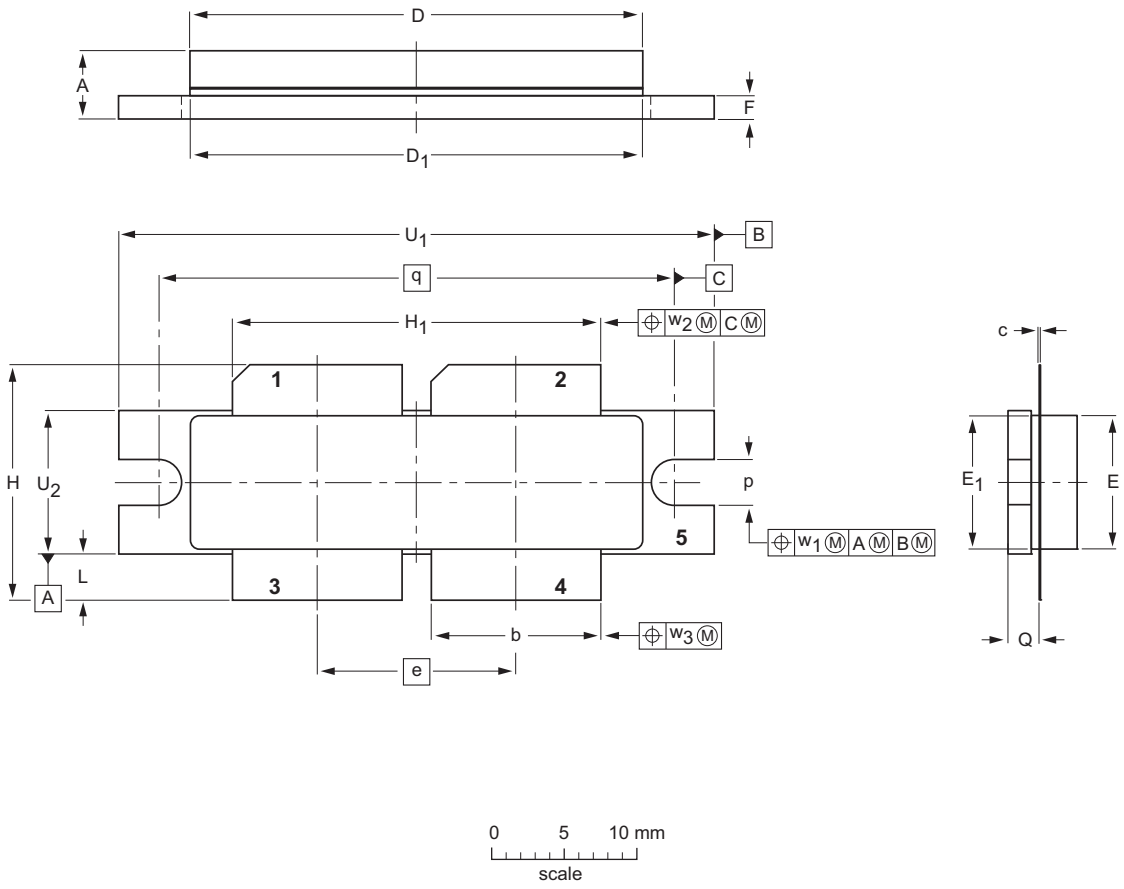
(3) f = 2700 MHz

Fig 14. Single carrier W-CDMA peak output power as a function of load power; typical values

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8. Package outline

Flanged balanced ceramic package; 2 mounting holes; 4 leads **SOT539A**



DIMENSIONS (millimetre dimensions are derived from the original inch dimensions)

UNIT	A	b	c	D	D_1	e	E	E_1	F	H	H_1	L	p	Q	q	U_1	U_2	w_1	w_2	w_3
mm	4.7 4.2	11.81 11.56	0.18 0.10	31.55 30.94	31.52 30.96	13.72	9.50 9.30	9.53 9.27	1.75 1.50	17.12 16.10	25.53 25.27	3.48 2.97	3.30 3.05	2.26 2.01	35.56	41.28 41.02	10.29 10.03	0.25	0.51	0.25
inches	0.185 0.165	0.465 0.455	0.007 0.004	1.242 1.218	1.241 1.219	0.540	0.374 0.366	0.375 0.365	0.069 0.059	0.674 0.634	1.005 0.995	0.137 0.117	0.130 0.120	0.089 0.079	1.400	1.625 1.615	0.405 0.395	0.010	0.020	0.010

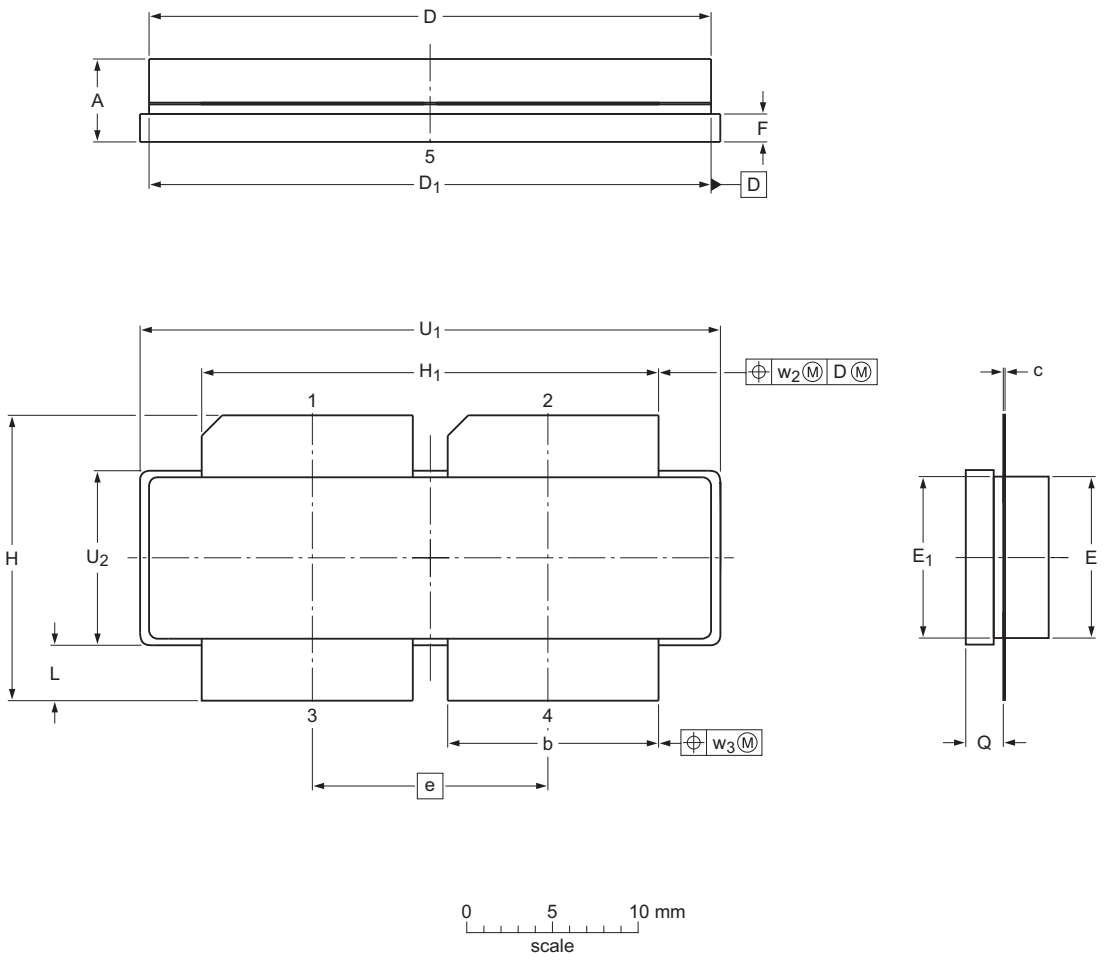
Note
1. millimeter dimensions are derived from the original inch dimensions.
2. recommended screw pitch dimension of 1.52 inch (38.6 mm) based on M3 screw.

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT539A						~~10-02-02~~ 12-05-02

Fig 15. Package outline SOT539A

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Earless flanged balanced ceramic package; 4 leads **SOT539B**



Dimensions

Unit[1]		A	b	c	D	D_1	E	E_1	e	F	H	H_1	L	Q	U_1	U_2	w_2	w_3
mm	max	4.7	11.81	0.18	31.55	31.52	9.5	9.53	13.72	1.75	17.12	25.53	3.48	2.26	32.39	10.29	0.25	0.25
	nom																	
	min	4.2	11.56	0.10	30.94	30.96	9.3	9.27		1.50	16.10	25.27	2.97	2.01	32.13	10.03		
inches	max	0.185	0.465	0.007	1.242	1.241	0.374	0.375	0.54	0.069	0.674	1.005	0.137	0.089	1.275	0.405	0.01	0.01
	nom																	
	min	0.165	0.455	0.004	1.218	1.219	0.366	0.365		0.059	0.634	0.995	0.117	0.079	1.265	0.395		

Note
1. millimeter dimensions are derived from the original inch dimensions.

sot539b_po

Outline version	References				European projection	Issue date
	IEC	JEDEC	JEITA			
SOT539B						~~12-05-02~~ 13-05-24

Fig 16. Package outline SOT539B

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9. Abbreviations

Table 8. Abbreviations

Acronym	Description
CCDF	Complementary Cumulative Distribution Function
CW	Continuous Wave
IS-95	Interim Standard 95
ESD	ElectroStatic Discharge
LDMOS	Laterally Diffused Metal Oxide Semiconductor
LDMOST	Laterally Diffused Metal Oxide Semiconductor Transistor
N-CDMA	Narrowband Code Division Multiple Access
PAR	Peak-to-Average power Ratio
RF	Radio Frequency
VSWR	Voltage Standing Wave Ratio
W-CDMA	Wideband Code Division Multiple Access

10. Revision history

Table 9. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BLF7G27L-150P_7G27LS-150P#3	20150901	Product data sheet	-	BLF7G27L-150P_7G27LS-150P v.2
Modifications:	• The format of this document has been redesigned to comply with the new identity guidelines of Ampleon. • Legal texts have been adapted to the new company name where appropriate.			
BLF7G27L-150P_7G27LS-150P v.2	20130712	Product data sheet	-	BLF7G27L-150P_7G27LS-150P v.1
BLF7G27L-150P_7G27LS-150P v.1	20101112	Product data sheet	-	-

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11. Legal information

11.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.ampleon.com.

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