

J112
SILICON
N-CHANNEL JFET



www.centrasemi.com

The CENTRAL SEMICONDUCTOR J112 is a silicon N-Channel JFET designed for chopper and switching applications.



TO-92 CASE

MARKING: FULL PART NUMBER

MAXIMUM RATINGS: ($T_A=25^{\circ}\text{C}$)

	SYMBOL		UNITS
Drain-Gate Voltage	V_{DG}	35	V
Gate-Source Voltage	V_{GS}	35	V
Continuous Gate Current	I_G	50	mA
Power Dissipation	P_D	350	mW
Operating and Storage Junction Temperature	T_J, T_{stg}	-65 to +150	$^{\circ}\text{C}$

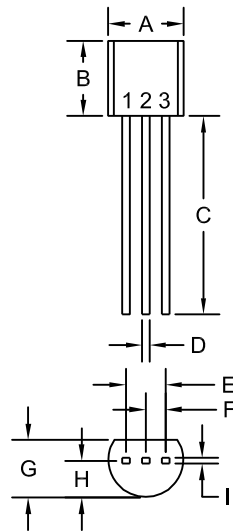
ELECTRICAL CHARACTERISTICS: ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
I_{GSS}	$V_{GS}=15\text{V}$		1.0	nA
I_{DSS}	$V_{DS}=15\text{V}$	5.0		mA
BV_{GSS}	$I_G=1.0\mu\text{A}$	35		V
$V_{GS(OFF)}$	$V_{DS}=5.0\text{V}, I_D=1.0\mu\text{A}$	1.0	5.0	V
$r_{DS(ON)}$	$V_{DS}=0.1\text{V}, V_{GS}=0$		50	Ω
$C_{dg(on)}$	$V_{DS}=V_{GS}=0, f=1.0\text{MHz}$		28	pF
$C_{sg(on)}$	$V_{DS}=V_{GS}=0, f=1.0\text{MHz}$		28	pF
$C_{dg(off)}$	$V_{GS}=10\text{V}, f=1.0\text{MHz}$		5.0	pF
$C_{sg(off)}$	$V_{GS}=10\text{V}, f=1.0\text{MHz}$		5.0	pF

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TO-92 CASE - MECHANICAL OUTLINE



R1

DIMENSIONS				
SYMBOL	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A (DIA)	0.175	0.205	4.45	5.21
B	0.170	0.210	4.32	5.33
C	0.500	-	12.70	-
D	0.016	0.022	0.41	0.56
E	0.100		2.54	
F	0.050		1.27	
G	0.125	0.165	3.18	4.19
H	0.080	0.105	2.03	2.67
I	0.015		0.38	

TO-92 (REV: R1)

LEAD CODE:

- 1) Drain
- 2) Source
- 3) Gate

MARKING:

FULL PART NUMBER

R0 (4-December 2014)

PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options
- Custom bar coding for shipments
- Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities
- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- Application and design sample kits
- Custom product and package development

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