LM2830 High Frequency 1.0A Load - Step-Down DC-DC Regulator

Check for Samples: LM2830

FEATURES

- **LM2830Z-Q1 and LM2830X-Q1 in the SOT-23 Package are Automotive Grade Products that are AEC-Q100 Grade 1 Qualified (-40°C to +125°C Operating Junction Temperature)**
- **Space Saving SOT-23 Package**
- **Input Voltage Range of 3.0V to 5.5V**
- **Output Voltage Range of 0.6V to 4.5V**
- **1.0A Output Current**
- **High Switching Frequencies**
  - **1.6MHz (LM2830X)**
  - **3.0MHz (LM2830Z)**
- **130mΩ PMOS Switch**
- **0.6V, 2% Internal Voltage Reference**
- **Internal Soft-Start**
- **Current Mode, PWM Operation**
- **Thermal Shutdown**
- **Over Voltage Protection**

APPLICATIONS

- **Local 5V to Vcore Step-Down Converters**
- **Core Power in HDDs**
- **Set-Top Boxes**
- **USB Powered Devices**
- **DSL Modems**
- **Automotive**

DESCRIPTION

The LM2830 regulator is a monolithic, high frequency, PWM step-down DC/DC converter in a 5 pin SOT-23 and a 6 Pin WSON package. It provides all the active functions to provide local DC/DC conversion with fast transient response and accurate regulation in the smallest possible PCB area. With a minimum of external components, the LM2830 is easy to use. The ability to drive 1.0A loads with an internal 130 mΩ PMOS switch using state-of-the-art 0.5 µm BiCMOS technology results in the best power density available. The world-class control circuitry allows on-times as low as 30ns, thus supporting exceptionally high frequency conversion over the entire 3V to 5.5V input operating range down to the minimum output voltage of 0.6V. Switching frequency is internally set to 1.6 MHz, or 3.0 MHz, allowing the use of extremely small surface mount inductors and chip capacitors. Even though the operating frequency is high, efficiencies up to 93% are easy to achieve. External shutdown is included, featuring an ultra-low stand-by current of 30 nA. The LM2830 utilizes current-mode control and internal compensation to provide high-performance regulation over a wide range of operating conditions. Additional features include internal soft-start circuitry to reduce inrush current, pulse-by-pulse current limit, thermal shutdown, and output over-voltage protection.

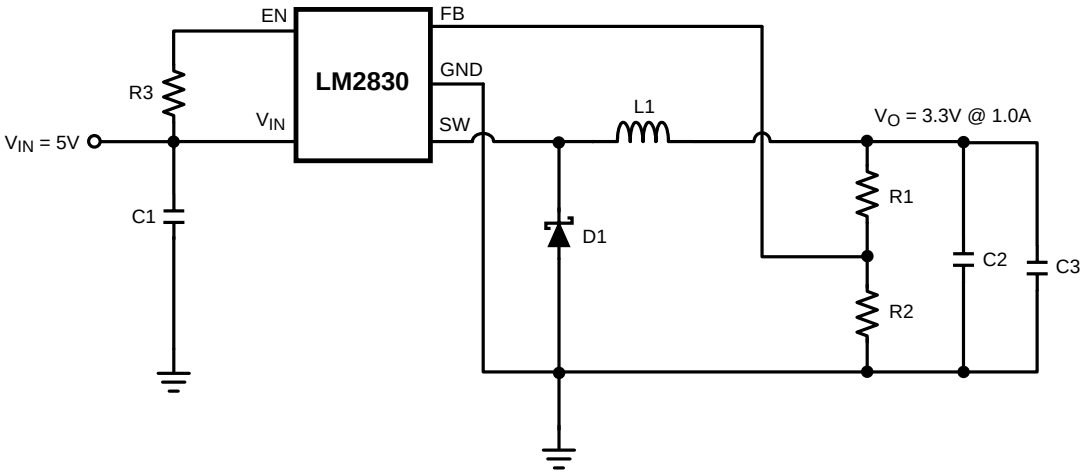
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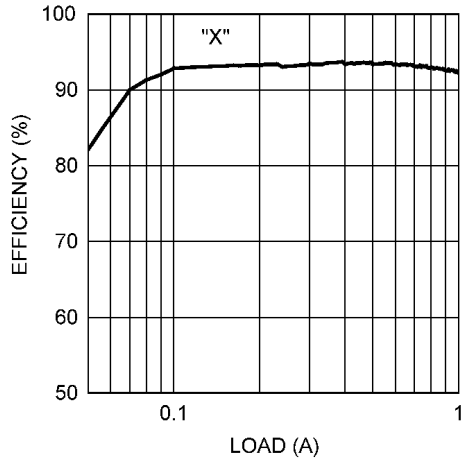
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Typical Application Circuit





Connection Diagrams

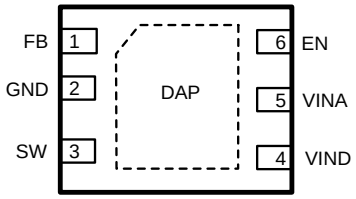


Figure 1. 6-Pin WSON
See Package Number NGG

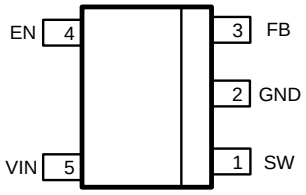


Figure 2. 5-Pin SOT-23
See Package Number DBV

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PIN DESCRIPTIONS (5-PIN SOT-23)

Pin	Name	Function
1	SW	Output switch. Connect to the inductor and catch diode.
2	GND	Signal and power ground pin. Place the bottom resistor of the feedback network as close as possible to this pin.
3	FB	Feedback pin. Connect to external resistor divider to set output voltage.
4	EN	Enable control input. Logic high enables operation. Do not allow this pin to float or be greater than VIN + 0.3V.
5	VIN	Input supply voltage.

PIN DESCRIPTIONS (6-PIN WSON)

Pin	Name	Function
1	FB	Feedback pin. Connect to external resistor divider to set output voltage.
2	GND	Signal and power ground pin. Place the bottom resistor of the feedback network as close as possible to this pin.
3	SW	Output switch. Connect to the inductor and catch diode.
4	VIND	Power Input supply.
5	VINA	Control circuitry supply voltage. Connect VINA to VIND on PC board.
6	EN	Enable control input. Logic high enables operation. Do not allow this pin to float or be greater than VINA + 0.3V.
DAP	Die Attach Pad	Connect to system ground for low thermal impedance, but it cannot be used as a primary GND connection.

These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

Absolute Maximum Ratings(1)(2)

VIN	-0.5V to 7V
FB Voltage	-0.5V to 3V
EN Voltage	-0.5V to 7V
SW Voltage	-0.5V to 7V
ESD Susceptibility	2kV
Junction Temperature(3)	150°C
Storage Temperature	−65°C to +150°C

(1) Absolute maximum ratings indicate limits beyond which damage to the device may occur. Operating Range indicates conditions for which the device is intended to be functional, but does not ensure specific performance limits. For ensured specifications and test conditions, see the Electrical Characteristics.
(2) If Military/Aerospace specified devices are required, please contact the Texas Instruments Sales Office/ Distributors for availability and specifications.
(3) Thermal shutdown will occur if the junction temperature exceeds the maximum junction temperature of the device.

Operating Ratings

VIN	3V to 5.5V
Junction Temperature	−40°C to +125°C

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Electrical Characteristics

VIN = 5V unless otherwise indicated under the **Conditions** column. Limits in standard type are for T_J = 25°C only; limits in **boldface type** apply over the junction temperature (T_J) range of -40°C to +125°C. Minimum and Maximum limits are ensured through test, design, or statistical correlation. Typical values represent the most likely parametric norm at T_J = 25°C, and are provided for reference purposes only.

Symbol	Parameter	Conditions	Min	Typ	Max	Units
V_{FB}	Feedback Voltage	WSON and SOT-23 Package	**0.588**	0.600	**0.612**	V
$\Delta V_{FB}/V_{IN}$	Feedback Voltage Line Regulation	V_{IN} = 3V to 5V		0.02		%/V
I_B	Feedback Input Bias Current			0.1	**100**	nA
UVLO	Undervoltage Lockout	V_{IN} Rising		2.73	**2.90**	V
		V_{IN} Falling	**1.85**	2.3		
	UVLO Hysteresis			0.43		V
F_{SW}	Switching Frequency	LM2830-X	**1.2**	1.6	**1.95**	MHz
		LM2830-Z	**2.25**	3.0	**3.75**	
D_{MAX}	Maximum Duty Cycle	LM2830-X	**86**	94		%
		LM2830-Z	**82**	90		
D_{MIN}	Minimum Duty Cycle	LM2830-X		5		%
		LM2830-Z		7		
$R_{DS(ON)}$	Switch On Resistance	WSON Package		150		mΩ
		SOT-23 Package		130	**195**	
I_{CL}	Switch Current Limit	V_{IN} = 3.3V	**1.2**	1.75		A
V_{EN_TH}	Shutdown Threshold Voltage				**0.4**	V
	Enable Threshold Voltage		**1.8**			
I_{SW}	Switch Leakage			100		nA
I_{EN}	Enable Pin Current	Sink/Source		100		nA
I_Q	Quiescent Current (switching)	LM2830X V_{FB} = 0.55		3.3	**5**	mA
		LM2830Z V_{FB} = 0.55		4.3	**6.5**	mA
	Quiescent Current (shutdown)	All Options V_{EN} = 0V		30		nA
θ_{JA}	Junction to Ambient 0 LFPM Air Flow(1)	WSON Package		80		°C/W
		SOT-23 Package		118		
θ_{JC}	Junction to Case(1)	WSON Package		18		°C/W
		SOT-23 Package		80		
T_{SD}	Thermal Shutdown Temperature			165		°C

(1) Applies for packages soldered directly onto a 3” x 3” PC board with 2oz. copper on 4 layers in still air.

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Typical Performance Characteristics

All curves taken at VIN = 5.0V with configuration in typical application circuit shown in Applications Information section of this datasheet. T_J = 25°C, unless otherwise specified.

η vs Load "X" Vin = 5V, Vo = 1.8V & 3.3V

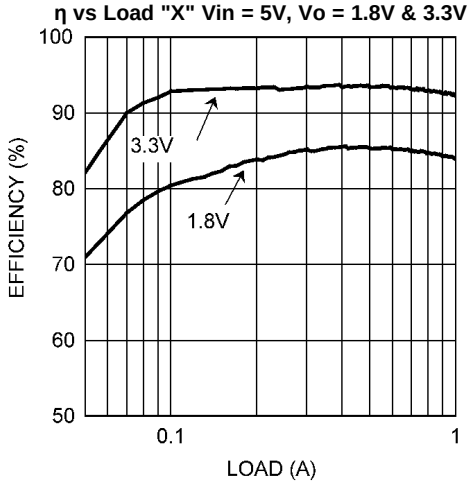


Figure 3.

η vs Load "Z" Vin = 5V, Vo = 3.3V & 1.8V

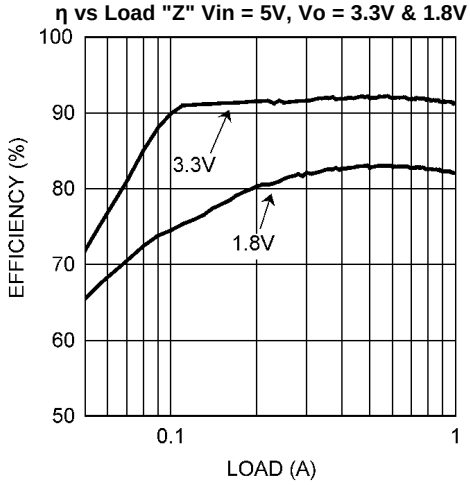


Figure 4.

η vs Load "X and Z" Vin = 3.3V, Vo = 1.8V

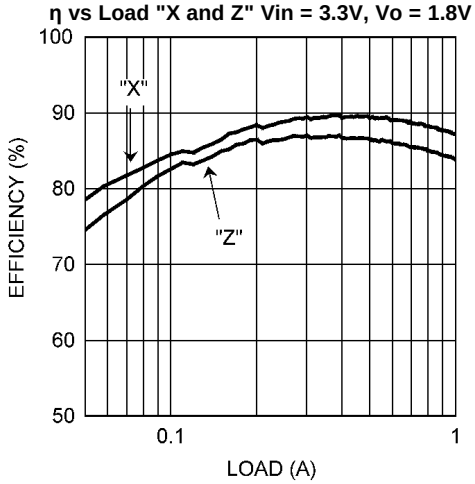


Figure 5.

Load Regulation Vin = 3.3V, Vo = 1.8V (All Options)

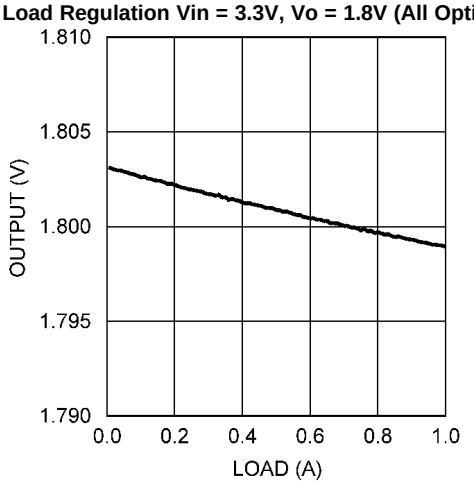


Figure 6.

Load Regulation Vin = 5V, Vo = 1.8V (All Options)

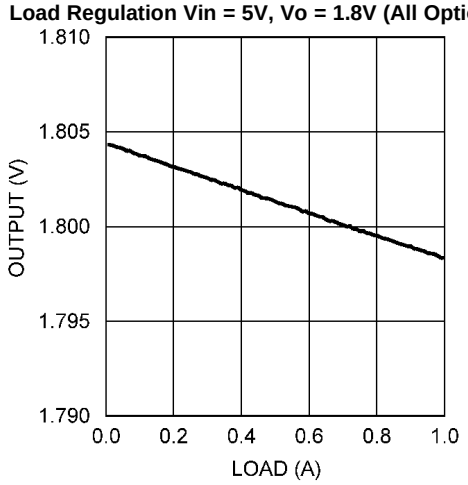


Figure 7.

Load Regulation Vin = 5V, Vo = 3.3V (All Options)

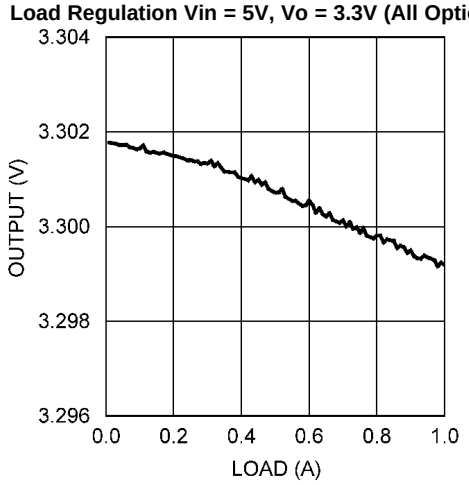


Figure 8.

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Typical Performance Characteristics (continued)

All curves taken at VIN = 5.0V with configuration in typical application circuit shown in Applications Information section of this datasheet. T_J = 25°C, unless otherwise specified.

Oscillator Frequency vs Temperature - "X"

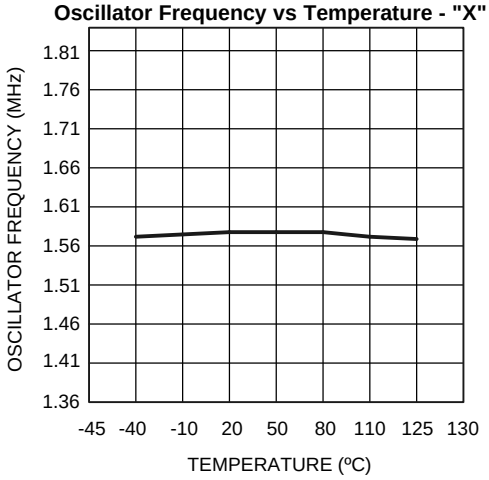


Figure 9.

Oscillator Frequency vs Temperature - "Z"

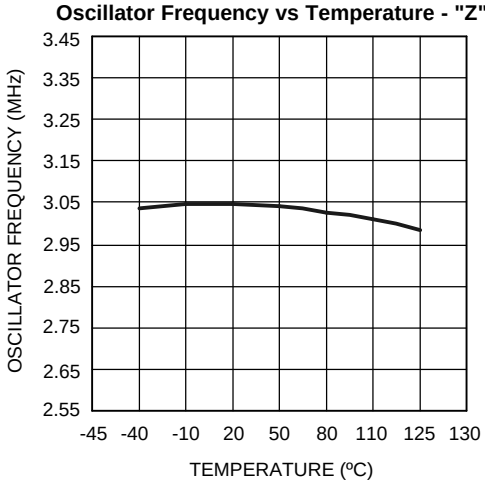


Figure 10.

Current Limit vs Temperature Vin = 3.3V

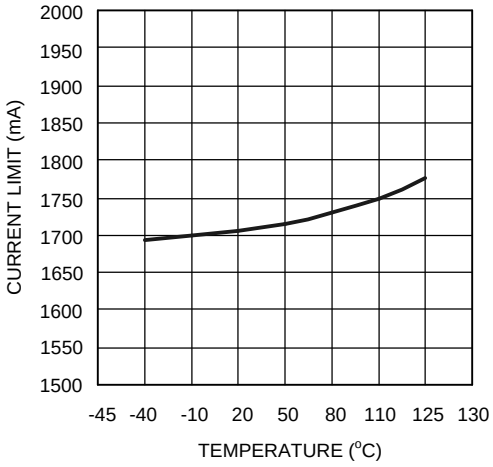


Figure 11.

RDSON vs Temperature (WSON Package)

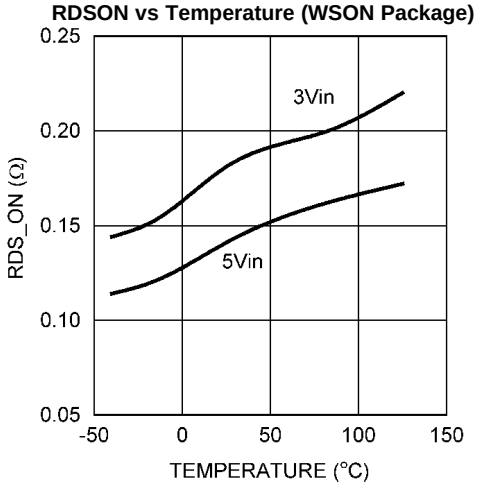


Figure 12.

RDSON vs Temperature (SOT-23 Package)

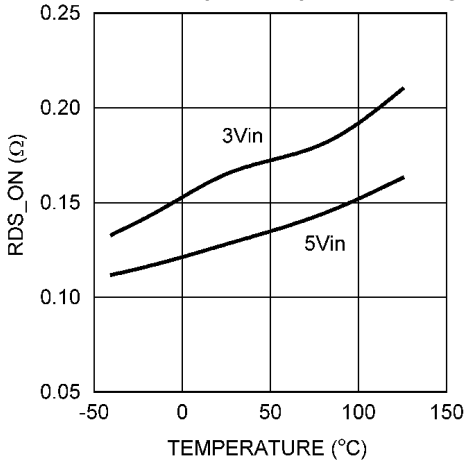


Figure 13.

LM2830X I_Q (Quiescent Current)

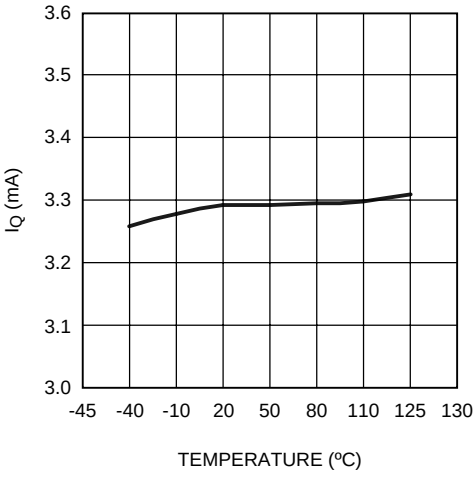


Figure 14.

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Typical Performance Characteristics (continued)

All curves taken at VIN = 5.0V with configuration in typical application circuit shown in Applications Information section of this datasheet. T_J = 25°C, unless otherwise specified.

LM2830Z I_Q (Quiescent Current)

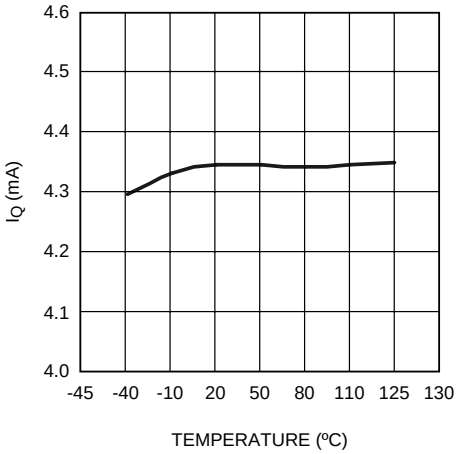


Figure 15.

Line Regulation Vo = 1.8V, Io = 500mA

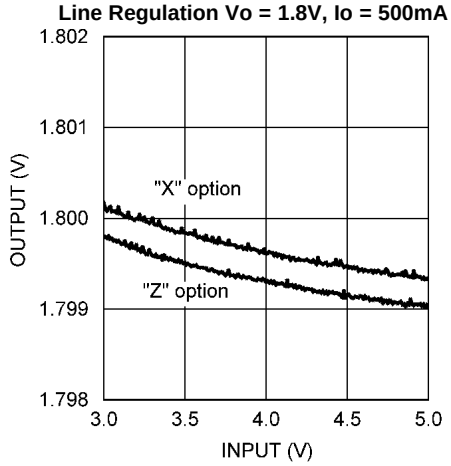


Figure 16.

V_{FB} vs Temperature

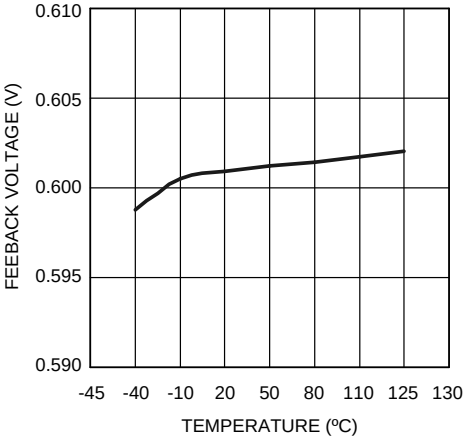


Figure 17.

Gain vs Frequency (Vin = 5V, Vo = 1.2V @ 1A)

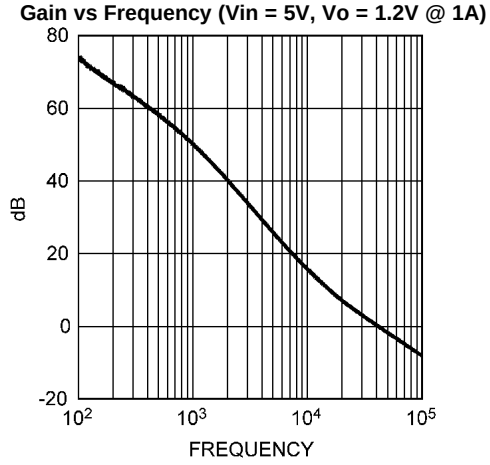


Figure 18.

Phase Plot vs Frequency (Vin = 5V, Vo = 1.2V @ 1A)

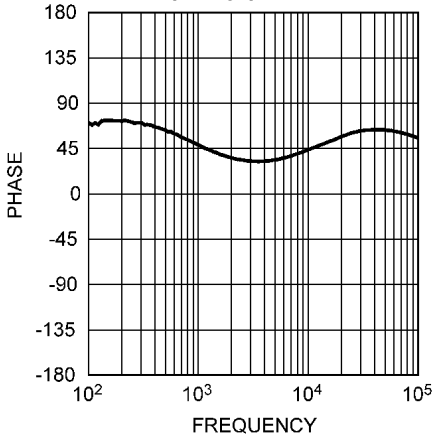


Figure 19.

Simplified Block Diagram

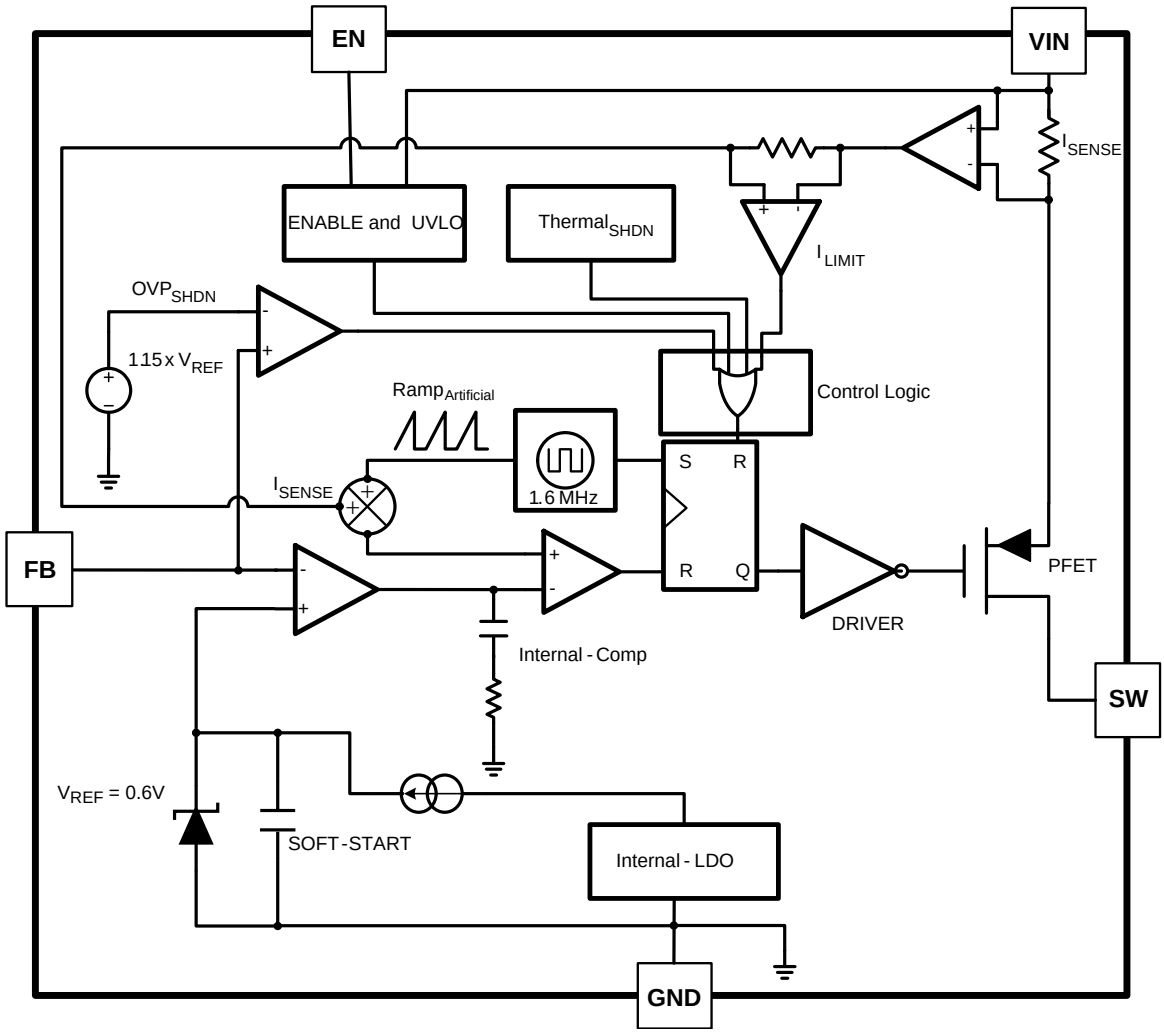


Figure 20.

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APPLICATIONS INFORMATION

THEORY OF OPERATION

The LM2830 is a constant frequency PWM buck regulator IC that delivers a 1.0A load current. The regulator has a preset switching frequency of 1.6MHz or 3.0MHz. This high frequency allows the LM2830 to operate with small surface mount capacitors and inductors, resulting in a DC/DC converter that requires a minimum amount of board space. The LM2830 is internally compensated, so it is simple to use and requires few external components. The LM2830 uses current-mode control to regulate the output voltage. The following operating description of the LM2830 will refer to the Simplified Block Diagram (Figure 20) and to the waveforms in Figure 21. The LM2830 supplies a regulated output voltage by switching the internal PMOS control switch at constant frequency and variable duty cycle. A switching cycle begins at the falling edge of the reset pulse generated by the internal oscillator. When this pulse goes low, the output control logic turns on the internal PMOS control switch. During this on-time, the SW pin voltage (V_{SW}) swings up to approximately V_{IN}, and the inductor current (I_L) increases with a linear slope. I_L is measured by the current sense amplifier, which generates an output proportional to the switch current. The sense signal is summed with the regulator's corrective ramp and compared to the error amplifier's output, which is proportional to the difference between the feedback voltage and V_{REF}. When the PWM comparator output goes high, the output switch turns off until the next switching cycle begins. During the switch off-time, inductor current discharges through the Schottky catch diode, which forces the SW pin to swing below ground by the forward voltage (V_D) of the Schottky catch diode. The regulator loop adjusts the duty cycle (D) to maintain a constant output voltage.

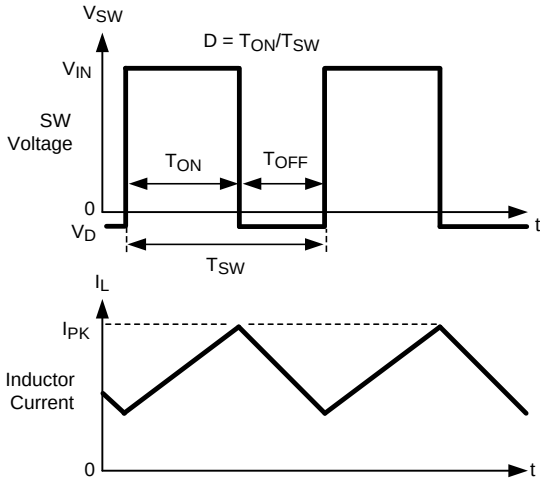


Figure 21. Typical Waveforms

SOFT-START

This function forces V_{OUT} to increase at a controlled rate during start up. During soft-start, the error amplifier's reference voltage ramps from 0V to its nominal value of 0.6V in approximately 600 µs. This forces the regulator output to ramp up in a controlled fashion, which helps reduce inrush current.

OUTPUT OVERVOLTAGE PROTECTION

The over-voltage comparator compares the FB pin voltage to a voltage that is 15% higher than the internal reference V_{REF}. Once the FB pin voltage goes 15% above the internal reference, the internal PMOS control switch is turned off, which allows the output voltage to decrease toward regulation.

UNDERVOLTAGE LOCKOUT

Under-voltage lockout (UVLO) prevents the LM2830 from operating until the input voltage exceeds 2.73V (typ). The UVLO threshold has approximately 430 mV of hysteresis, so the part will operate until V_{IN} drops below 2.3V (typ). Hysteresis prevents the part from turning off during power up if V_{IN} is non-monotonic.

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CURRENT LIMIT

The LM2830 uses cycle-by-cycle current limiting to protect the output switch. During each switching cycle, a current limit comparator detects if the output switch current exceeds 1.75A (typ), and turns off the switch until the next switching cycle begins.

THERMAL SHUTDOWN

Thermal shutdown limits total power dissipation by turning off the output switch when the IC junction temperature exceeds 165°C. After thermal shutdown occurs, the output switch doesn't turn on until the junction temperature drops to approximately 150°C.

Design Guide

INDUCTOR SELECTION

The Duty Cycle (D) can be approximated quickly using the ratio of output voltage (V_O) to input voltage (V_{IN}):

$$D = \frac{V_{OUT}}{V_{IN}} \quad (1)$$

The catch diode (D1) forward voltage drop and the voltage drop across the internal PMOS must be included to calculate a more accurate duty cycle. Calculate D by using the following formula:

$$D = \frac{V_{OUT} + V_D}{V_{IN} + V_D - V_{SW}} \quad (2)$$

V_{SW} can be approximated by:

$$V_{SW} = I_{OUT} \times R_{DSON} \quad (3)$$

The diode forward drop (V_D) can range from 0.3V to 0.7V depending on the quality of the diode. The lower the V_D, the higher the operating efficiency of the converter. The inductor value determines the output ripple current. Lower inductor values decrease the size of the inductor, but increase the output ripple current. An increase in the inductor value will decrease the output ripple current.

One must ensure that the minimum current limit (1.2A) is not exceeded, so the peak current in the inductor must be calculated. The peak current (I_{LPK}) in the inductor is calculated by:

$$I_{LPK} = I_{OUT} + \Delta i_L \quad (4)$$

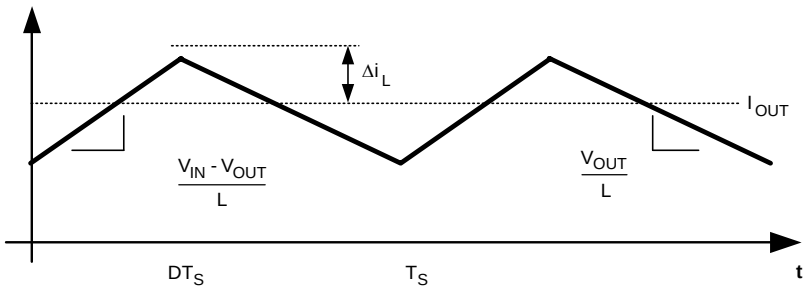


Figure 22. Inductor Current

$$\frac{V_{IN} - V_{OUT}}{L} = \frac{2\Delta i_L}{DT_S} \quad (5)$$

In general,

$$\Delta i_L = 0.1 \times (I_{OUT}) \rightarrow 0.2 \times (I_{OUT}) \quad (6)$$

If Δi_L = 20% of 1A, the peak current in the inductor will be 1.2A. The minimum ensured current limit over all operating conditions is 1.2A. One can either reduce Δi_L, or make the engineering judgment that zero margin will be safe enough. The typical current limit is 1.75A.

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The LM2830 operates at frequencies allowing the use of ceramic output capacitors without compromising transient response. Ceramic capacitors allow higher inductor ripple without significantly increasing output ripple. See the OUTPUT CAPACITOR for more details on calculating output voltage ripple. Now that the ripple current is determined, the inductance is calculated by:

$$L = \left(\frac{DT_S}{2\Delta i_L}\right) \times (V_{IN} - V_{OUT})$$

where

- $T_s = 1/f_s$ (7)

When selecting an inductor, make sure that it is capable of supporting the peak output current without saturating. Inductor saturation will result in a sudden reduction in inductance and prevent the regulator from operating correctly. Because of the speed of the internal current limit, the peak current of the inductor need only be specified for the required maximum output current. For example, if the designed maximum output current is 1.0A and the peak current is 1.25A, then the inductor should be specified with a saturation current limit of > 1.25A. There is no need to specify the saturation or peak current of the inductor at the 1.75A typical switch current limit. The difference in inductor size is a factor of 5. Because of the operating frequency of the LM2830, ferrite based inductors are preferred to minimize core losses. This presents little restriction since the variety of ferrite-based inductors is huge. Lastly, inductors with lower series resistance (R_{DCR}) will provide better operating efficiency. For recommended inductors see Example Circuits.

INPUT CAPACITOR

An input capacitor is necessary to ensure that V_{IN} does not drop excessively during switching transients. The primary specifications of the input capacitor are capacitance, voltage, RMS current rating, and ESL (Equivalent Series Inductance). The recommended input capacitance is 22 µF.The input voltage rating is specifically stated by the capacitor manufacturer. Make sure to check any recommended deratings and also verify if there is any significant change in capacitance at the operating input voltage and the operating temperature. The input capacitor maximum RMS input current rating (I_{RMS-IN}) must be greater than:

$$I_{RMS_IN} \sqrt{D\left[I_{OUT}^2\,(1\text{-}D) + \frac{\Delta i^2}{3}\right]} \tag{8}$$

Neglecting inductor ripple simplifies the above equation to:

$$I_{RMS_IN} = I_{OUT} \times \sqrt{D(1 - D)} \tag{9}$$

It can be shown from the above equation that maximum RMS capacitor current occurs when D = 0.5. Always calculate the RMS at the point where the duty cycle D is closest to 0.5. The ESL of an input capacitor is usually determined by the effective cross sectional area of the current path. A large leaded capacitor will have high ESL and a 0805 ceramic chip capacitor will have very low ESL. At the operating frequencies of the LM2830, leaded capacitors may have an ESL so large that the resulting impedance (2πfL) will be higher than that required to provide stable operation. As a result, surface mount capacitors are strongly recommended.

Sanyo POSCAP, Tantalum or Niobium, Panasonic SP, and multilayer ceramic capacitors (MLCC) are all good choices for both input and output capacitors and have very low ESL. For MLCCs it is recommended to use X7R or X5R type capacitors due to their tolerance and temperature characteristics. Consult capacitor manufacturer datasheets to see how rated capacitance varies over operating conditions.

OUTPUT CAPACITOR

The output capacitor is selected based upon the desired output ripple and transient response. The initial current of a load transient is provided mainly by the output capacitor. The output ripple of the converter is:

$$\Delta V_{OUT} = \Delta I_L \left(R_{ESR} + \frac{1}{8 \times F_{SW} \times C_{OUT}}\right) \tag{10}$$

When using MLCCs, the ESR is typically so low that the capacitive ripple may dominate. When this occurs, the output ripple will be approximately sinusoidal and 90° phase shifted from the switching action. Given the availability and quality of MLCCs and the expected output voltage of designs using the LM2830, there is really no need to review any other capacitor technologies. Another benefit of ceramic capacitors is their ability to bypass high frequency noise. A certain amount of switching edge noise will couple through parasitic capacitances in the

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inductor to the output. A ceramic capacitor will bypass this noise while a tantalum will not. Since the output capacitor is one of the two external components that control the stability of the regulator control loop, most applications will require a minimum of 22 µF of output capacitance. Capacitance often, but not always, can be increased significantly with little detriment to the regulator stability. Like the input capacitor, recommended multilayer ceramic capacitors are X7R or X5R types.

CATCH DIODE

The catch diode (D1) conducts during the switch off-time. A Schottky diode is recommended for its fast switching times and low forward voltage drop. The catch diode should be chosen so that its current rating is greater than:

$$I_{D1} = I_{OUT} \times (1\text{-}D) \tag{11}$$

The reverse breakdown rating of the diode must be at least the maximum input voltage plus appropriate margin. To improve efficiency, choose a Schottky diode with a low forward voltage drop.

OUTPUT VOLTAGE

The output voltage is set using the following equation where R2 is connected between the FB pin and GND, and R1 is connected between V_O and the FB pin. A good value for R2 is 10kΩ. When designing a unity gain converter (Vo = 0.6V), R1 should be between 0Ω and 100Ω, and R2 should be equal or greater than 10kΩ.

$$R1 = \left(\frac{V_{OUT}}{V_{REF}} - 1 \right) \times R2 \tag{12}$$

$$V_{REF} = 0.60V \tag{13}$$

PCB LAYOUT CONSIDERATIONS

When planning layout there are a few things to consider when trying to achieve a clean, regulated output. The most important consideration is the close coupling of the GND connections of the input capacitor and the catch diode D1. These ground ends should be close to one another and be connected to the GND plane with at least two through-holes. Place these components as close to the IC as possible. Next in importance is the location of the GND connection of the output capacitor, which should be near the GND connections of CIN and D1. There should be a continuous ground plane on the bottom layer of a two-layer board except under the switching node island. The FB pin is a high impedance node and care should be taken to make the FB trace short to avoid noise pickup and inaccurate regulation. The feedback resistors should be placed as close as possible to the IC, with the GND of R1 placed as close as possible to the GND of the IC. The V_{OUT} trace to R2 should be routed away from the inductor and any other traces that are switching. High AC currents flow through the V_{IN}, SW and V_{OUT} traces, so they should be as short and wide as possible. However, making the traces wide increases radiated noise, so the designer must make this trade-off. Radiated noise can be decreased by choosing a shielded inductor. The remaining components should also be placed as close as possible to the IC. Please see Application Note AN-1229 SNVA054 for further considerations and the LM2830 demo board as an example of a four-layer layout.

Calculating Efficiency, and Junction Temperature

The complete LM2830 DC/DC converter efficiency can be calculated in the following manner.

$$\eta = \frac{P_{OUT}}{P_{IN}} \tag{14}$$

Or

$$\eta = \frac{P_{OUT}}{P_{OUT} + P_{LOSS}} \tag{15}$$

Calculations for determining the most significant power losses are shown below. Other losses totaling less than 2% are not discussed.

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Power loss (P_{LOSS}) is the sum of two basic types of losses in the converter: switching and conduction. Conduction losses usually dominate at higher output loads, whereas switching losses remain relatively fixed and dominate at lower output loads. The first step in determining the losses is to calculate the duty cycle (D):

$$D = \frac{V_{OUT} + V_D}{V_{IN} + V_D - V_{SW}} \quad (16)$$

V_{SW} is the voltage drop across the internal PFET when it is on, and is equal to:

$$V_{SW} = I_{OUT} \times R_{DSON} \quad (17)$$

V_D is the forward voltage drop across the Schottky catch diode. It can be obtained from the diode manufactures Electrical Characteristics section. If the voltage drop across the inductor (V_{DCR}) is accounted for, the equation becomes:

$$D = \frac{V_{OUT} + V_D + V_{DCR}}{V_{IN} + V_D + V_{DCR} - V_{SW}} \quad (18)$$

The conduction losses in the free-wheeling Schottky diode are calculated as follows:

$$P_{DIODE} = V_D \times I_{OUT} \times (1-D) \quad (19)$$

Often this is the single most significant power loss in the circuit. Care should be taken to choose a Schottky diode that has a low forward voltage drop.

Another significant external power loss is the conduction loss in the output inductor. The equation can be simplified to:

$$P_{IND} = I_{OUT}^2 \times R_{DCR} \quad (20)$$

The LM2830 conduction loss is mainly associated with the internal PFET:

$$P_{COND} = (I_{OUT}^2 \times D)\left(1 + \frac{1}{3} \times \left(\frac{\Delta i_L}{I_{OUT}}\right)^2\right) R_{DSON} \quad (21)$$

If the inductor ripple current is fairly small, the conduction losses can be simplified to:

$$P_{COND} = I_{OUT}^2 \times R_{DSON} \times D \quad (22)$$

Switching losses are also associated with the internal PFET. They occur during the switch on and off transition periods, where voltages and currents overlap resulting in power loss. The simplest means to determine this loss is to empirically measuring the rise and fall times (10% to 90%) of the switch at the switch node.

Switching Power Loss is calculated as follows:

$$P_{SWR} = 1/2(V_{IN} \times I_{OUT} \times F_{SW} \times T_{RISE}) \quad (23)$$

$$P_{SWF} = 1/2(V_{IN} \times I_{OUT} \times F_{SW} \times T_{FALL}) \quad (24)$$

$$P_{SW} = P_{SWR} + P_{SWF} \quad (25)$$

Another loss is the power required for operation of the internal circuitry:

$$P_Q = I_Q \times V_{IN} \quad (26)$$

I_Q is the quiescent operating current, and is typically around 3.3mA for the 1.6MHz frequency option.

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Typical Application power losses are:

Table 1. Power Loss Tabulation

V_{IN}	5.0V		
V_{OUT}	3.3V	P_{OUT}	3.3W
I_{OUT}	1.0A		
V_D	0.45V	P_{DIODE}	150mW
F_{SW}	1.6MHz		
I_Q	3.3mA	P_Q	17mW
T_{RISE}	4nS	P_{SWR}	6mW
T_{FALL}	4nS	P_{SWF}	6mW
$R_{DS(ON)}$	150mΩ	P_{COND}	100mW
IND_{DCR}	70mΩ	P_{IND}	70mW
D	0.667	P_{LOSS}	345mW
η	88%	$P_{INTERNAL}$	125mW

$$\Sigma P_{COND} + P_{SW} + P_{DIODE} + P_{IND} + P_Q = P_{LOSS} \quad (27)$$

$$\Sigma P_{COND} + P_{SWF} + P_{SWR} + P_Q = P_{INTERNAL} \quad (28)$$

$$P_{INTERNAL} = 125mW \quad (29)$$

Thermal Definitions

T_J Chip junction temperature

T_A Ambient temperature

$R_{\theta JC}$ Thermal resistance from chip junction to device case

$R_{\theta JA}$ Thermal resistance from chip junction to ambient air

Heat in the LM2830 due to internal power dissipation is removed through conduction and/or convection.

Conduction: Heat transfer occurs through cross sectional areas of material. Depending on the material, the transfer of heat can be considered to have poor to good thermal conductivity properties (insulator vs. conductor).

Heat Transfer goes as:

Silicon → package → lead frame → PCB

Convection: Heat transfer is by means of airflow. This could be from a fan or natural convection. Natural convection occurs when air currents rise from the hot device to cooler air.

Thermal impedance is defined as:

$$R_\theta = \frac{\Delta T}{Power} \quad (30)$$

Thermal impedance from the silicon junction to the ambient air is defined as:

$$R_{\theta JA} = \frac{T_J - T_A}{Power} \quad (31)$$

The PCB size, weight of copper used to route traces and ground plane, and number of layers within the PCB can greatly effect $R_{\theta JA}$. The type and number of thermal vias can also make a large difference in the thermal impedance. Thermal vias are necessary in most applications. They conduct heat from the surface of the PCB to the ground plane. Four to six thermal vias should be placed under the exposed pad to the ground plane if the WSON package is used.

Thermal impedance also depends on the thermal properties of the application operating conditions (Vin, Vo, Io etc), and the surrounding circuitry.

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Silicon Junction Temperature Determination Method 1:

To accurately measure the silicon temperature for a given application, two methods can be used. The first method requires the user to know the thermal impedance of the silicon junction to top case temperature.

Some clarification needs to be made before we go any further.

$R_{\theta JC}$ is the thermal impedance from all six sides of an IC package to silicon junction.

$R_{\Phi JC}$ is the thermal impedance from top case to the silicon junction.

In this data sheet we will use $R_{\Phi JC}$ so that it allows the user to measure top case temperature with a small thermocouple attached to the top case.

$R_{\Phi JC}$ is approximately 30°C/Watt for the 6-pin WSON package with the exposed pad. Knowing the internal dissipation from the efficiency calculation given previously, and the case temperature, which can be empirically measured on the bench we have:

$$R_{\Phi JC} = \frac{T_J - T_C}{\text{Power}} \tag{32}$$

Therefore:

$$T_j = (R_{\Phi JC} \times P_{LOSS}) + T_C \tag{33}$$

From the previous example:

$$T_j = (R_{\Phi JC} \times P_{INTERNAL}) + T_C \tag{34}$$

$$T_j = 30°C/W \times 0.189W + T_C \tag{35}$$

The second method can give a very accurate silicon junction temperature.

The first step is to determine $R_{\theta JA}$ of the application. The LM2830 has over-temperature protection circuitry. When the silicon temperature reaches 165°C, the device stops switching. The protection circuitry has a hysteresis of about 15°C. Once the silicon temperature has decreased to approximately 150°C, the device will start to switch again. Knowing this, the $R_{\theta JA}$ for any application can be characterized during the early stages of the design one may calculate the $R_{\theta JA}$ by placing the PCB circuit into a thermal chamber. Raise the ambient temperature in the given working application until the circuit enters thermal shutdown. If the SW-pin is monitored, it will be obvious when the internal PFET stops switching, indicating a junction temperature of 165°C. Knowing the internal power dissipation from the above methods, the junction temperature, and the ambient temperature $R_{\theta JA}$ can be determined.

$$R_{\theta JA} = \frac{165° - Ta}{P_{INTERNAL}} \tag{36}$$

Once this is determined, the maximum ambient temperature allowed for a desired junction temperature can be found.

An example of calculating $R_{\theta JA}$ for an application using the Texas Instruments LM2830 WSON demonstration board is shown below.

The four layer PCB is constructed using FR4 with ½ oz copper traces. The copper ground plane is on the bottom layer. The ground plane is accessed by two vias. The board measures 3.0cm x 3.0cm. It was placed in an oven with no forced airflow. The ambient temperature was raised to 144°C, and at that temperature, the device went into thermal shutdown.

From the previous example:

$$P_{INTERNAL} = 189mW \tag{37}$$

$$R_{\theta JA} = \frac{165^oC - 144^oC}{189\ mW} = 111^o\ C/W \tag{38}$$

If the junction temperature was to be kept below 125°C, then the ambient temperature could not go above 109°C

$$T_j - (R_{\theta JA} \times P_{LOSS}) = T_A \tag{39}$$

$$125°C - (111°C/W \times 189mW) = 104°C \tag{40}$$

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WSON Package

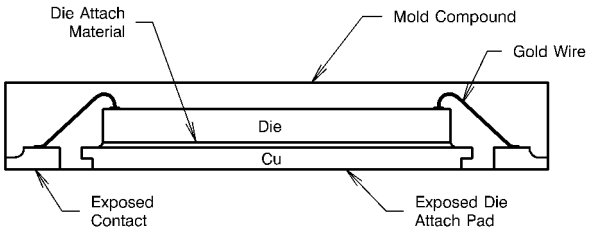


Figure 23. Internal WSON Connection

For certain high power applications, the PCB land may be modified to a "dog bone" shape (see Figure 24). By increasing the size of ground plane, and adding thermal vias, the $R_{\theta JA}$ for the application can be reduced.

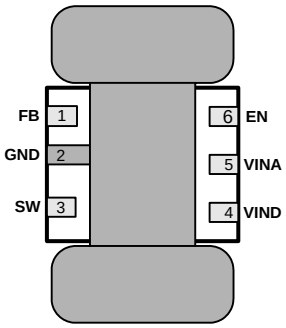


Figure 24. 6-Lead WSON PCB Dog Bone Layout

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LM2830X Design Example 1

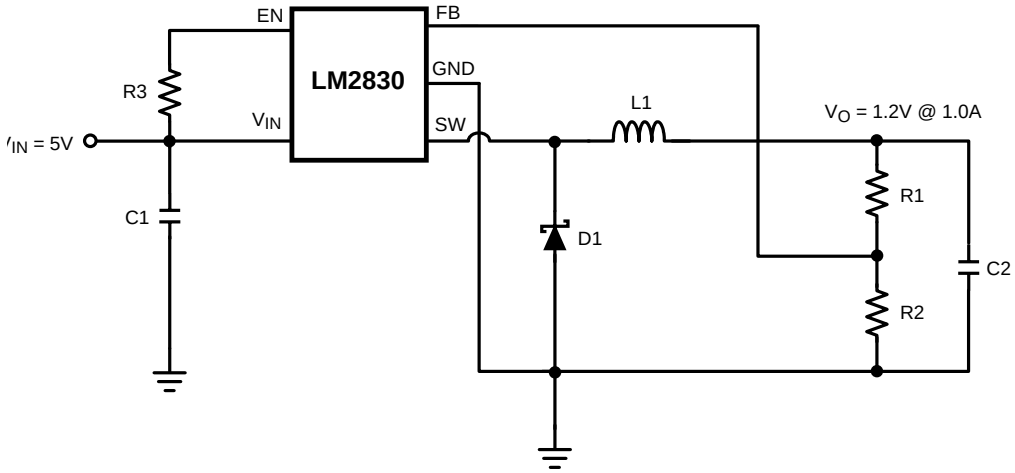


Figure 25. LM2830X (1.6MHz): Vin = 5V, Vo = 1.2V @ 1.0A

Table 2. Bill of Materials

Part ID	Part Value	Manufacturer	Part Number
U1	1.0A Buck Regulator	TI	LM2830X
C1, Input Cap	22µF, 6.3V, X5R	TDK	C3216X5ROJ226M
C2, Output Cap	22µF, 6.3V, X5R	TDK	C3216X5ROJ226M
D1, Catch Diode	$0.3V_f$ Schottky 1.5A, $30V_R$	TOSHIBA	CRS08
L1	3.3µH, 1.3A	Coilcraft	ME3220-332
R2	15.0kΩ, 1%	Vishay	CRCW08051502F
R1	15.0kΩ, 1%	Vishay	CRCW08051502F
R3	100kΩ, 1%	Vishay	CRCW08051003F

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LM2830X Design Example 2

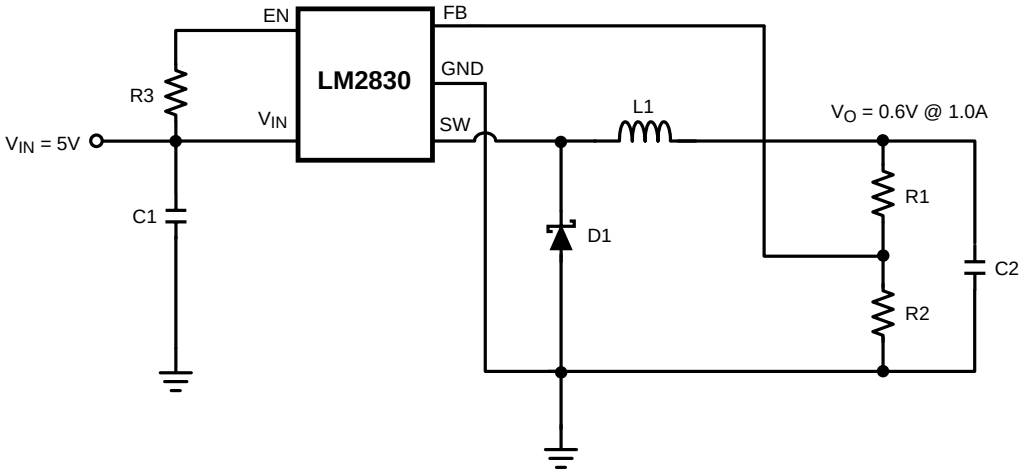


Figure 26. LM2830X (1.6MHz): Vin = 5V, Vo = 0.6V @ 1.0A

Table 3. Bill of Materials

Part ID	Part Value	Manufacturer	Part Number
U1	1.0A Buck Regulator	TI	LM2830X
C1, Input Cap	22µF, 6.3V, X5R	TDK	C3216X5ROJ226M
C2, Output Cap	22µF, 6.3V, X5R	TDK	C3216X5ROJ226M
D1, Catch Diode	$0.3V_f$ Schottky 1.5A, $30V_R$	TOSHIBA	CRS08
L1	3.3µH, 1.3A	Coilcraft	ME3220-332
R2	10.0kΩ, 1%	Vishay	CRCW08051000F
R1	0Ω		
R3	100kΩ, 1%	Vishay	CRCW08051003F

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LM2830X Design Example 3

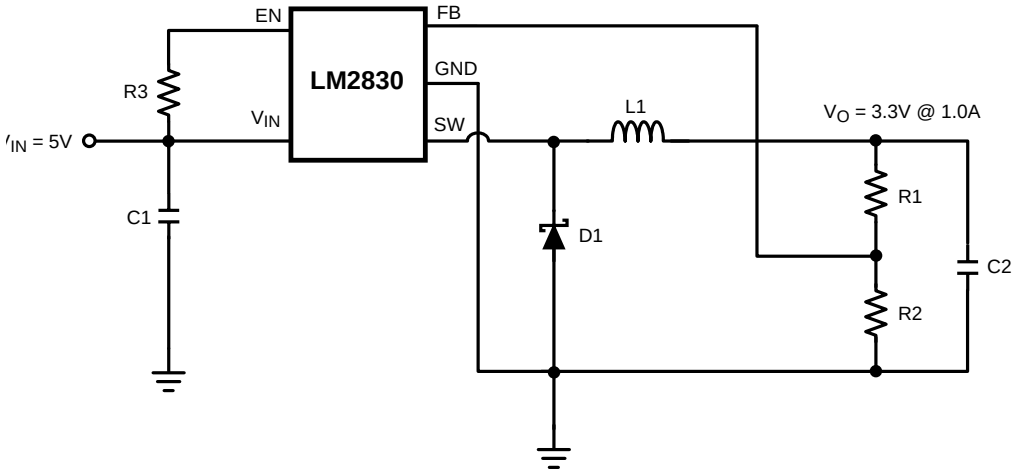


Figure 27. LM2830X (1.6MHz): Vin = 5V, Vo = 3.3V @ 1.0A

Table 4. Bill of Materials

Part ID	Part Value	Manufacturer	Part Number
U1	1.0A Buck Regulator	TI	LM2830X
C1, Input Cap	22µF, 6.3V, X5R	TDK	C3216X5ROJ226M
C2, Output Cap	22µF, 6.3V, X5R	TDK	C3216X5ROJ226M
D1, Catch Diode	$0.3V_f$ Schottky 1.5A, $30V_R$	TOSHIBA	CRS08
L1	2.2µH, 1.8A	Coilcraft	ME3220-222
R2	10.0kΩ, 1%	Vishay	CRCW08051002F
R1	45.3kΩ, 1%	Vishay	CRCW08054532F
R3	100kΩ, 1%	Vishay	CRCW08051003F

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LM2830Z Design Example 4

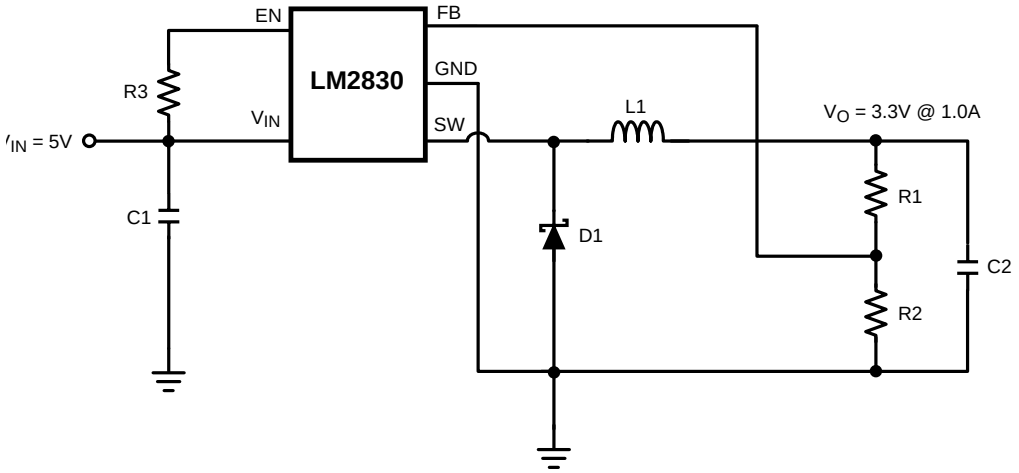


Figure 28. LM2830Z (3MHz): Vin = 5V, Vo = 3.3V @ 1.0A

Table 5. Bill of Materials

Part ID	Part Value	Manufacturer	Part Number
U1	1.0A Buck Regulator	TI	LM2830Z
C1, Input Cap	22µF, 6.3V, X5R	TDK	C3216X5ROJ226M
C2, Output Cap	22µF, 6.3V, X5R	TDK	C3216X5ROJ226M
D1, Catch Diode	$0.3V_f$ Schottky 1.5A, $30V_R$	TOSHIBA	CRS08
L1	1.6µH, 2.0A	TDK	VLCF4018T-1R6N1R7-2
R2	10.0kΩ, 1%	Vishay	CRCW08051002F
R1	45.3kΩ, 1%	Vishay	CRCW08054532F
R3	100kΩ, 1%	Vishay	CRCW08051003F

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LM2830Z Design Example 5

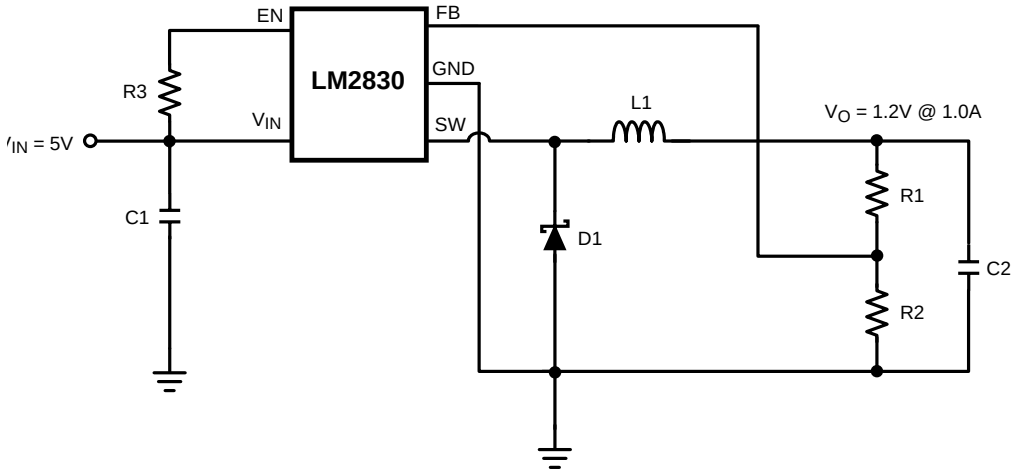


Figure 29. LM2830Z (3MHz): Vin = 5V, Vo = 1.2V @ 1.0A

Table 6. Bill of Materials

Part ID	Part Value	Manufacturer	Part Number
U1	1.0A Buck Regulator	TI	LM2830Z
C1, Input Cap	22µF, 6.3V, X5R	TDK	C3216X5ROJ226M
C2, Output Cap	22µF, 6.3V, X5R	TDK	C3216X5ROJ226M
D1, Catch Diode	$0.3V_f$ Schottky 1.5A, $30V_R$	TOSHIBA	CRS08
L1	1.6µH, 2.0A	TDK	VLCF4018T-1R6N1R7-2
R2	10.0kΩ, 1%	Vishay	CRCW08051002F
R1	10.0kΩ, 1%	Vishay	CRCW08051002F
R3	100kΩ, 1%	Vishay	CRCW08051003F

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LM2830X Dual Converters with Delayed Enabled Design Example 6

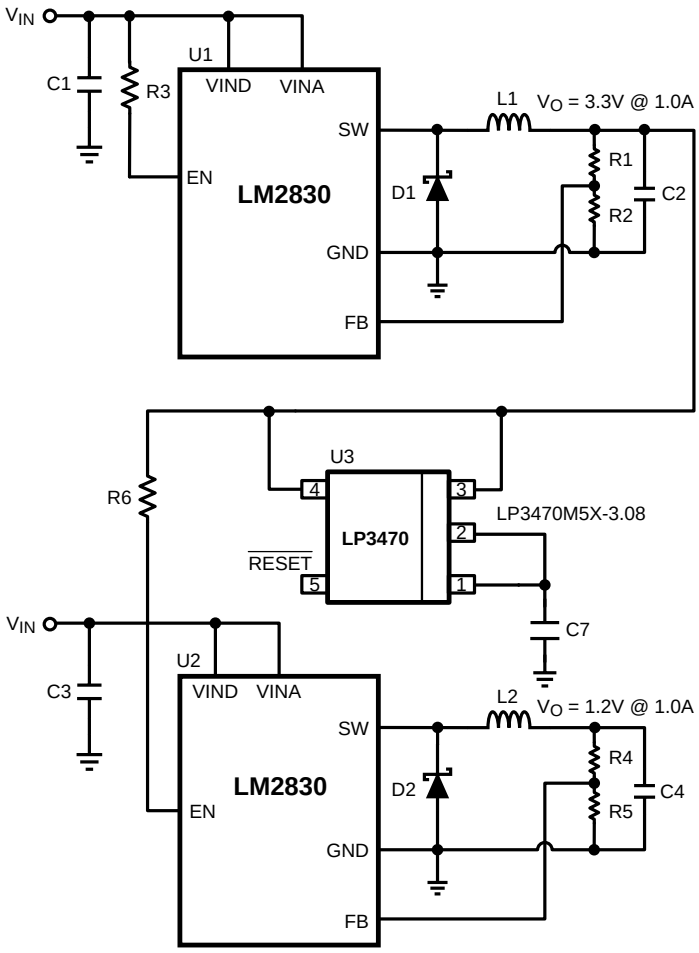


Figure 30. LM2830X (1.6MHz): Vin = 5V, Vo = 1.2V @ 1.0A & 3.3V @1.0A

Table 7. Bill of Materials

Part ID	Part Value	Manufacturer	Part Number
U1, U2	1.0A Buck Regulator	TI	LM2830X
U3	Power on Reset	TI	LP3470M5X-3.08
C1, C3 Input Cap	22µF, 6.3V, X5R	TDK	C3216X5ROJ226M
C2, C4 Output Cap	22µF, 6.3V, X5R	TDK	C3216X5ROJ226M
C7	Trr delay capacitor	TDK	
D1, D2 Catch Diode	$0.3V_f$ Schottky 1.5A, $30V_R$	TOSHIBA	CRS08
L1, L2	3.3µH, 1.3A	Coilcraft	ME3220-332
R2, R4, R5	10.0kΩ, 1%	Vishay	CRCW08051002F
R1, R6	45.3kΩ, 1%	Vishay	CRCW08054532F
R3	100kΩ, 1%	Vishay	CRCW08051003F

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LM2830X Buck Converter & Voltage Double Circuit with LDO Follower Design Example 7

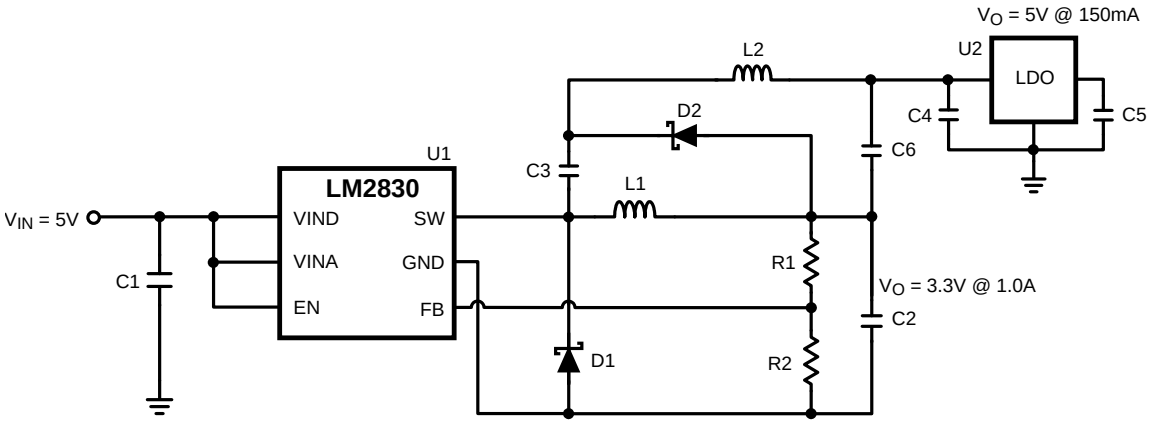


Figure 31. LM2830X (1.6MHz): Vin = 5V, Vo = 3.3V @ 1.0A & LP2986-5.0 @ 150mA

Table 8. Bill of Materials

Part ID	Part Value	Manufacturer	Part Number
U1	1.0A Buck Regulator	TI	LM2830X
U2	200mA LDO	TI	LP2986-5.0
C1, Input Cap	22µF, 6.3V, X5R	TDK	C3216X5ROJ226M
C2, Output Cap	22µF, 6.3V, X5R	TDK	C3216X5ROJ226M
C3 – C6	2.2µF, 6.3V, X5R	TDK	C1608X5R0J225M
D1, Catch Diode	$0.3V_f$ Schottky 1.5A, $30V_R$	TOSHIBA	CRS08
D2	$0.4V_f$ Schottky $20V_R$, 500mA	ON Semi	MBR0520
L2	10µH, 800mA	CoilCraft	ME3220-103
L1	3.3µH, 2.2A	TDK	VLCF5020T-3R3N2R0-1
R2	45.3kΩ, 1%	Vishay	CRCW08054532F
R1	10.0kΩ, 1%	Vishay	CRCW08051002F

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REVISION HISTORY

Changes from Revision C (April 2013) to Revision D	Page
• Changed layout of National Data Sheet to TI format	23

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PACKAGING INFORMATION

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
LM2830XMF	ACTIVE	SOT-23	DBV	5	1000	TBD	Call TI	Call TI	-40 to 125	SKTB	Samples
LM2830XMF/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	SKTB	Samples
LM2830XMFX	ACTIVE	SOT-23	DBV	5		TBD	Call TI	Call TI	-40 to 125	SKTB	Samples
LM2830XMFX/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	SKTB	Samples
LM2830XQMF/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	SUFB	Samples
LM2830XQMFE/NOPB	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	SUFB	Samples
LM2830XQMFX/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	SUFB	Samples
LM2830ZMF	ACTIVE	SOT-23	DBV	5		TBD	Call TI	Call TI	-40 to 125	SKXB	Samples
LM2830ZMF/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	SKXB	Samples
LM2830ZMFX	ACTIVE	SOT-23	DBV	5		TBD	Call TI	Call TI	-40 to 125	SKXB	Samples
LM2830ZMFX/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	SKXB	Samples
LM2830ZQMF/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	SURB	Samples
LM2830ZQMFE/NOPB	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	SURB	Samples
LM2830ZQMFX/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	SURB	Samples
LM2830ZSD	ACTIVE	WSON	NGG	6		TBD	Call TI	Call TI	-40 to 125	L192B	Samples
LM2830ZSD/NOPB	ACTIVE	WSON	NGG	6	1000	Green (RoHS & no Sb/Br)	CU SN	Level-3-260C-168 HR	-40 to 125	L192B	Samples
LM2830ZSDX	ACTIVE	WSON	NGG	6		TBD	Call TI	Call TI	-40 to 125	L192B	Samples
LM2830ZSDX/NOPB	ACTIVE	WSON	NGG	6	4500	Green (RoHS & no Sb/Br)	CU SN	Level-3-260C-168 HR	-40 to 125	L192B	Samples

(1) The marketing status values are defined as follows:
ACTIVE: Product device recommended for new designs.
LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.
NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.
PREVIEW: Device has been announced but is not in production. Samples may or may not be available.
OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.
TBD: The Pb-Free/Green conversion plan has not been defined.
Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.
Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.
Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

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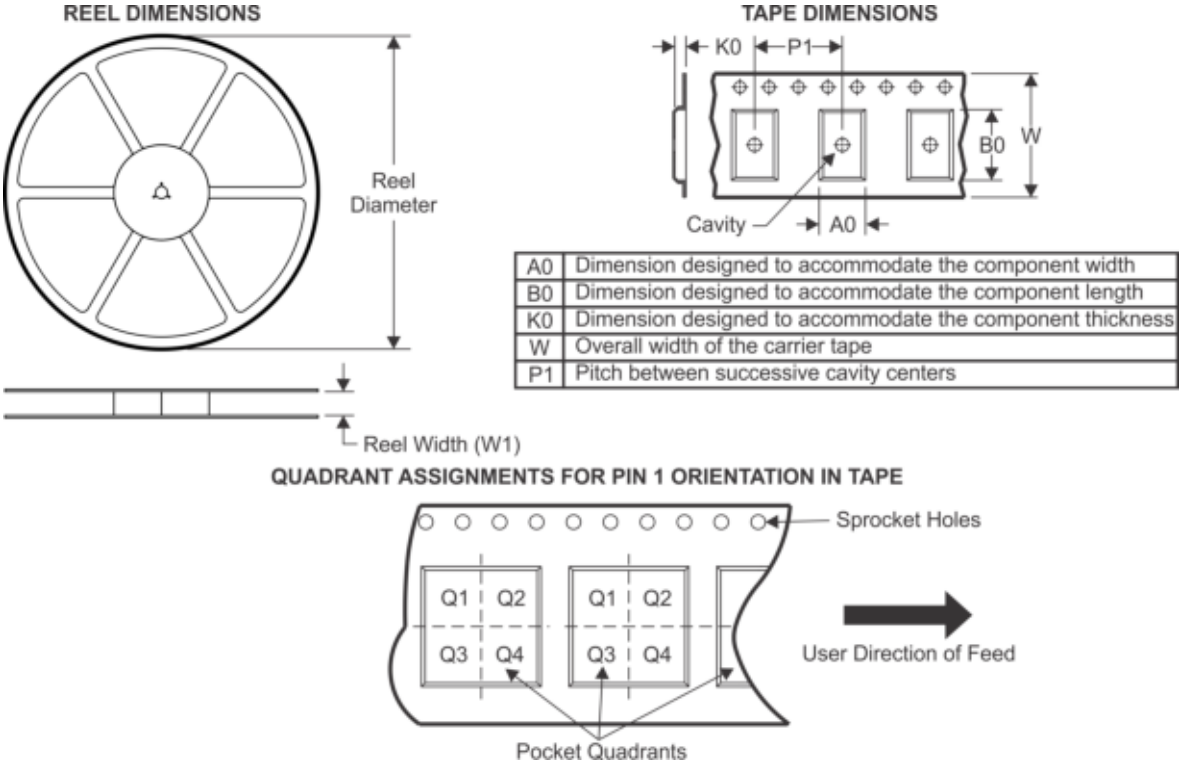
OTHER QUALIFIED VERSIONS OF LM2830, LM2830-Q1 :

- Catalog: LM2830
- Automotive: LM2830-Q1

NOTE: Qualified Version Definitions:

- Catalog - TI's standard catalog product
- Automotive - Q100 devices qualified for high-reliability automotive applications targeting zero defects

TAPE AND REEL INFORMATION

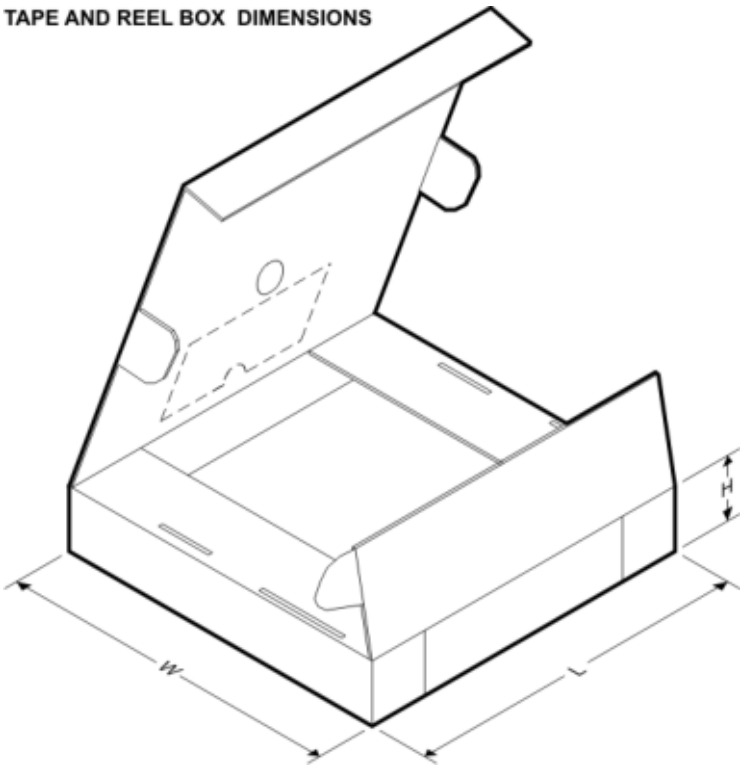


A0	Dimension designed to accommodate the component width
B0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
LM2830XMF	SOT-23	DBV	5	1000	178.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
LM2830XMF/NOPB	SOT-23	DBV	5	1000	178.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
LM2830XMFX/NOPB	SOT-23	DBV	5	3000	178.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
LM2830XQMF/NOPB	SOT-23	DBV	5	1000	178.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
LM2830XQMFE/NOPB	SOT-23	DBV	5	250	178.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
LM2830XQMFX/NOPB	SOT-23	DBV	5	3000	178.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
LM2830ZMF/NOPB	SOT-23	DBV	5	1000	178.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
LM2830ZMFX/NOPB	SOT-23	DBV	5	3000	178.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
LM2830ZQMF/NOPB	SOT-23	DBV	5	1000	178.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
LM2830ZQMFE/NOPB	SOT-23	DBV	5	250	178.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
LM2830ZQMFX/NOPB	SOT-23	DBV	5	3000	178.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
LM2830ZSD/NOPB	WSON	NGG	6	1000	178.0	12.4	3.3	3.3	1.0	8.0	12.0	Q1
LM2830ZSDX/NOPB	WSON	NGG	6	4500	330.0	12.4	3.3	3.3	1.0	8.0	12.0	Q1

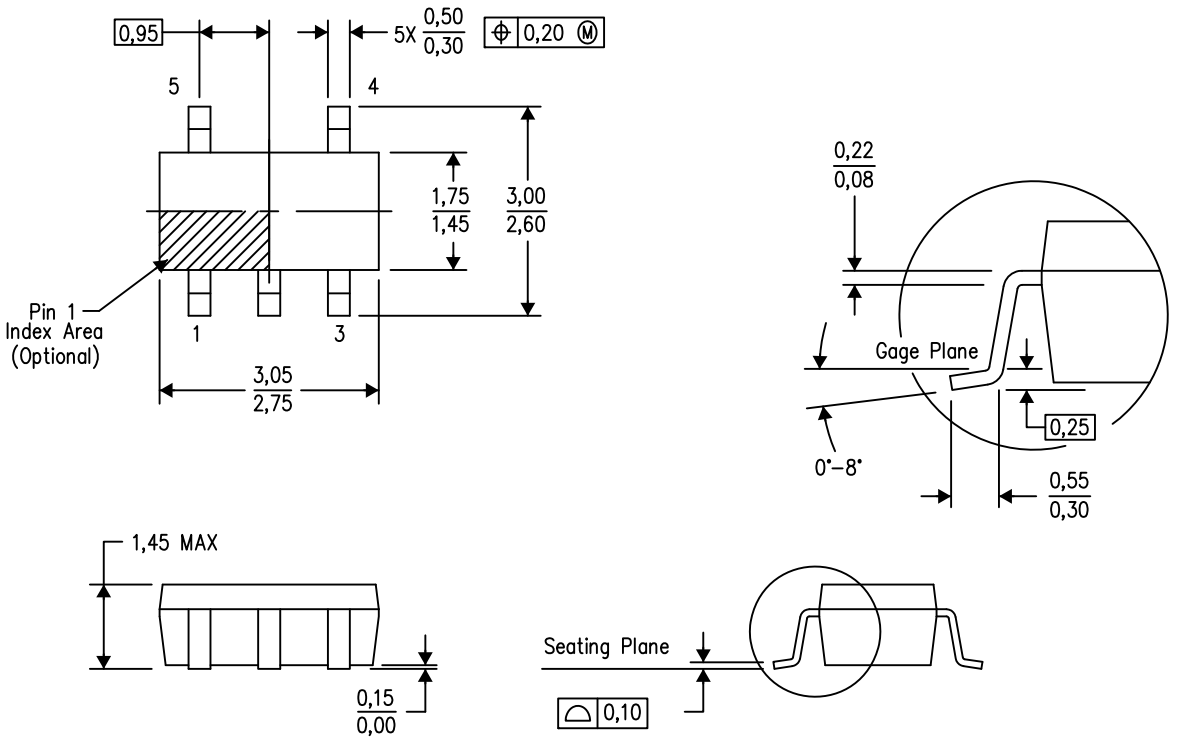
TAPE AND REEL BOX DIMENSIONS



*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
LM2830XMF	SOT-23	DBV	5	1000	210.0	185.0	35.0
LM2830XMF/NOPB	SOT-23	DBV	5	1000	210.0	185.0	35.0
LM2830XMFX/NOPB	SOT-23	DBV	5	3000	210.0	185.0	35.0
LM2830XQMF/NOPB	SOT-23	DBV	5	1000	210.0	185.0	35.0
LM2830XQMFE/NOPB	SOT-23	DBV	5	250	210.0	185.0	35.0
LM2830XQMFX/NOPB	SOT-23	DBV	5	3000	210.0	185.0	35.0
LM2830ZMF/NOPB	SOT-23	DBV	5	1000	210.0	185.0	35.0
LM2830ZMFX/NOPB	SOT-23	DBV	5	3000	210.0	185.0	35.0
LM2830ZQMF/NOPB	SOT-23	DBV	5	1000	210.0	185.0	35.0
LM2830ZQMFE/NOPB	SOT-23	DBV	5	250	210.0	185.0	35.0
LM2830ZQMFX/NOPB	SOT-23	DBV	5	3000	210.0	185.0	35.0
LM2830ZSD/NOPB	WSON	NGG	6	1000	203.0	190.0	41.0
LM2830ZSDX/NOPB	WSON	NGG	6	4500	367.0	367.0	35.0

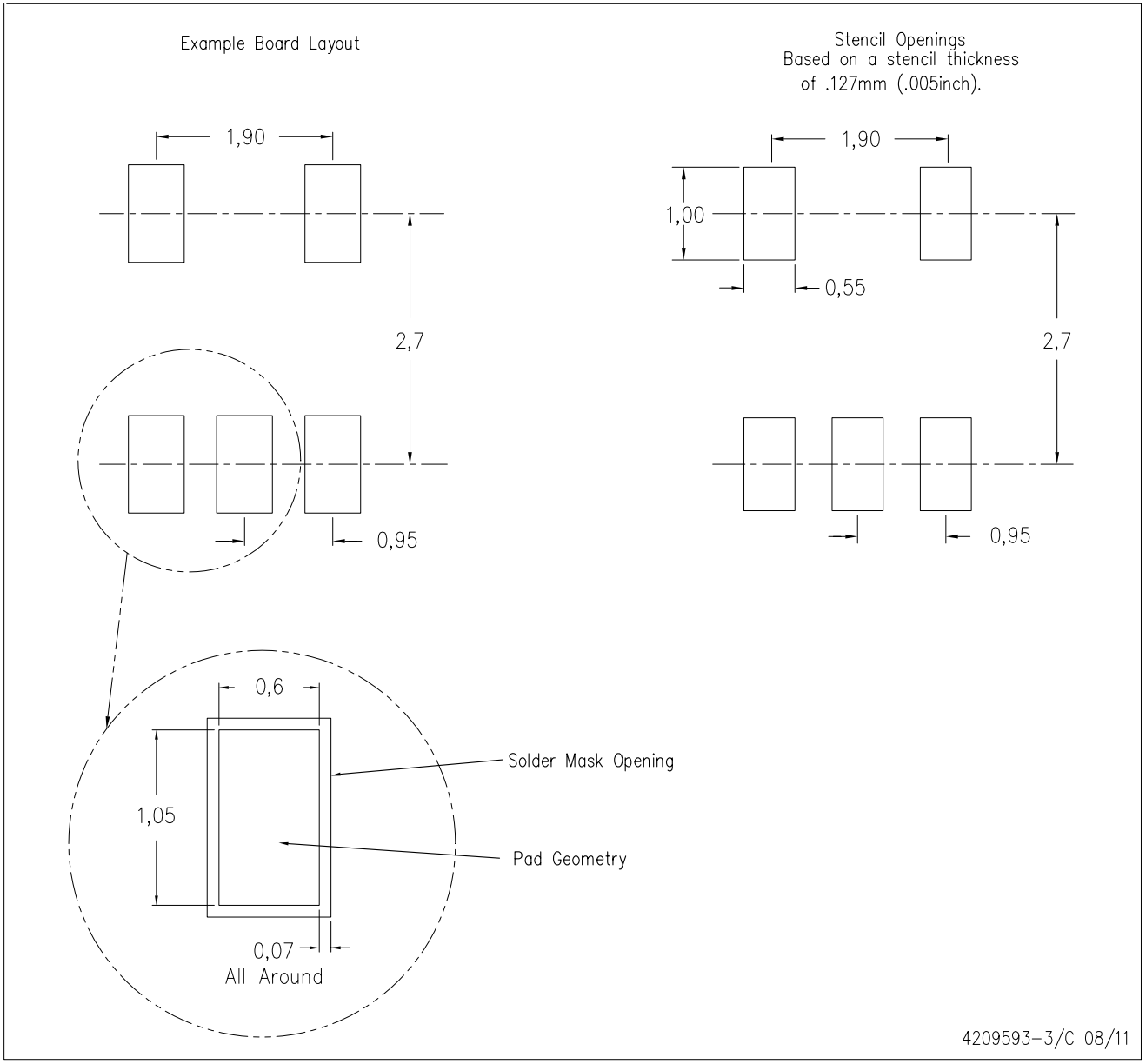
DBV (R–PDSO–G5) PLASTIC SMALL–OUTLINE PACKAGE



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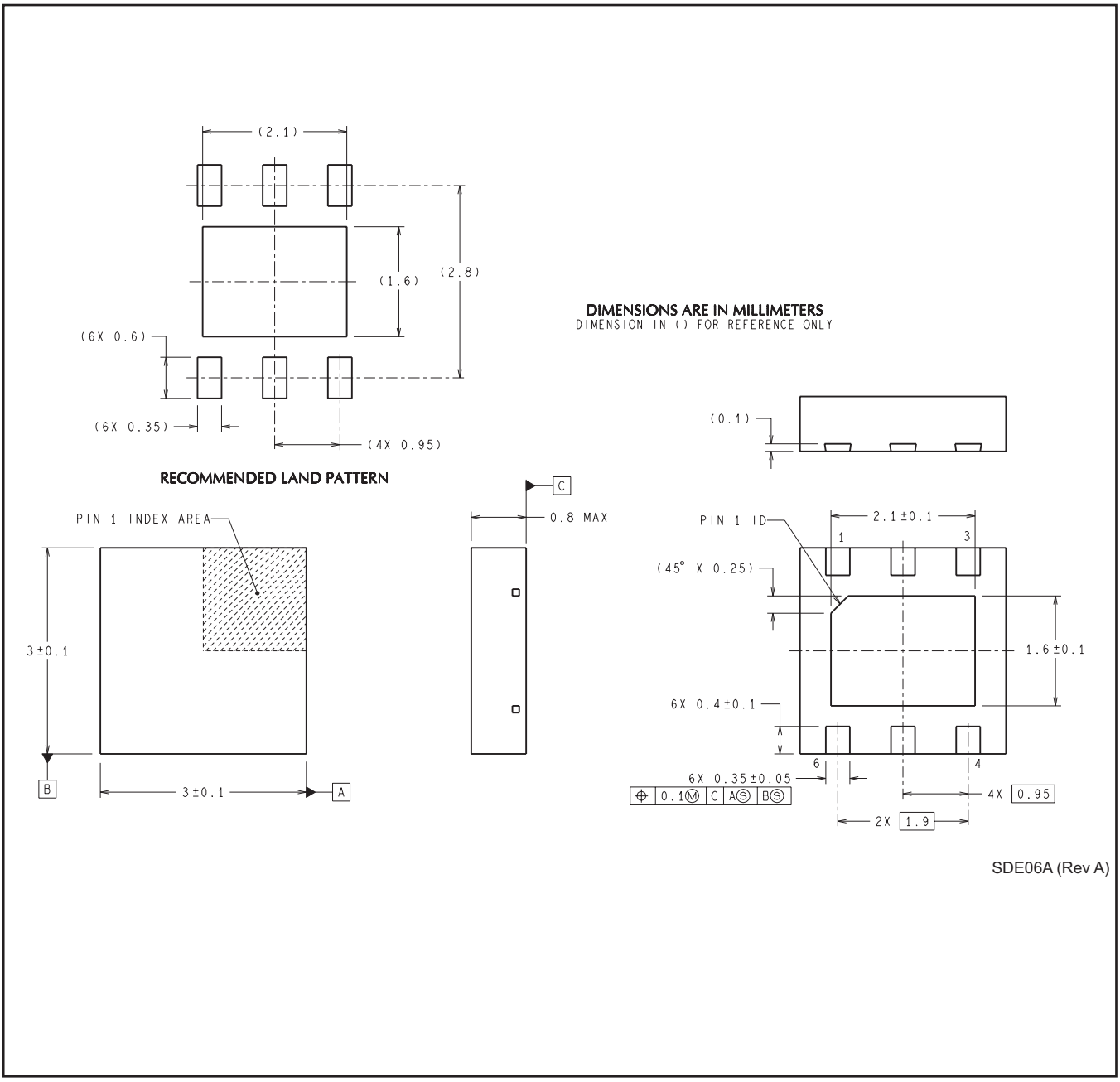
NOTES:
A. All linear dimensions are in millimeters.
B. This drawing is subject to change without notice.
C. Body dimensions do not include mold flash or protrusion. Mold flash and protrusion shall not exceed 0.15 per side.
D. Falls within JEDEC MO–178 Variation AA.

DBV (R–PDSO–G5) PLASTIC SMALL OUTLINE



NOTES:
A. All linear dimensions are in millimeters.
B. This drawing is subject to change without notice.
C. Customers should place a note on the circuit board fabrication drawing not to alter the center solder mask defined pad.
D. Publication IPC–7351 is recommended for alternate designs.
E. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Example stencil design based on a 50% volumetric metal load solder paste. Refer to IPC–7525 for other stencil recommendations.

NGG0006A



SDE06A (Rev A)

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Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265
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