

MMBT3906

40 V, 200 mA PNP switching transistor

10 April 2025 Product data sheet

1. General description

PNP switching transistor in a small SOT23 Surface-Mounted Device (SMD) plastic package.

NPN complement: MMBT3904

2. Features and benefits

- Collector current capability I_C = -200 mA
- Collector-emitter voltage V_{CEO} = -40 V
- AEC-Q101 qualified

3. Applications

- General switching and amplification

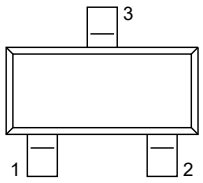
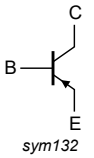
4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V_{CEO}	collector-emitter voltage	open base		-	-	-40	V
I_C	collector current			-	-	-200	mA
h_{FE}	DC current gain	V_{CE} = -1 V; I_C = -0.1 mA; T_{amb} = 25 °C		60	-	-	

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	B	base		
2	E	emitter		
3	C	collector		

SOT23

sym132

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
MMBT3906	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
MMBT3906	7B%

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	-40	V
V_{CEO}	collector-emitter voltage	open base		-	-40	V
V_{EBO}	emitter-base voltage	open collector		-	-6	V
I_C	collector current			-	-200	mA
I_{CM}	peak collector current			-	-200	mA
I_{BM}	peak base current			-	-100	mA
P_{tot}	total power dissipation	$T_{amb} \leq 25$ °C	[1]	-	250	mW
T_j	junction temperature			-	150	°C
T_{amb}	ambient temperature			-65	150	°C
T_{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint.

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient		[1]	-	-	500	K/W

[1] Device mounted on an FR4 , PCB; single-sided copper; tin-plated and standard footprint.

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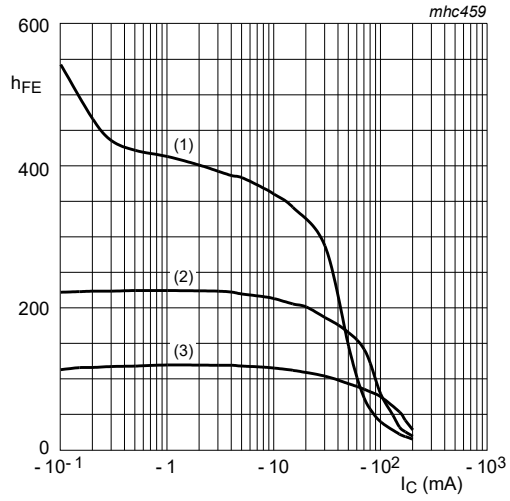
10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
I_{CBO}	collector-base cut-off current	V_{CB} = -30 V; I_E = 0 A; T_{amb} = 25 °C		-	-	-50	nA
I_{EBO}	emitter-base cut-off current	V_{EB} = -6 V; I_C = 0 A; T_{amb} = 25 °C		-	-	-50	nA
h_{FE}	DC current gain	V_{CE} = -1 V; I_C = -0.1 mA; T_{amb} = 25 °C		60	-	-	
		V_{CE} = -1 V; I_C = -1 mA; T_{amb} = 25 °C		80	-	-	
		V_{CE} = -1 V; I_C = -10 mA; T_{amb} = 25 °C		100	-	300	
		V_{CE} = -1 V; I_C = -50 mA; T_{amb} = 25 °C		60	-	-	
		V_{CE} = -1 V; I_C = -100 mA; T_{amb} = 25 °C		30	-	-	
V_{CEsat}	collector-emitter saturation voltage	I_C = -10 mA; I_B = -1 mA; T_{amb} = 25 °C		-	-	-250	mV
		I_C = -50 mA; I_B = -5 mA; T_{amb} = 25 °C		-	-	-400	mV
V_{BEsat}	base-emitter saturation voltage	I_C = -10 mA; I_B = -1 mA; T_{amb} = 25 °C		-	-	-850	mV
		I_C = -50 mA; I_B = -5 mA; T_{amb} = 25 °C		-	-	-950	mV
C_c	collector capacitance	V_{CB} = -5 V; I_E = 0 A; i_e = 0 A; f = 1 MHz; T_{amb} = 25 °C		-	-	4.5	pF
C_e	emitter capacitance	V_{EB} = -500 mV; I_C = 0 A; i_c = 0 A; f = 1 MHz; T_{amb} = 25 °C		-	-	10	pF
f_T	transition frequency	V_{CE} = -20 V; I_C = -10 mA; f = 100 MHz; T_{amb} = 25 °C		250	-	-	MHz
NF	noise figure	V_{CE} = -5 V; I_C = -100 µA; R_S = 1 kΩ; f = 10 Hz to 15.7 kHz		-	-	4	dB
Switching times (between 10 % and 90 % levels)							
t_d	delay time	I_{Bon} = -1 mA; I_{Boff} = 1 mA; I_{Con} = −10 mA; T_{amb} = 25 °C		-	-	35	ns
t_r	rise time			-	-	35	ns
t_s	storage time			-	-	225	ns
t_f	fall time			-	-	75	ns

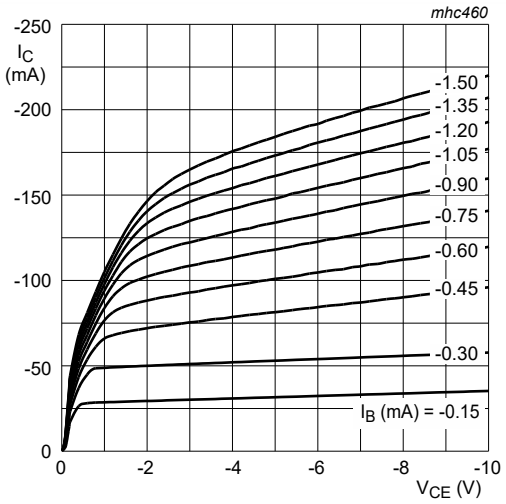
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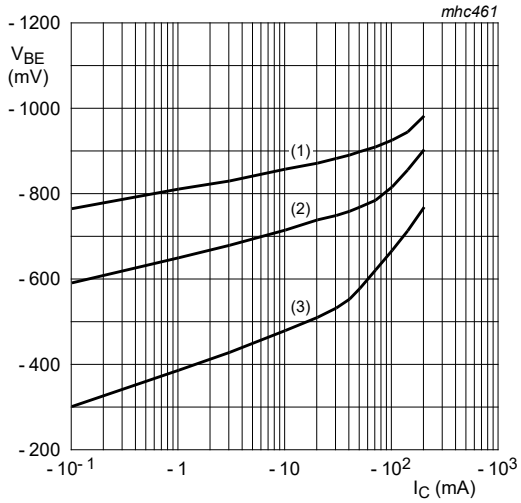
V_{CE} = −1 V
(1) T_{amb} = 150 °C
(2) T_{amb} = 25 °C
(3) T_{amb} = −55 °C

Fig. 1. DC current gain as a function of collector current; typical values



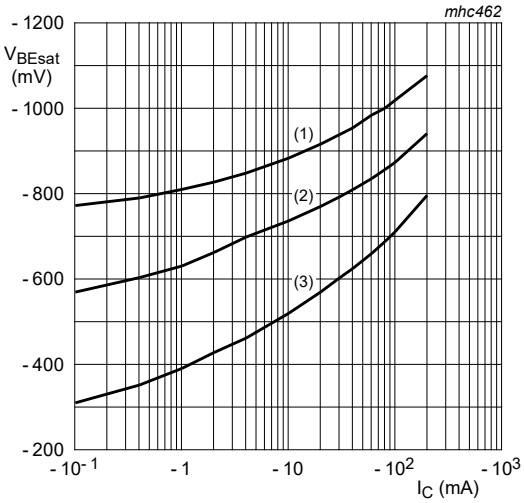
T_{amb} = 25 °C

Fig. 2. Collector current as a function of collector-emitter voltage; typical values



V_{CE} = -1 V
(1) T_{amb} = −55 °C
(2) T_{amb} = 25 °C
(3) T_{amb} = 150 °C

Fig. 3. Base-emitter voltage as a function of collector current; typical values

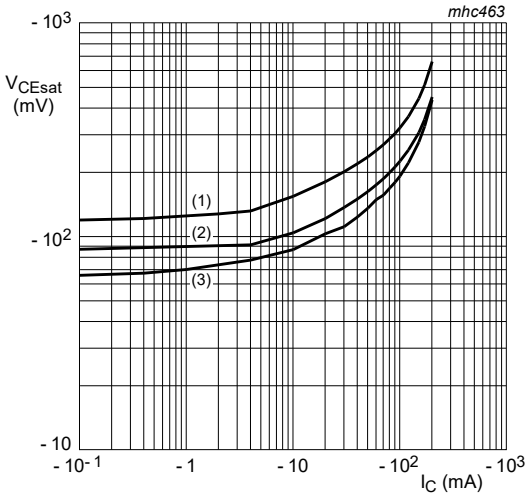


I_C/I_B = 10
(1) T_{amb} = −55 °C
(2) T_{amb} = 25 °C
(3) T_{amb} = 150 °C

Fig. 4. Base-emitter saturation voltage as a function of collector current; typical values

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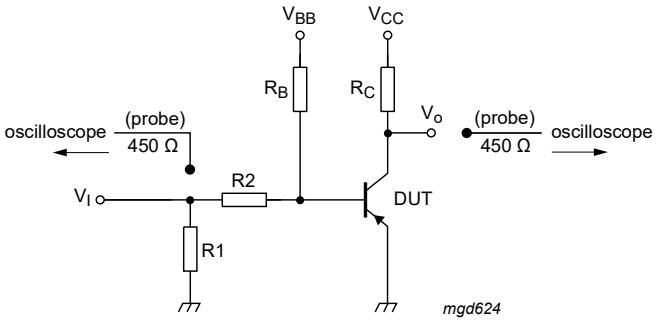
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I_C/I_B = 10
(1) T_{amb} = 150 °C
(2) T_{amb} = 25 °C
(3) T_{amb} = −55 °C

Fig. 5. Collector-emitter saturation voltage as a function of collector current; typical values

11. Test information



V_I = -5 V; T = 500 µs; t_p = 10 µs; $t_r = t_f \leq 3$ ns
R1 = 56 Ω; R2 = 2.5 kΩ; R_B = 3.9 kΩ; R_C = 270 Ω
V_{BB} = -1.9 V; V_{CC} = -3 V
Oscilloscope: input impedance Z_i = 50 Ω

Fig. 6. Test circuit for switching times

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

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12. Package outline

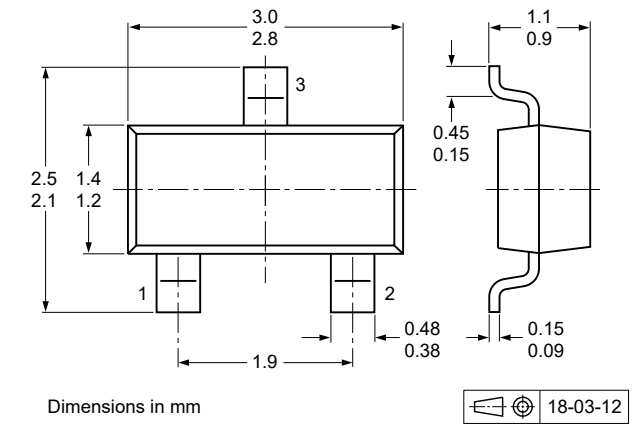


Fig. 7. Package outline SOT23

13. Soldering

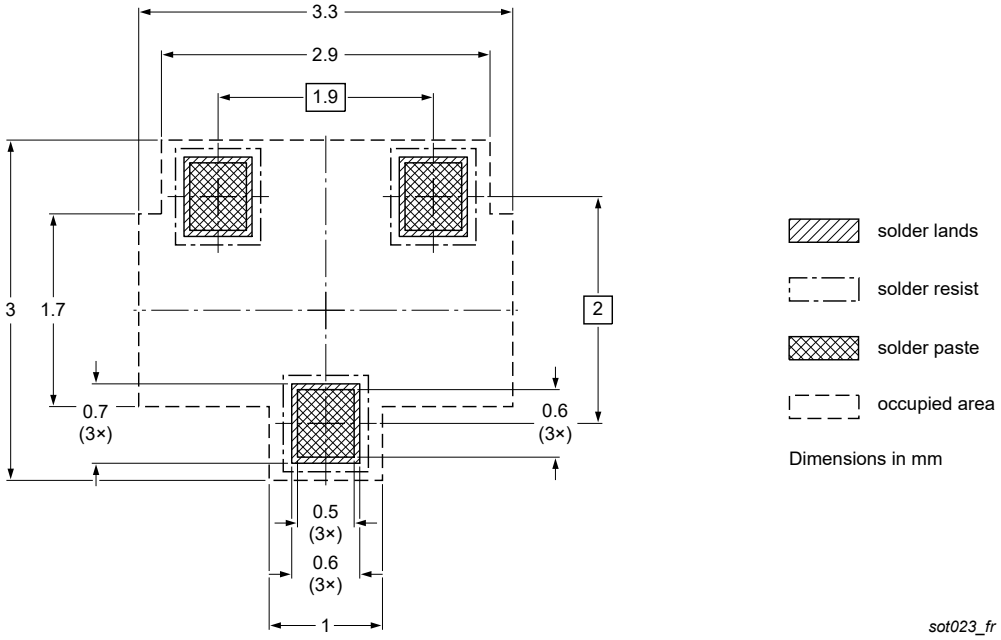


Fig. 8. Reflow soldering footprint for SOT23

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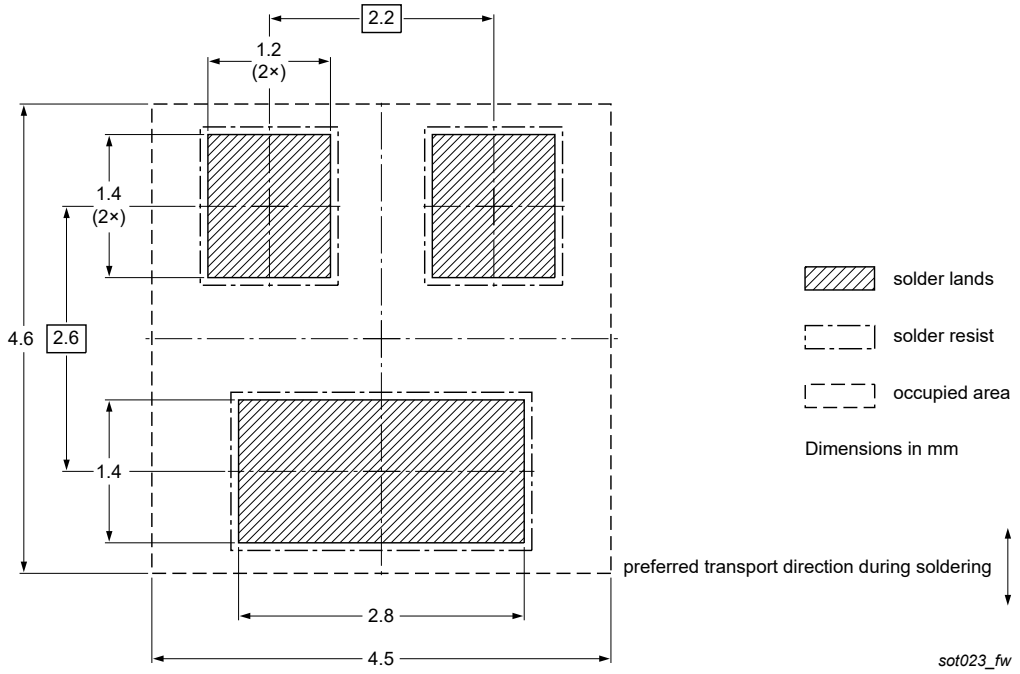


Fig. 9. Wave soldering footprint for SOT23

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14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
MMBT3906 v.4	20250410	Product data sheet	-	MMBT3906 v.3
Modifications:	• Characteristics: unit changed to mV at V_{BEsat}			
MMBT3906 v.3	20230901	Product data sheet	-	MMBT3906 v.2
MMBT3906 v.2	20030318	Product data sheet	-	MMBT3906 v.1
MMBT3906 v.1	20000411	Product data sheet	-	-

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Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.
[2] The term 'short data sheet' is explained in section "Definitions".
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Date of release: 10 April 2025