NCP584

Tri−Mode 200 mA CMOS LDO Regulator with Enable

The NCP584 series of low drop out regulators are designed for portable battery powered applications which require precise output voltage accuracy, low quiescent current, and high ripple rejection. These devices feature an enable function which lowers current consumption significantly and are offered in the SOT23−5 package.

This series of devices have three modes. Chip Enable (CE mode), Fast Transient Mode (FT mode), and Low Power Mode (LP mode). Both the FT and LP mode are utilized via the ECO pin.

Features

- Tri−mode Operation
- Low Dropout Voltage of 400 mV at 200 mA, Output Voltage = 0.9 V
  300 mV at 200 mA, Output Voltage = 1.2 V
  200 mV at 200 mA, Output Voltage = 1.8 V
- Excellent Line Regulation of 0.05%/V (0.10% LP Mode)
- Excellent Load Regulation of 10 mV (20 mV FT Mode)
- High Output Voltage Accuracy of ±2% (±3% LP mode)
- Ultra−Low Iq Current of:
  3.5 μA (LP mode, Output Voltage ≤ 1.5 V)
  40 μA (FT mode)
- Very Low Shutdown Current of 0.1 μA
- Excellent Power Supply Rejection Ratio of 75 dB at f = 1.0 kHz
- Low Temperature Drift Coefficient on the Output Voltage of ± 100 ppm/°C
- Fold Back Protection Circuit
- Input Voltage up to 6.5 V
- These are Pb−Free Devices

Typical Applications

- Portable Equipment
- Hand−Held Instrumentation
- Camcorders and Cameras

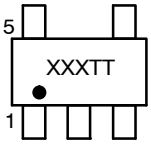
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MARKING DIAGRAM



SOT23−5
SN SUFFIX
CASE 1212



XXX = Specific Device Code
TT = Traceability Information

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 13 of this data sheet.

*Additional voltage options may be available between 0.8 V and 3.3 V in 100 mV steps.

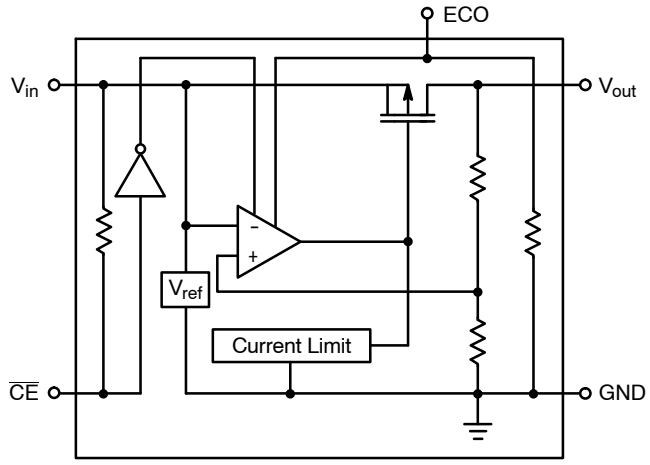


Figure 1. Simplified Block Diagram for Active Low

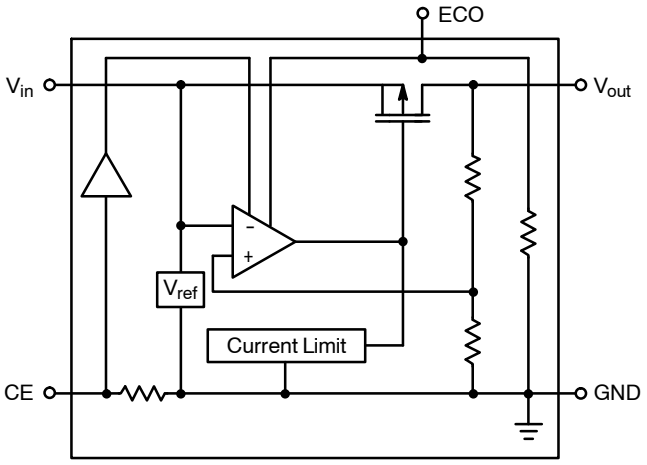


Figure 2. Simplified Block Diagram for Active High

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PIN FUNCTION DESCRIPTION

SOT23−5	Pin Name	Description
1	V_{in}	Power supply input voltage.
2	GND	Power supply ground.
3	$\overline{CE}$ or CE	Chip enable pin.
4	ECO	Mode alternative pin. (V_{ECO} = V_{in} for FT mode; V_{ECO} = GND for LP mode)
5	V_{out}	Regulated output voltage.

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Input Voltage	V_{in}	6.5	V
Input Voltage ($\overline{CE}$ or CE Pin)	V_{CE}	−0.3 to V_{in} +0.3	V
Input Voltage (ECO Pin)	V_{ECO}	−0.3 to V_{in} +0.3	V
Output Voltage	V_{out}	−0.3 to V_{in} +0.3	V
Output Current	I_{out}	250	mA
Power Dissipation	P_D	250	mW
ESD Capability, Human Body Model, C = 100 pF, R = 1.5 kΩ	ESD_{HBM}	1000	V
ESD Capability, Machine Model, C = 200 pF, R = 0 Ω	ESD_{MM}	150	V
Operating Ambient Temperature Range	T_A	−40 to +85	°C
Maximum Junction Temperature	$T_{J(max)}$	125	°C
Storage Temperature Range	T_{stg}	−55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

ELECTRICAL CHARACTERISTICS (V_{in} = V_{out} + 1.0 V, T_A = 25°C, unless otherwise noted.)

Characteristic	Symbol	Min	Typ	Max	Unit
Input Voltage	V_{in}	1.4	–	6.0	V
Output Voltage (1.0 μA ≤ I_{out} ≤ 30 mA) V_{ECO} = V_{in} V_{ECO} = GND	V_{out}	 V_{out} x 0.980 V_{out} x 0.970	 – –	 V_{out} x 1.020 V_{out} x 1.030	V
Line Regulation (I_{out} = 30 mA, V_{out} + 0.5 V ≤ V_{in} ≤ 6.0 V) FT Mode V_{ECO} = V_{in} LP Mode V_{ECO} = GND	Reg_{line}	 – –	 0.05 0.10	 0.20 0.30	%/V
Load Regulation FT Mode (1.0 mA ≤ I_{out} ≤ 200 mA), V_{ECO} = V_{in} LP Mode (1.0 mA ≤ I_{out} ≤ 100 mA), V_{ECO} = GND	Reg_{load}	 – –	 20 10	 40 40	mV
Dropout Voltage (I_{out} = 200 mA) V_{out} = 0.9 V 1.2 V ≤ V_{out} ≤ 1.5 V 1.8 V ≤ V_{out} ≤ 2.5 V 2.6 V ≤ V_{out} ≤ 3.3 V	V_{DO}	 – – – –	 0.40 0.30 0.20 0.10	 0.70 0.50 0.30 0.20	V
Quiescent Current (I_{out} = 0 mA) FT Mode, V_{ECO} = V_{in} LP Mode, V_{ECO} = GND V_{out} ≤ 1.5 V V_{out} ≥ 1.8 V	Iq	 – – –	 40 3.5 4.5	 70 6.0 8.0	μA
Output Current (V_{in} − V_{out} = 0.5 V) V_{in} ≥ 1.5 V, V_{out} = 0.9 V	I_{out}	 200	 –	 –	mA
Shutdown Current (V_{CE} = V_{in})	I_{SD}	–	0.1	1.0	μA
Output Short Circuit Current (V_{out} = 0 V)	I_{lim}	–	50	–	mA
Enable Input Threshold Voltage High Low	 Vth_{enh} Vth_{enl}	 1.0 0	 – –	 V_{in} 0.3	V
Ripple Rejection (I_{out} = 30 mA, V_{out} = 0.9 V, V_{in} − V_{out} = 1.0 V) f = 120 Hz f = 1.0 kHz f = 10 kHz	RR	 – – –	 75 75 65	 – – –	dB
Output Noise Voltage (BW = 10 Hz to 100 kHz	Vn	–	30	–	μVrms
Output Voltage Temperature Coefficient (I_{out} = 30 mA, −40°C ≤ T_A ≤ 85°C)	$\Delta V_{out}/\Delta T$	–	±100	–	ppm/°C

TYPICAL CHARACTERISTICS

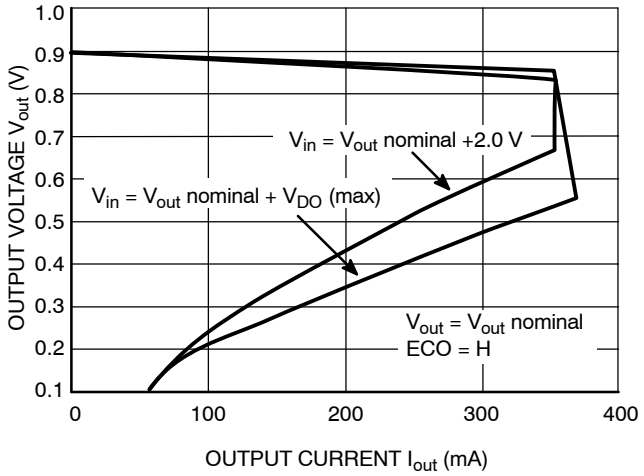


Figure 3. Output Voltage vs. Output Current

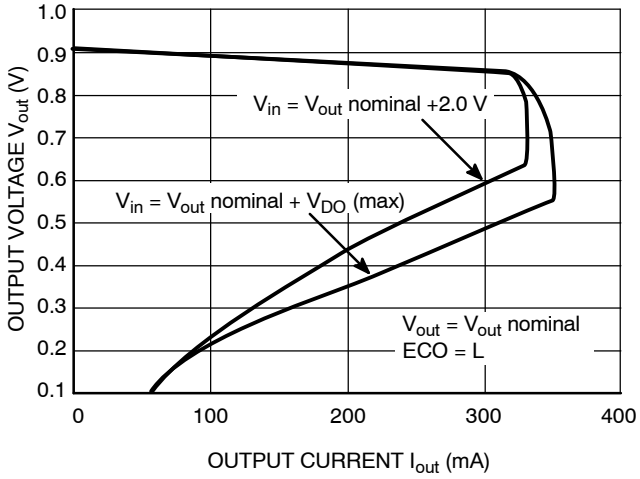


Figure 4. Output Voltage vs. Output Current

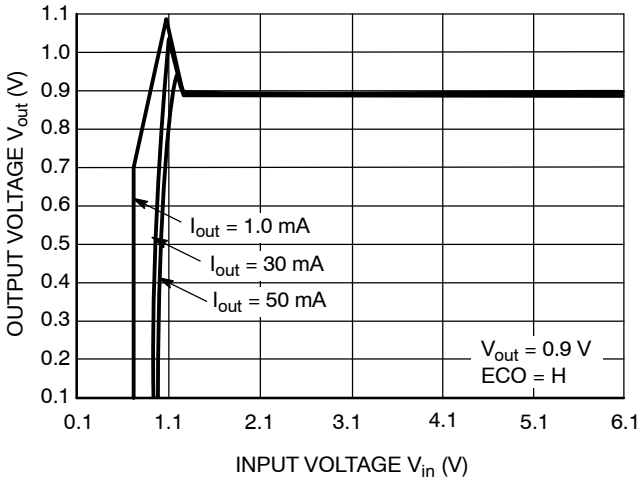


Figure 5. Output Voltage vs. Input Voltage

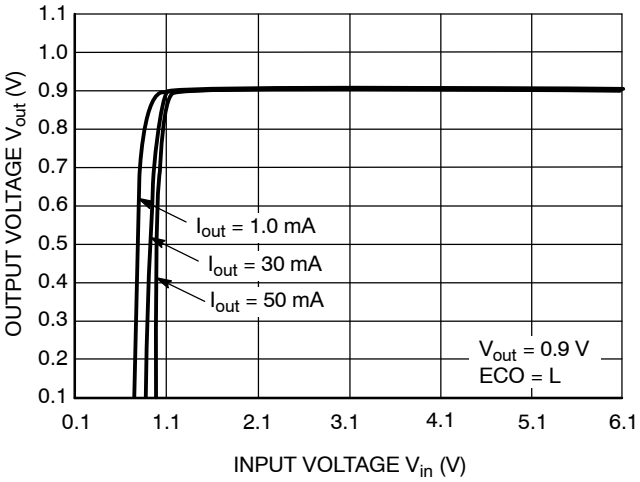


Figure 6. Output Voltage vs. Input Voltage

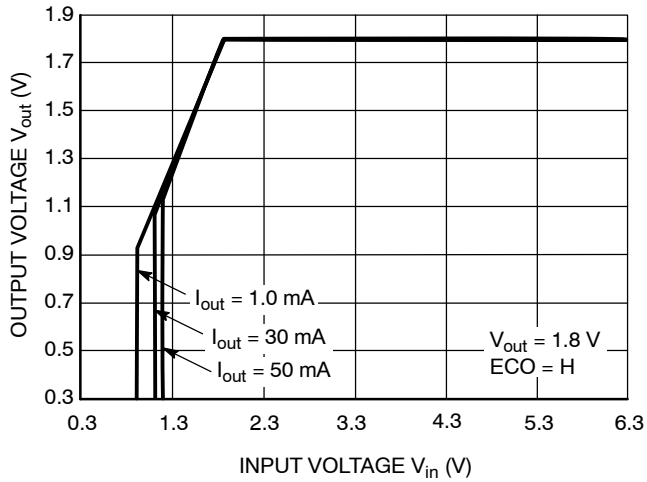


Figure 7. Output Voltage vs. Input Voltage

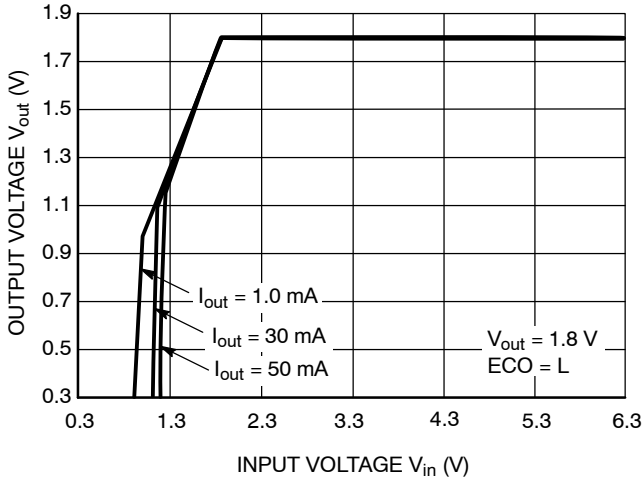


Figure 8. Output Voltage vs. Input Voltage

TYPICAL CHARACTERISTICS

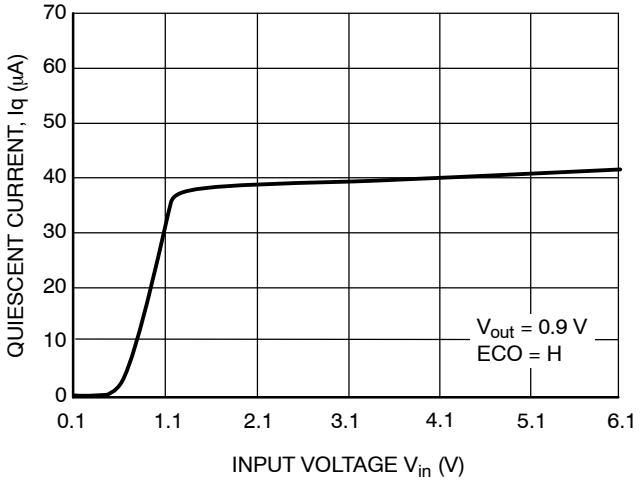


Figure 9. Quiescent Current vs. Input Voltage

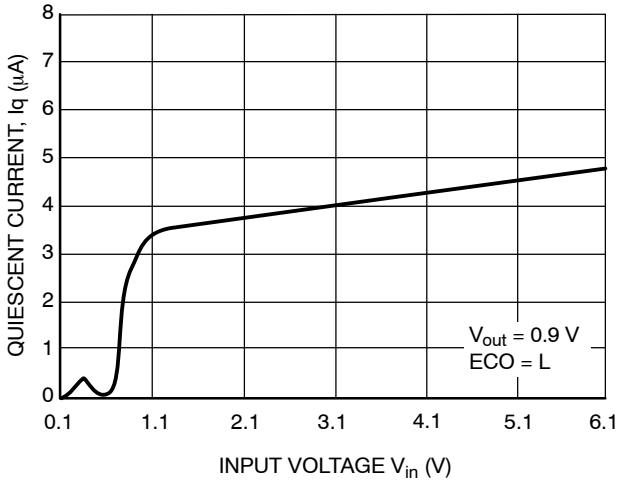


Figure 10. Quiescent Current vs. Input Voltage

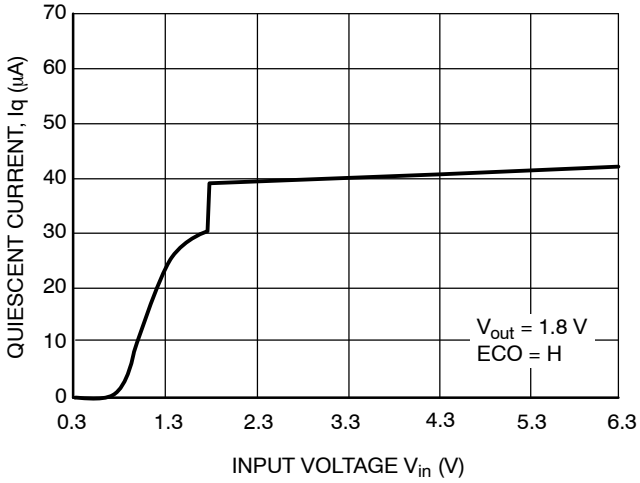


Figure 11. Quiescent Current vs. Input Voltage

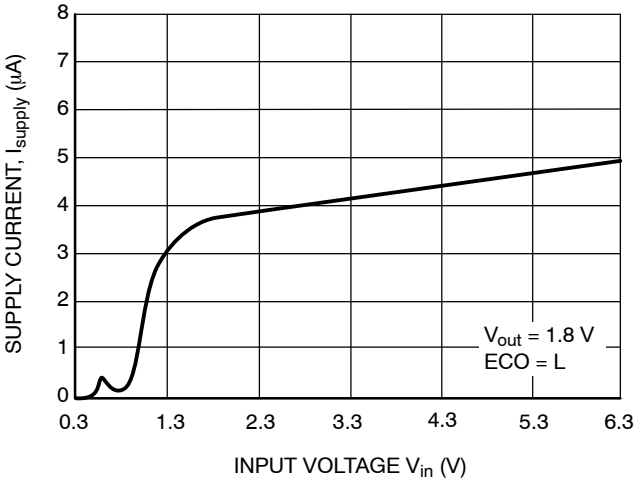


Figure 12. Quiescent Current vs. Input Voltage

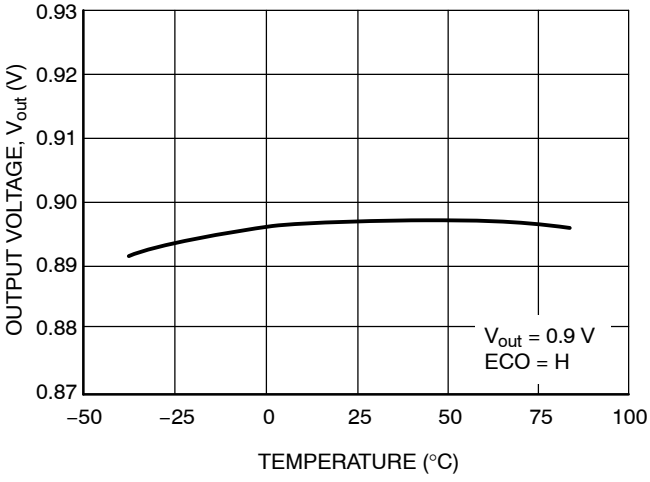


Figure 13. Output Voltage vs. Temperature

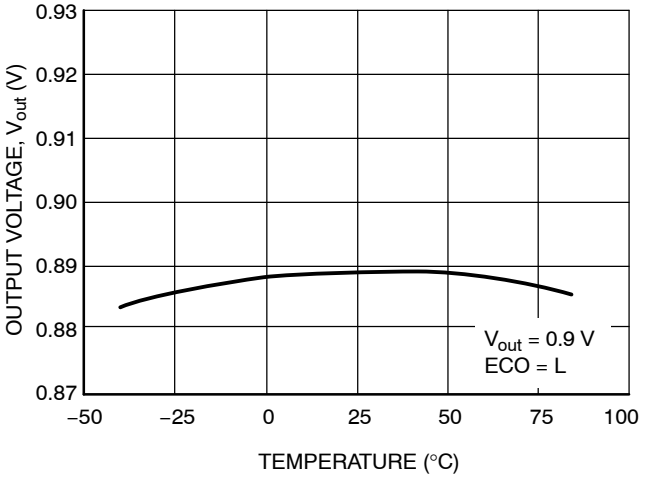


Figure 14. Output Voltage vs. Temperature

TYPICAL CHARACTERISTICS

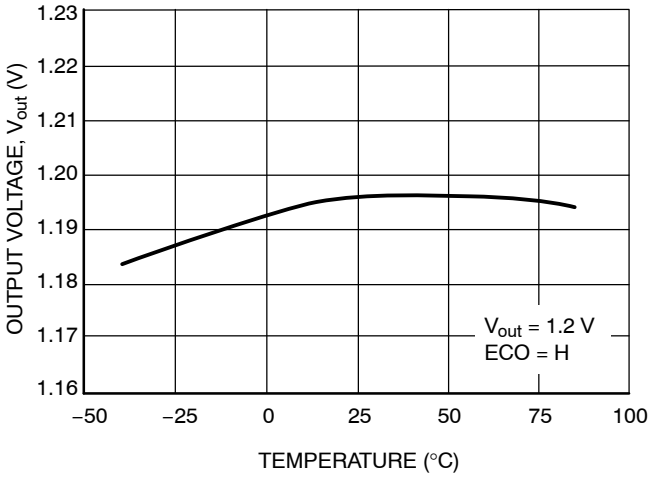


Figure 15. Output Voltage vs. Temperature

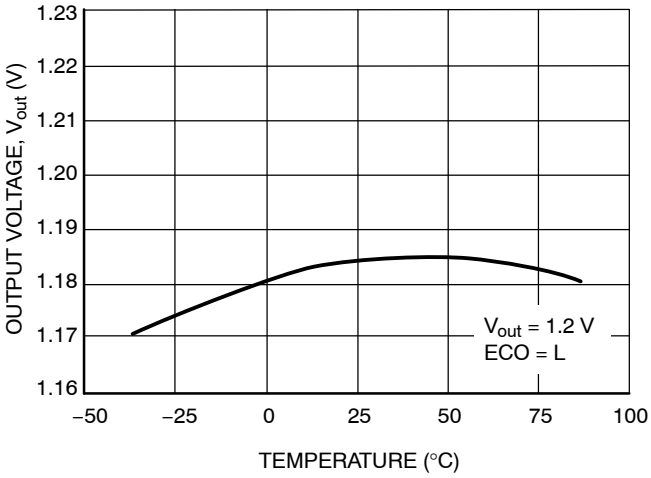


Figure 16. Output Voltage vs. Temperature

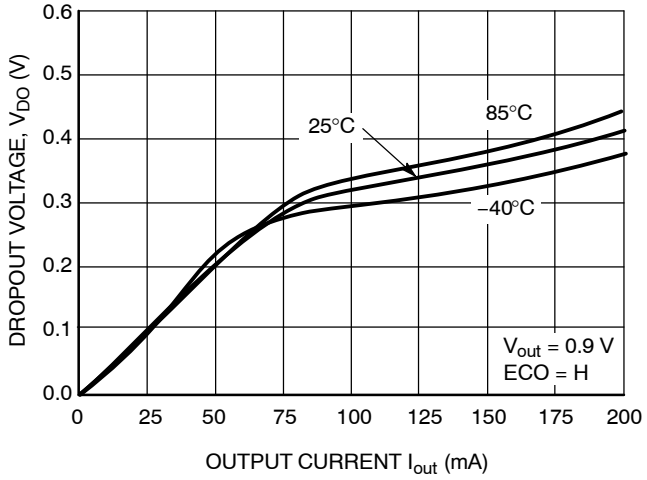


Figure 17. Dropout Voltage vs. Output Current

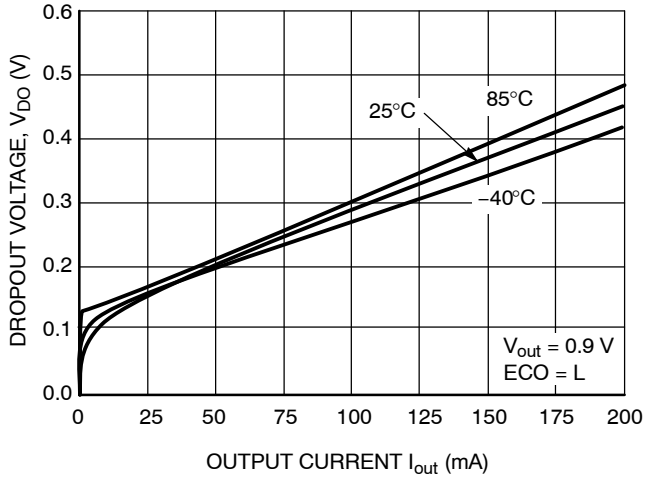


Figure 18. Dropout Voltage vs. Output Current

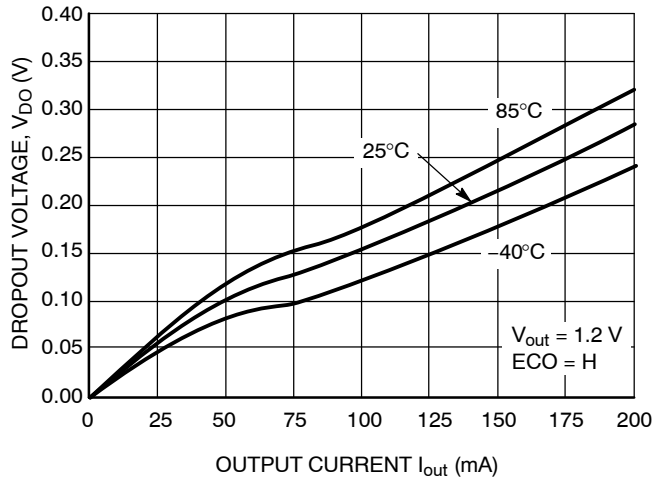


Figure 19. Dropout Voltage vs. Output Current

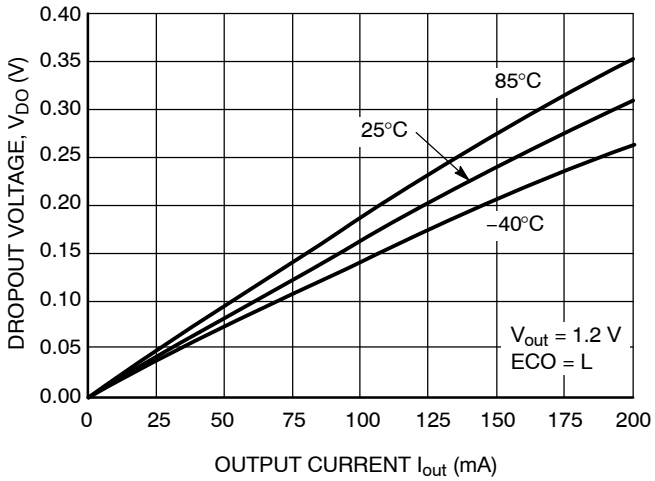


Figure 20. Dropout Voltage vs. Output Current

TYPICAL CHARACTERISTICS

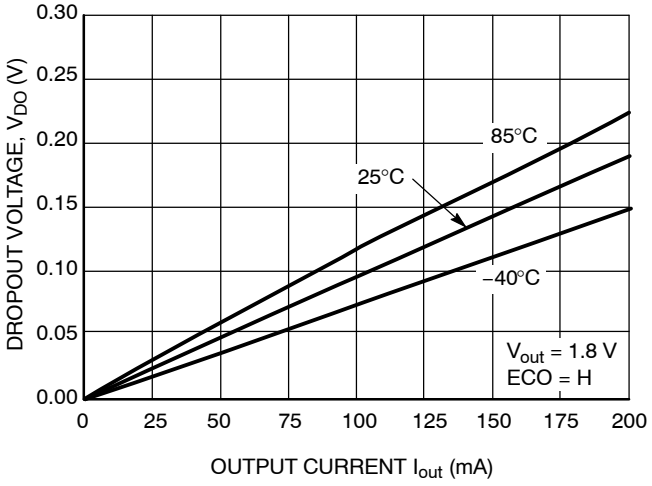


Figure 21. Dropout Voltage vs. Output Current

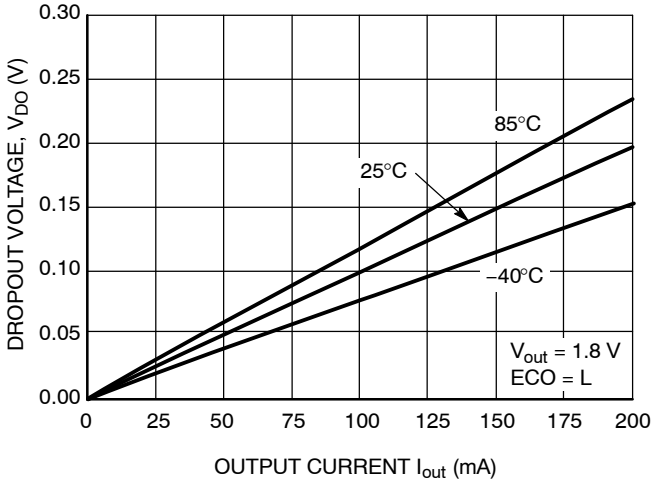


Figure 22. Dropout Voltage vs. Output Current

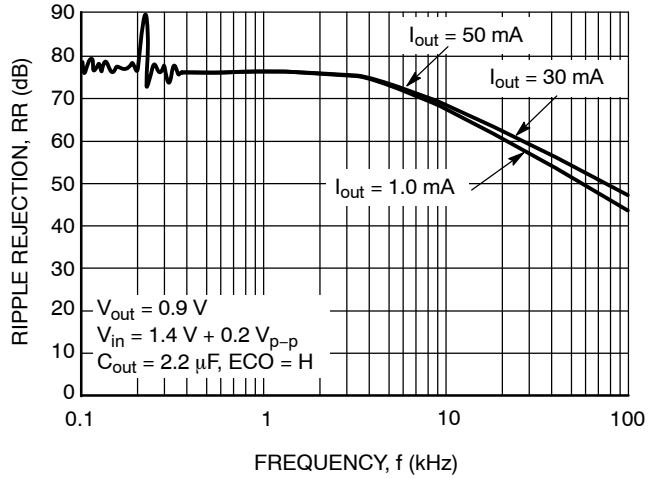


Figure 23. Ripple Rejection vs. Frequency

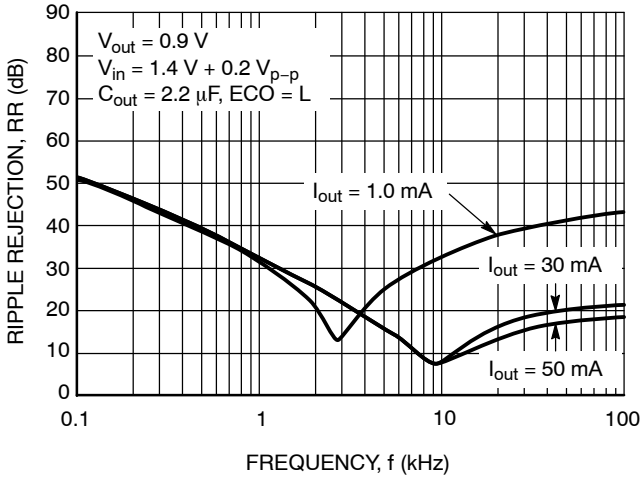


Figure 24. Ripple Rejection vs. Frequency

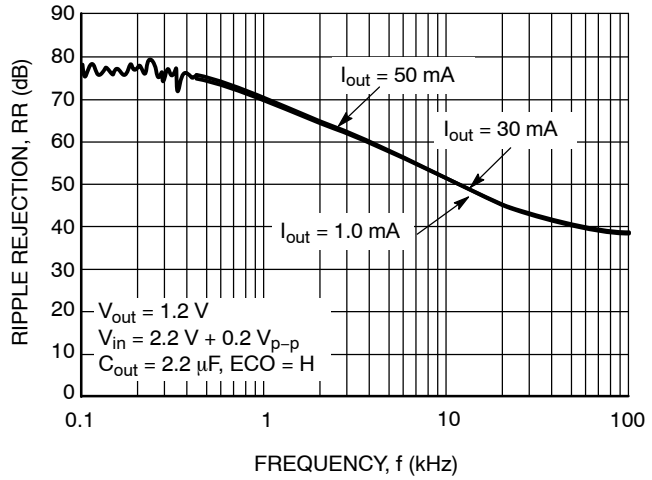


Figure 25. Ripple Rejection vs. Frequency

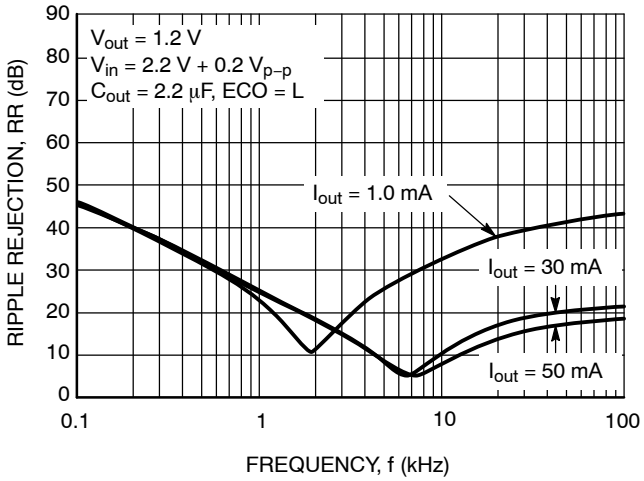


Figure 26. Ripple Rejection vs. Frequency

TYPICAL CHARACTERISTICS

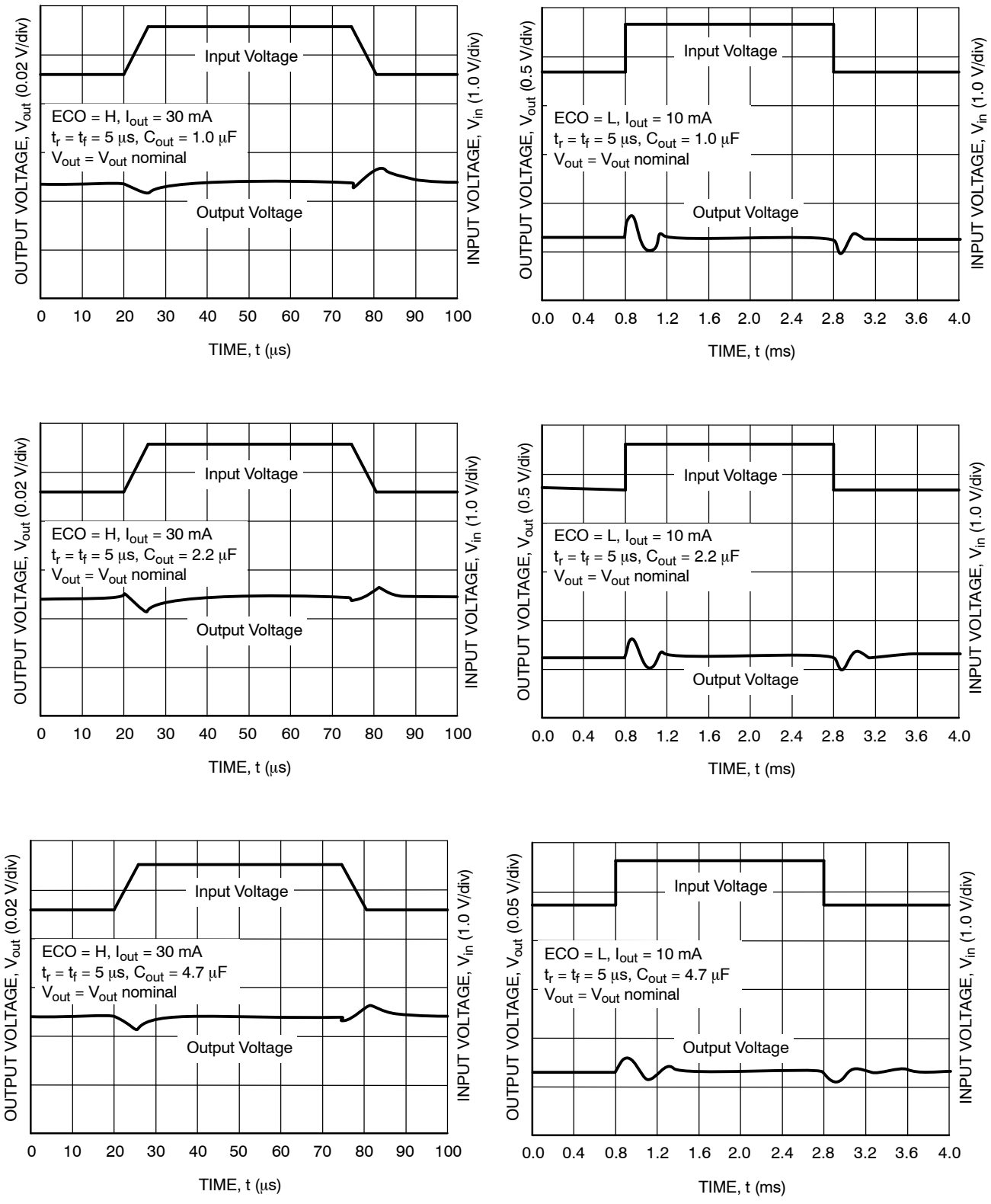


Figure 27. Input Transient Response

TYPICAL CHARACTERISTICS

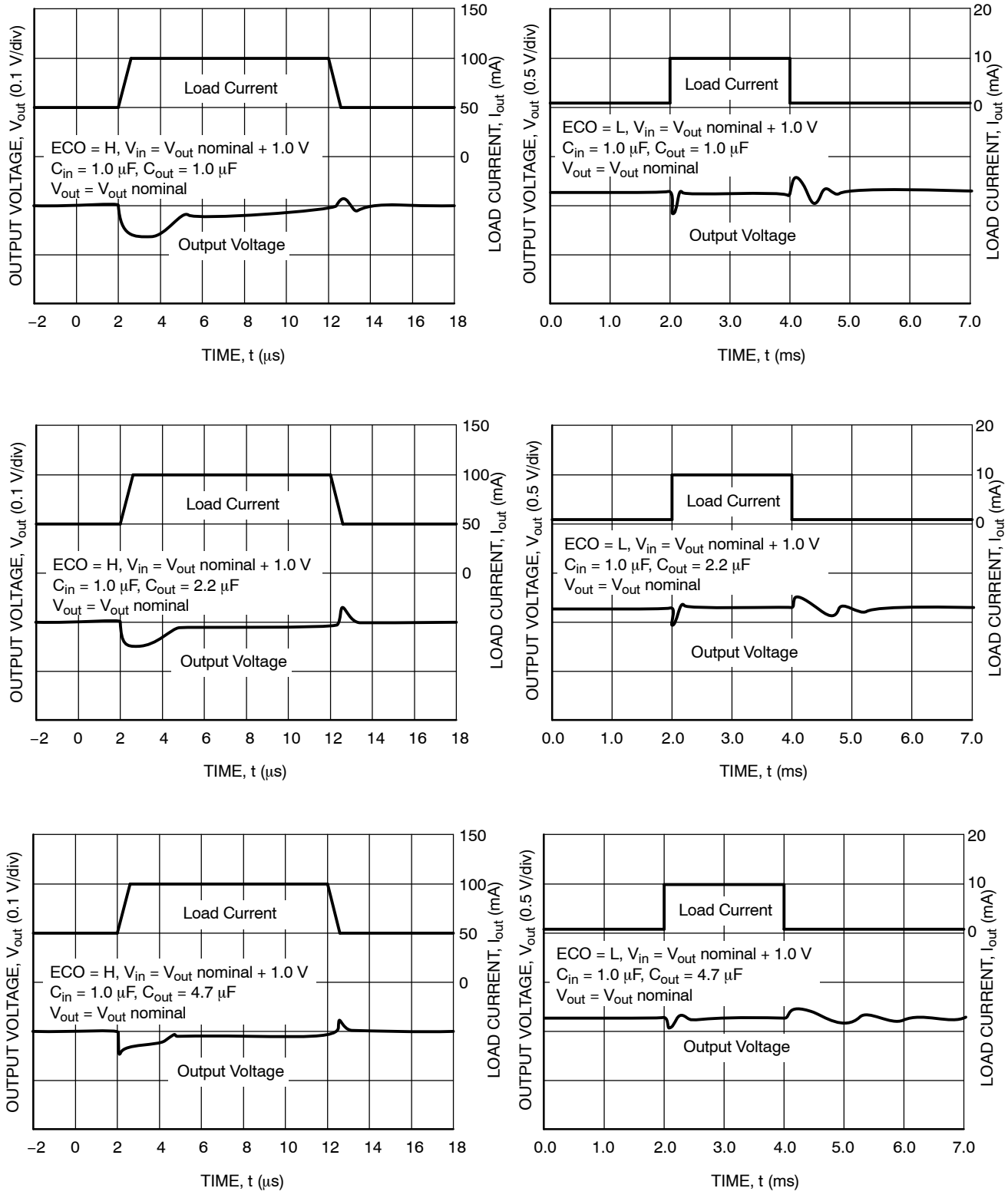


Figure 28. Load Transient Response

TYPICAL CHARACTERISTICS

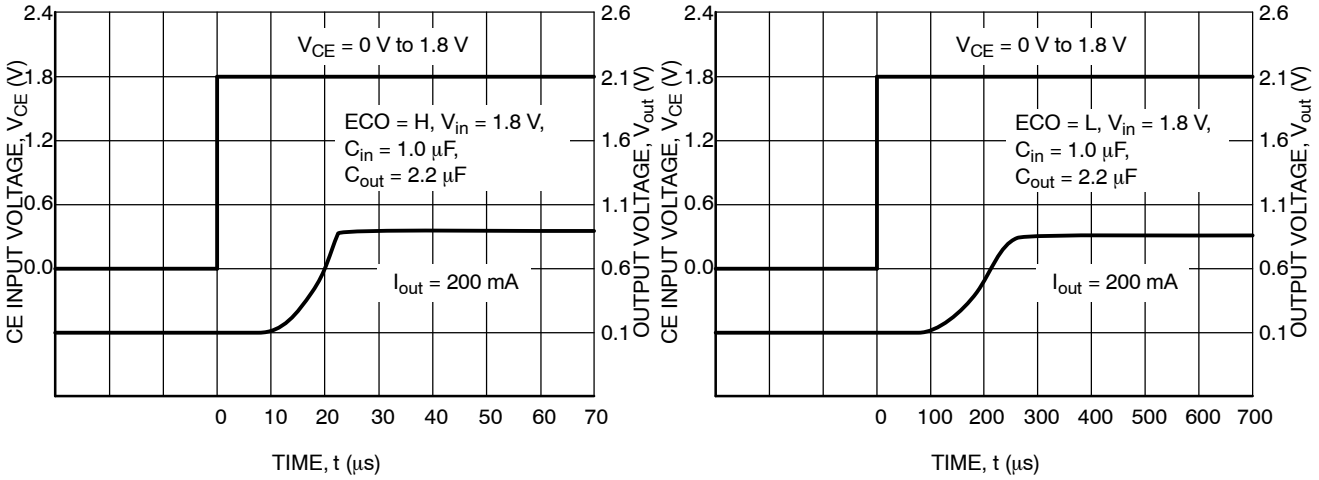


Figure 29. Turn−On/Off Speed with CE Pin (V_{out} = 0.9 V)

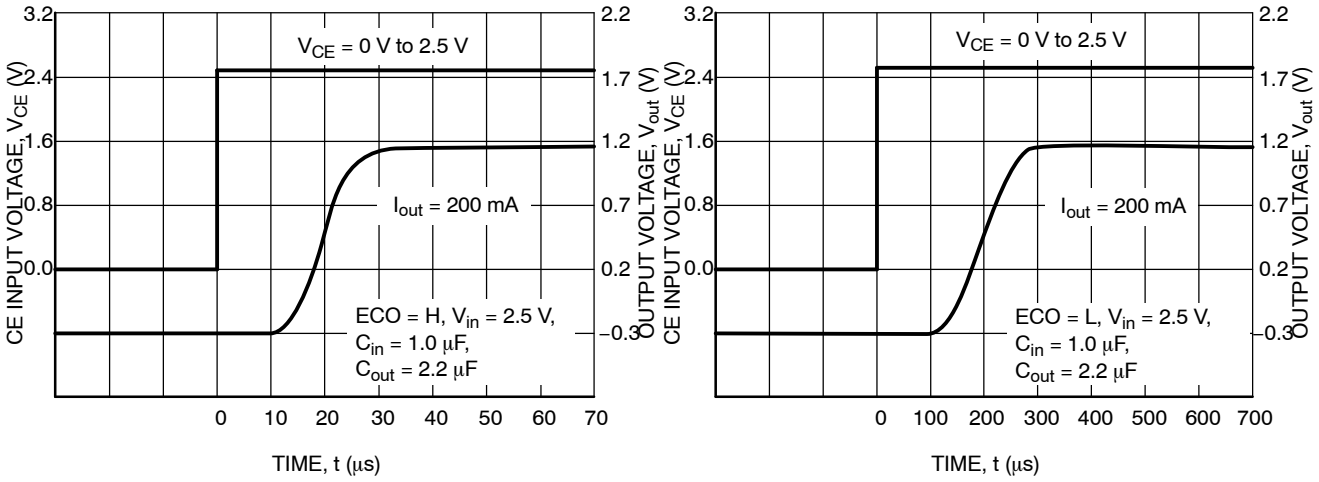


Figure 30. Turn−On/Off Speed with CE Pin (V_{out} = 1.2 V)

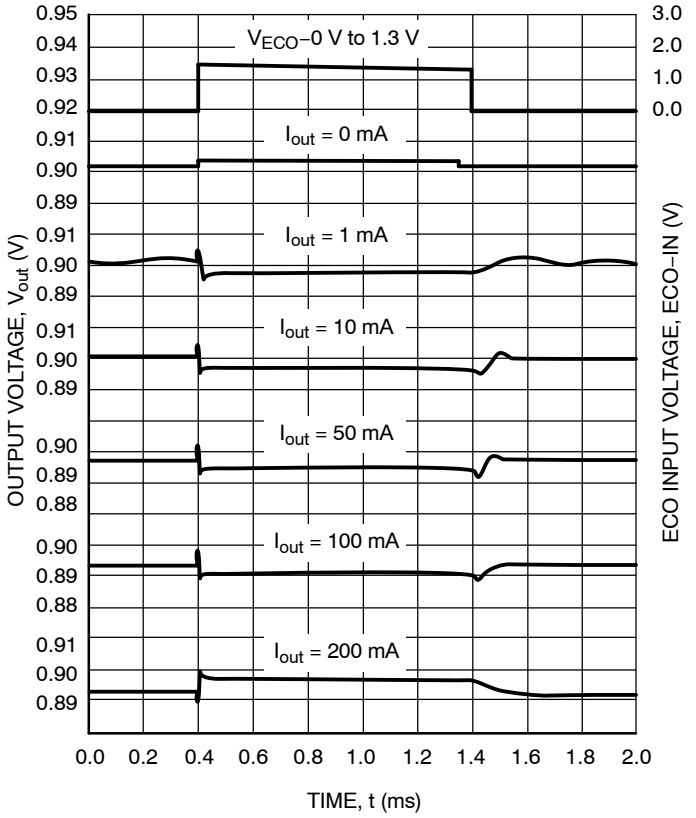


Figure 31. Output Voltage at Mode Alternative Point (C_{in} = 1.0 μF, C_{out} = 2.2 μF, 8.0 V, V_{out} = 0.9 V)

APPLICATION INFORMATION

Input Decoupling

A 1.0 μF tantalum capacitor is the recommended value to be connected between V_{in} and GND. For PCB layout considerations, the traces of V_{in} and GND should be sufficiently wide in order to minimize noise and prevent unstable operation.

Output Decoupling

It is recommended to use a 2.2 μF or higher tantalum capacitor on the V_{out} pin. For better performance, select a tantalum capacitor with low Equivalent Series Resistance (ESR). If you use a tantalum type capacitor with high ESR value of this capacitor, output might be unstable. For a ceramic type capacitor it is recommended connection about 1 Ω resistor in series for the stability of output voltage. The relation between Output Current of regulator and ESR of an output capacitor is shown in Figures 32 to 34. Those charts show minimal value of ESR for stable output voltage in Fast Transient mode. The minimal ESR of an output ceramic capacitor in Low Power mode is 40 mΩ for all output voltages. For PCB layout considerations, place the output capacitor close to the output pin and keep the leads short as possible.

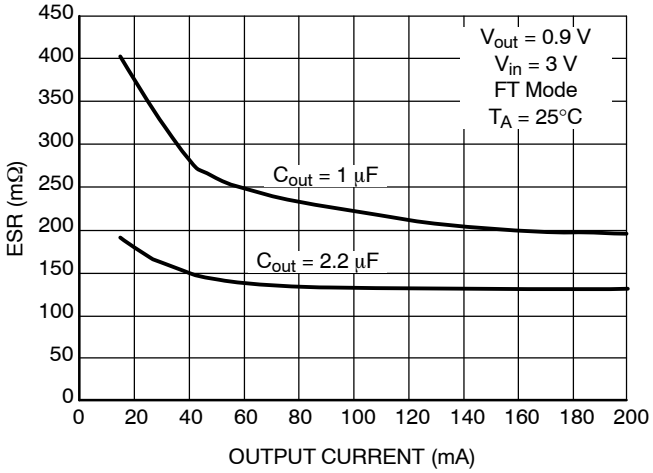


Figure 32. Minimal ESR of Output Ceramic Capacitor vs. Output Current

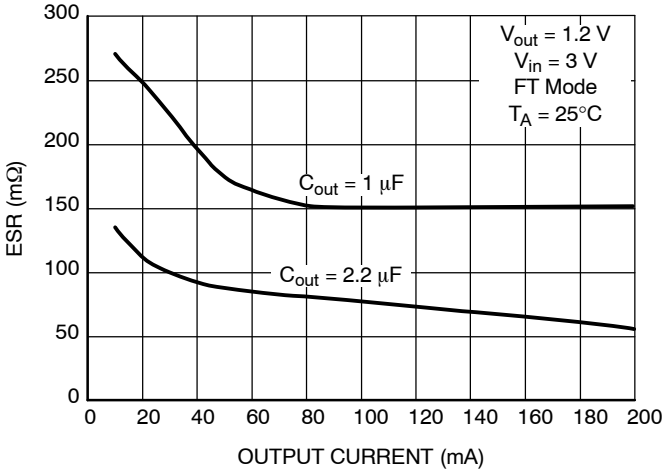


Figure 33. Minimal ESR of Output Ceramic Capacitor vs. Output Current

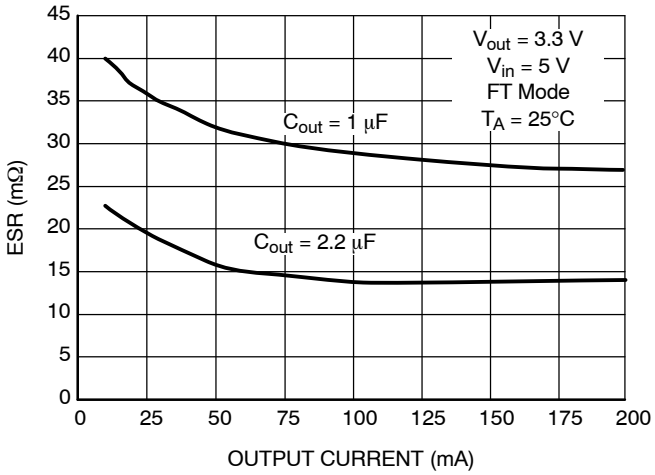


Figure 34. Minimal ESR of Output Ceramic Capacitor vs. Output Current

ORDERING INFORMATION

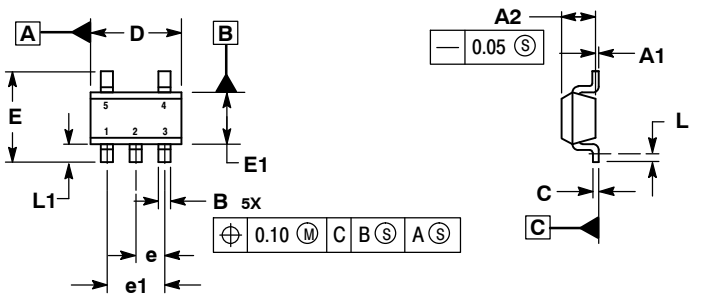
Device	Output Type / Features	Nominal Output Voltage	Marking	Package	Shipping†
NCP584HSN09T1G	Active High, LP and FT Mode	0.9	109	SOT23−5 (Pb−Free)	3000 / Tape & Reel
NCP584HSN12T1G	Active High, LP and FT Mode	1.2	112	SOT23−5 (Pb−Free)	3000 / Tape & Reel
NCP584HSN15T1G	Active High, LP and FT Mode	1.5	115	SOT23−5 (Pb−Free)	3000 / Tape & Reel
NCP584HSN18T1G	Active High, LP and FT Mode	1.8	118	SOT23−5 (Pb−Free)	3000 / Tape & Reel
NCP584HSN25T1G	Active High, LP and FT Mode	2.5	125	SOT23−5 (Pb−Free)	3000 / Tape & Reel
NCP584HSN26T1G	Active High, LP and FT Mode	2.6	126	SOT23−5 (Pb−Free)	3000 / Tape & Reel
NCP584HSN28T1G	Active High, LP and FT Mode	2.8	128	SOT23−5 (Pb−Free)	3000 / Tape & Reel
NCP584HSN30T1G	Active High, LP and FT Mode	3.0	130	SOT23−5 (Pb−Free)	3000 / Tape & Reel
NCP584HSN31T1G	Active High, LP and FT Mode	3.1	131	SOT23−5 (Pb−Free)	3000 / Tape & Reel
NCP584HSN33T1G	Active High, LP and FT Mode	3.3	133	SOT23−5 (Pb−Free)	3000 / Tape & Reel
NCP584LSN09T1G	Active Low, LP and FT Mode	0.9	009	SOT23−5 (Pb−Free)	3000 / Tape & Reel
NCP584LSN12T1G	Active Low, LP and FT Mode	1.2	012	SOT23−5 (Pb−Free)	3000 / Tape & Reel
NCP584LSN18T1G	Active Low, LP and FT Mode	1.8	018	SOT23−5 (Pb−Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

Other voltages are available. Consult your ON Semiconductor representative.

PACKAGE DIMENSIONS

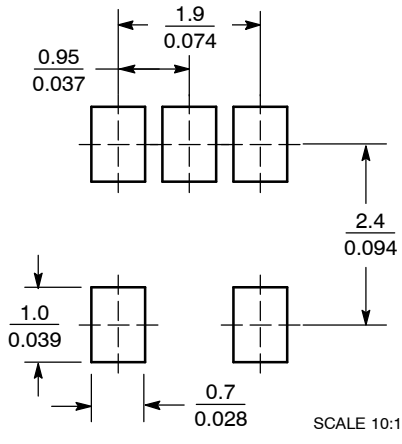
SOT23−5
SN SUFFIX
CASE 1212−01
ISSUE O



NOTES:
1. DIMENSIONS ARE IN MILLIMETERS.
2. INTERPRET DIMENSIONS AND TOLERANCES PER ASME Y14.5M, 1994.
3. DATUM C IS A SEATING PLANE.

	MILLIMETERS	
DIM	MIN	MAX
A1	0.00	0.10
A2	1.00	1.30
B	0.30	0.50
C	0.10	0.25
D	2.80	3.00
E	2.50	3.10
E1	1.50	1.80
e	0.95 BSC	
e1	1.90 BSC	
L	0.20	---
L1	0.45	0.75

SOLDERING FOOTPRINT*



*For additional information on our Pb−Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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