A product Line of
Diodes Incorporated

PI3USB103

USB 2.0 High-Speed (480 Mbps) Signal Switch with Vbus Short Protection

Features

- USB 2.0 compliant (high speed and full speed)
- R_{ON} is 3.0Ω typical @ V_{DD} = 3.0V
- Low bit-to-bit skew
- Low Crosstalk: –33dB @ 480 Mbps
- Off Isolation: –30dB @ 480 Mbps
- Near-Zero propagation delay: 250ps
- Channel On Capacitance: 6.0pF (typ)
- V_{DD} Operating Range: 3.0V to 3.6V
- Data pin I/O ESD (pins 1, 2, 4, 5, 6, and 7): ±4kV contact per IEC61000-4-2 spec
- -3dB BW of 1400MHz
- I/O pins have over-voltage protection and can tolerate a short to Vbus
- Packaging (Pb-free & Green):
  - 10-contact TQFN, 1.3mm x 1.6mm x 0.75mm (ZL10)

Description

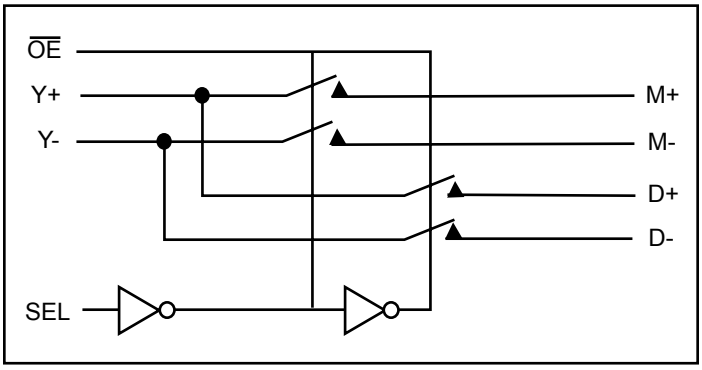
The PI3USB103 is a single differential channel 2:1 multiplexer/demultiplexer USB 2.0 Switch. Industry leading advantages include a propagation delay of less than 250ps, resulting from its low channel resistance and I/O capacitance. The device multiplexes differential outputs from a USB Host device to one of two corresponding outputs. The switch is bidirectional and offers little or no attenuation of the high-speed signals at the outputs. It is designed for low bit-to-bit skew, high channel-to-channel noise isolation and is compatible with various standards, such as High Speed USB 2.0 (480 Mb/s).

The PI3USB103 offers over voltage protection per the USB 2.0 specification. With the chip powered on or off, all I/O pins can withstand a short to Vbus. If V_{DD} = 0V, the I/Os can still have signals present, and the signal will NOT leak through to V_{DD}.

Application

- Routes signals for USB 2.0
- Hand-held devices

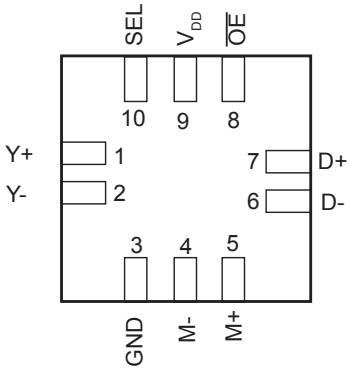
Block Diagram



Truth Table

SEL	$\overline{OE}$	Y+	Y-
X	H	Hi-Z	Hi-Z
L	L	M+	M-
H	L	D+	D-

Pin Description



Maximum Ratings

(Above which useful life may be impaired. For user guidelines, not tested.)

Storage Temperature .....–65°C to +150°C
Supply Voltage to Ground Potential .....–0.5V to +4.0V
DC Input Voltage .....–0.5V to +7V
DC Output Current 120mA
Power Dissipation 0.5W

Note: Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics for USB 2.0 Switching over Operating Range

(T_A = –40°C to +85°C, V_{DD} = 3.0 - 3.6V)

Parameter	Description	Test Conditions[1]		Min.	Typ.[2]	Max.	Units
V_{IH}	Input HIGH Voltage	Guaranteed HIGH level	V_{DD} = 3.3V	1.3		V_{DD}	V
V_{IL}	Input LOW Voltage	Guaranteed HIGH level		GND		0.6	
V_{IK}	Clamp Diode Voltage	V_{DD} = Max., I_{IK} = –18mA				-1.2	
I_{IH}	Input HIGH Current	V_{DD} = Max., V_{IH} = V_{DD}				±5	µA
I_{IL}	Input LOW Current	V_{DD} = Max., V_{IL} = GND				±5	
R_{ON}	Switch On-Resistance for USB 2.0 HS signal[3]	V_{DD} = Min., -0.4V ≤ V_{input} ≤ 1.0V, I_{ON} = –40mA			3.0	5.0	Ω
R_{ON}	Switch On-Resistance for USB FS signal	V_{DD} = min., 0V ≤ V_{input} ≤ 3.3V				9	
R_{ON}	Switch On-Resistance for MHL signal	V_{DD} = min. 2V ≤ V_{input} ≤ 3.3V				9	
$R_{ON(flat)}$	On-resistance flatness	V_{DD} = min. -0.4V ≤ V_{input} ≤ 1.0V			0.15	0.5	
ΔR_{ON}	On-Resistance match from center ports to any other port[3]	V_{DD} = Min., -0.4V ≤ V_{input} ≤ 0.4V, I_{ON} = -40mA			0.9	2.0	
I_{OZ}	I/O leakage current when port is off	V_{DD} = 3.6V, V_{input} = 0 to 3.6V, switch = off, $\overline{OE}$ = HIGH				±2	µA
I_{OFF}	Signal leakage when chip is off	V_{DD} = 0V, V_{input} = 0 to 7V				±10	µA

Notes:

1. For max. or min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
2. V_{DD} = 3.0 - 3.6V, T_A = 25°C ambient and maximum loading.
3. Measured by the voltage drop between D and D_n pin at indicated current through the Switch On-Resistance is determined by the lower of the voltages on the two (D, D_n) pins.

Power Supply Characteristics (V_{DD} = 3.0 - 3.6V)

Parameters	Description	Test Conditions[1]	Min.	Typ.	Max.	Units
I_{CC}	Power Supply Current	V_{DD} = Max., V_{SEL} = GND or V_{DD}, $\overline{OE}$ = LOW			75	µA
		V_{DD} = Max, V_{SEL} = 1.5V - 2.1V,			100	µA
I_{CCq}	Quiescent Power Supply Current	V_{DD} = 3.3V, V_{SEL} = GND or V_{DD},			6	µA
		V_{DD} = 3.3V, V_{SEL} = 1.5V - 2.1V,			8	µA

Notes:

1. For max. or min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.

Capacitance (T_A = 25°C, f = 1MHz)

Parameters[3]	Description	Test Conditions[1]	Typ.[2]	Max.	Units
C_{IN}	Input Capacitance	V_{SEL} = 0V	2.7	3.0	pF
C_{OFF}	Switch Capacitance, Switch OFF		2.8	3.0	
C_{ON}	Switch Capacitance, Switch ON		6.0	6.5	

Dynamic Electrical Characteristics Over the Operating Range

Parameters	Description	Test Conditions		Min.	Typ.[2]	Max.	Units
X_{TALK}	Crosstalk	R_L = 50Ω	f = 240 MHz f = 825 MHz	-35 -25	–39 –28		dB
O_{IRR}	OFF Isolation		f = 240 MHz f = 825 MHz	-29 -20	–32 –23		
BW	–3dB Bandwidth	R_L = 50Ω		1100	1400		MHz
BW	–0.5dB Bandwidth	R_L = 50Ω		150	400		MHz
IN	Insertion Loss	f = 240 MHz f = 825 MHz f = 1.0 GHz f = 1.125 GHz			0.47 1.8 2.2 2.4	0.67 2.4 2.6 3.0	dB

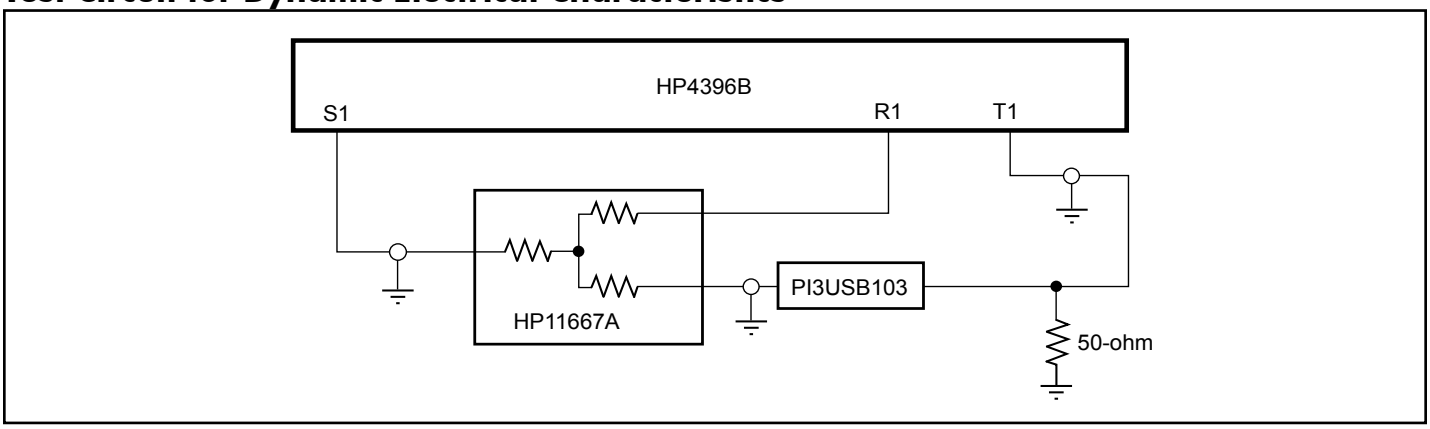
Notes:
1. For max. or min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at V_{DD} = 3.3V, T_A = 25°C ambient and maximum loading.
3. This parameter is determined by device characterization but is not production tested.

Switching Characteristics

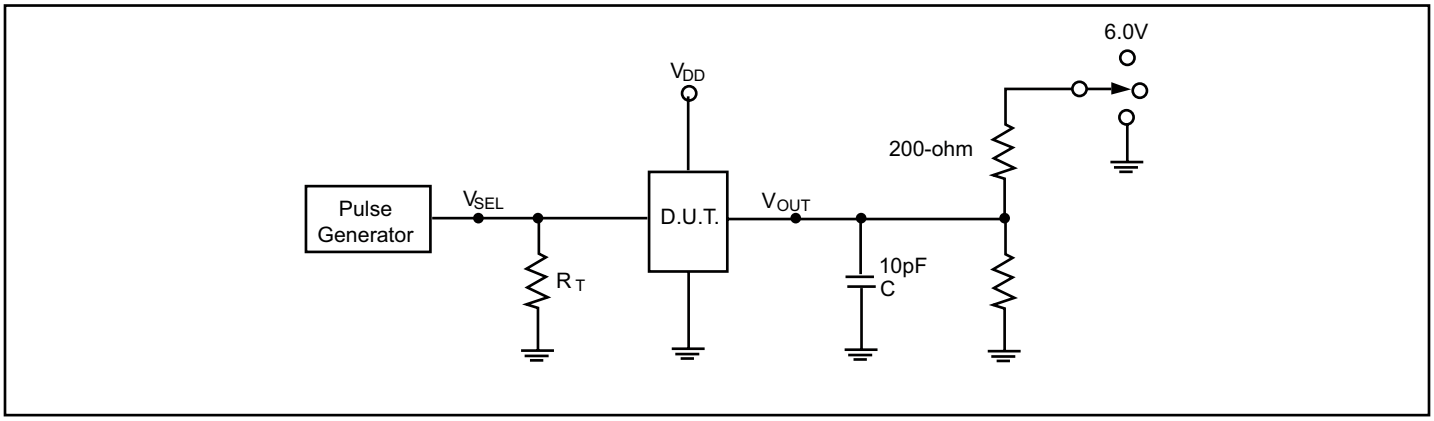
Parameters	Description	Test Conditions [1]	Min.	Typ.	Max.	Units
t_{PD}	Propagation Delay[2,3]	See Test Circuit for Electrical Characteristics		0.25	0.35	ns
t_{PZH}, t_{PZL}	Line Enable Time - SEL, $\overline{OE}$ to D(+/-), M(+/-)		0.5		60	
t_{PHZ}, t_{PLZ}	Line Disable Time - SEL, $\overline{OE}$ to D(+/-), M(+/-)		0.5		60	
t_{SKC-C}	Output skew, channel-to-channel[2]			3.5	14	ps

Notes:
1. For max. or min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Guaranteed by design.
3. The switch contributes no propagational delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 10pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the switch when used in a system is determined by the driving circuit on the driving side of the switch and its interactions with the load on the driven side.

Test Circuit for Dynamic Electrical Characteristics



Test Circuit for Electrical Characteristics



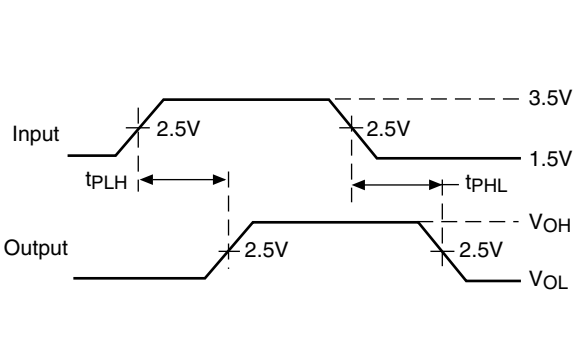
Notes:

C_L = Load capacitance: includes jig and probe capacitance.
R_T = Termination resistance: should be equal to Z_{OUT} of the Pulse Generator
All input impulses are supplied by generators having the following characteristics: PRR ≤ MHz, $Z_O = 50\Omega$, $t_R \leq 2.5$ns, $t_F \leq 2.5$ns.
The outputs are measured one at a time with on transition per measurement.

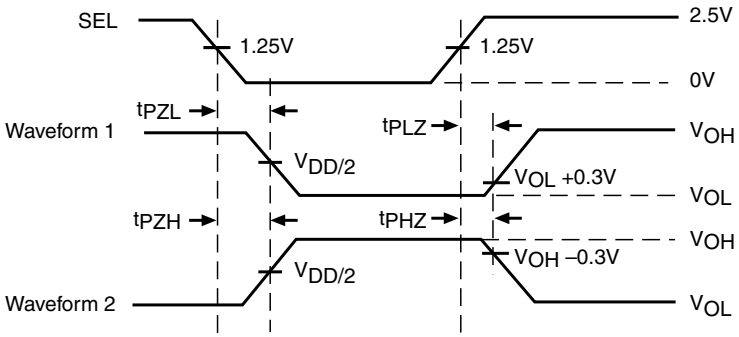
Switch Positions

Test	Switch
t_{PLZ}, t_{PZL}	6.0V
t_{PHZ}, t_{PZH}	GND
Prop Delay	Open

Switching Waveforms



Voltage Waveforms Propagation Delay Times



Voltage Waveforms Enable and Disable Times

Applications Information

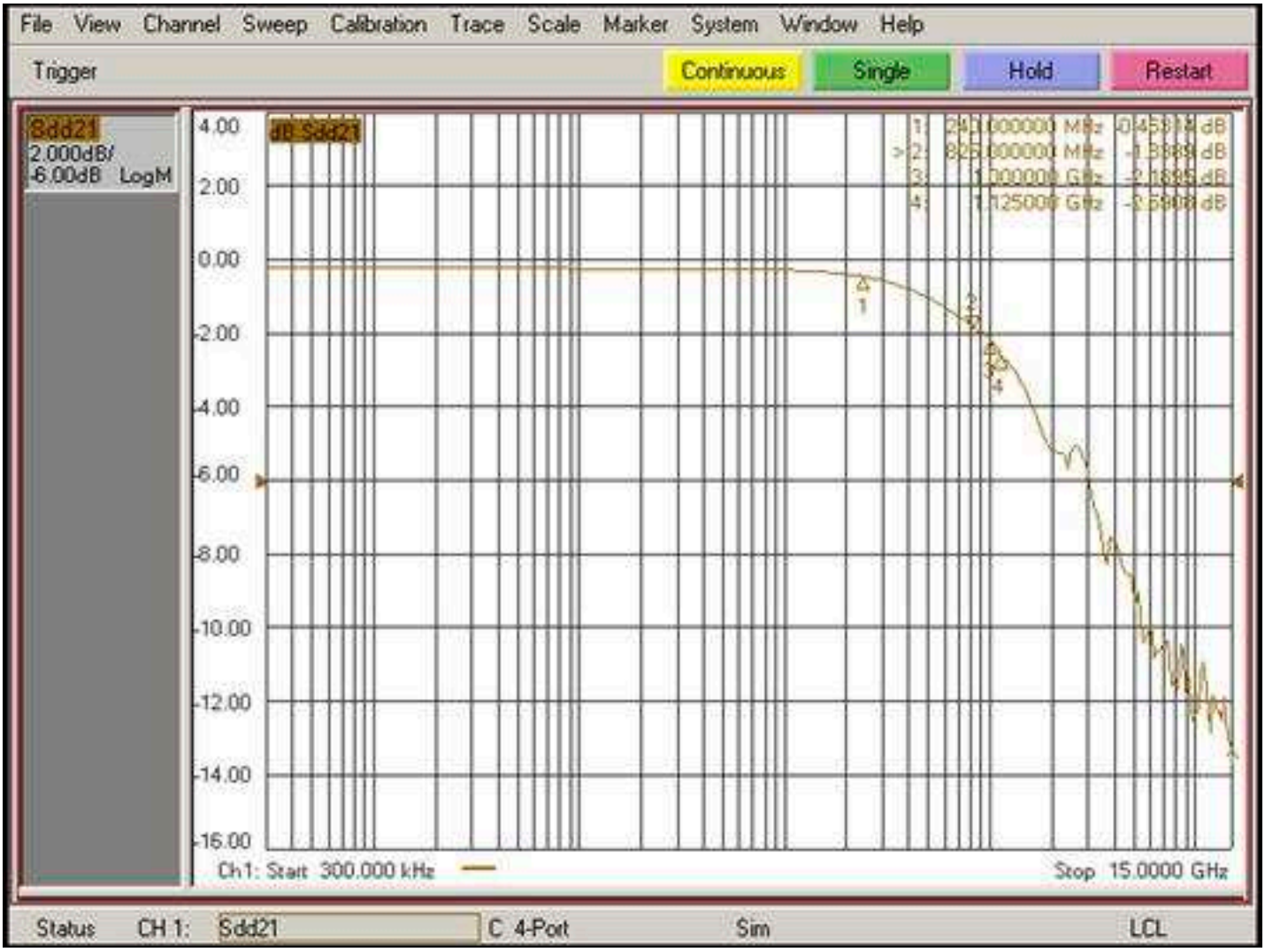
Logic Inputs

The logic control inputs can be driven up to +3.6V regardless of the supply voltage. For example, given a +3.3V supply, the output enables or select pins may be driven low to 0V and high to 3.6V. Driving IN Rail-to-Rail® minimizes power consumption.

Power Supply Sequencing

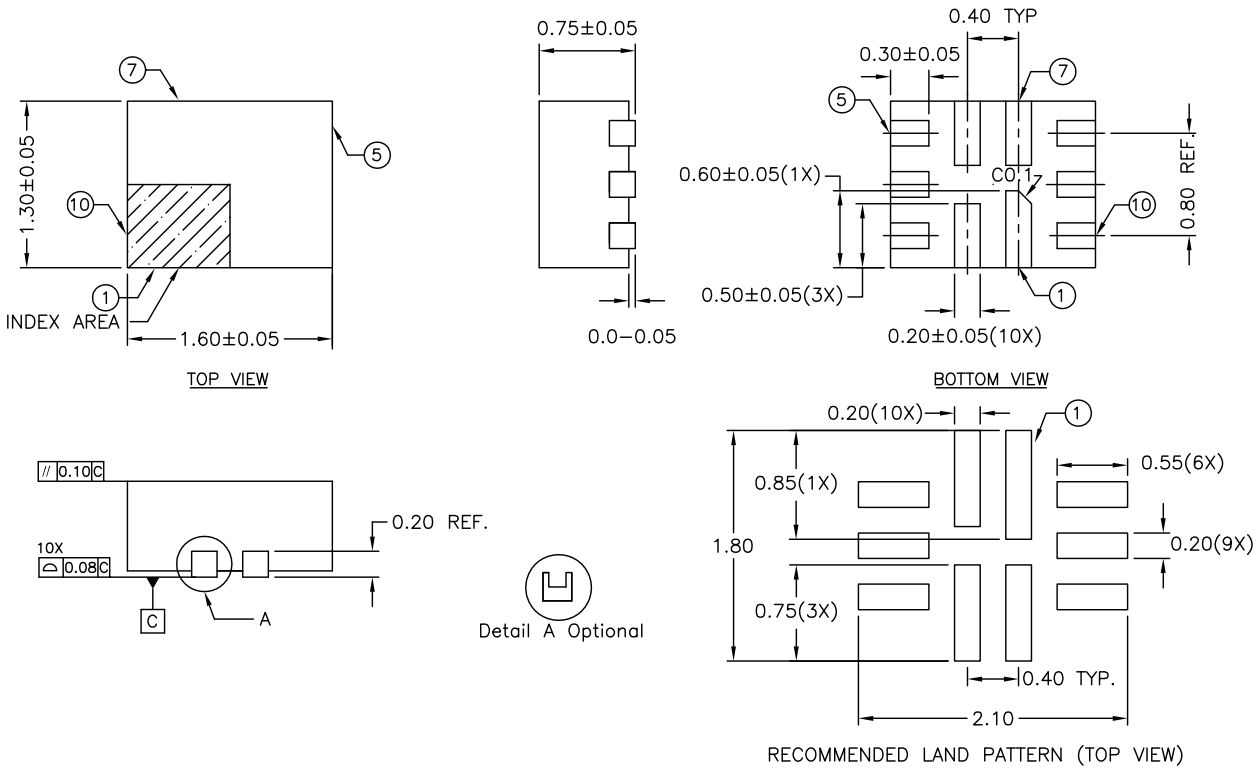
Since this product does not contain any PMOS parasitic at the I/O pins, proper supply sequencing is not required.

Rail-to-Rail is a registered trademark of Nippon Motorola, Ltd.



Differential Insertion loss, V_{DD} = 3.3V

Packaging Mechanicals: 10-Contact TQFN (ZL10)



NOTE :

1. ALL DIMENSIONS ARE IN MILLIMETERS.
2. REFER MO−220.
3. RECOMMENDED LAND PATTERN IS FOR REFERENCE ONLY.

PERICOM Semiconductor Corporation — DATE: 08/30/13

DESCRIPTION: 10-Contact, Thin Fine Pitch Quad Flat No-Lead (TQFN)

PACKAGE CODE: ZL (ZL10)

DOCUMENT CONTROL #: PD-2052 — REVISION: E

13-0175

Note: For latest package info, please check: http://www.pericom.com/support/packaging/packaging-mechanicals-and-thermal-characteristics/

Ordering Information

Ordering Code	Package Code	Package Description	Top Mark
PI3USB103ZLEX	ZL	10-contact, Thin Fine Pitch Quad Flat No-Lead (TQFN), Tape & Reel	CW

Notes:

- Thermal characteristics can be found on the company web site at www.pericom.com/packaging/
- E = Pb-free and Green
- Adding X suffix = Tape/Reel

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