LOW NOISE, HIGH IP3

Monolithic Amplifier PSA-5453+

50Ω 0.05 to 4 GHz

THE BIG DEAL

- Single Positive Supply Voltage, +3 V, I_D = 60 mA
- Ultra Low Noise Figure, 0.7 dB Typ. at 0.5 GHz
- High IP3, +35 dBm Typ. 1 GHz
- Gain, 19.6 dB Typ. at 1 GHz
- Output Power, Up to +19.5 dBm Typ.
- Micro-Miniature Size SOT-363 Package
- Aqueous Washable



Generic photo used for illustration purposes only

CASE STYLE: CA1389

+RoHS Compliant
The +Suffix identifies RoHS Compliance.
See our website for methodologies and qualifications

APPLICATIONS

- Cellular
- ISM
- GSM
- WCDMA
- LTE
- WiMAX
- WLAN
- UNII and HIPERLAN

PRODUCT OVERVIEW

Mini-Circuits PSA-5453+ is a E-pHEMT* based Ultra-Low Noise MMIC Amplifier operating from 50 MHz to 4 GHz with a unique combination of low noise and high IP3 making this amplifier ideal for sensitive receiver applications. This design operates on a single +3 V supply at only 60 mA and is internally matched to 50Ω.

KEY FEATURES

Feature	Advantages
Ultra Low Noise, 0.7 dB	Outstanding Noise Figure, measured in a 50Ω environment without any external matching.
High IP3, +35 dBm	Combining Low Noise and High IP3 makes this MMIC amplifier ideal for Low Noise Receiver Front End (RFE) because it gives the user advantages at both ends of the dynamic range: sensitivity & two-tone spur-free dynamic range.
Low Current, 60 mA	At only 60 mA, the PSA-5453+ is ideal for remote applications with limited available power or densely packed applications where thermal management is critical.
Broadband	Operating over a broadband, the PSA-5453+ covers the primary wireless communications bands: Cellular, PCS, LTE, WiMAX.
Internally Matched	No external matching elements required to achieve the advertised noise and output power over the full band.
SOT-363 Package	Small size, industry standard package.
Max Input Power, +15 dBm	Ruggedized design operates up to input powers of +15 dBm without the need of an external limiter.
High Reliability	Low, small signal operating current of 30 mA nominal maintains junction temperatures typically below +115°C at +85°C ground lead temperature.

* Enhancement mode pseudomorphic High Electron Mobility Transistor.

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ELECTRICAL SPECIFICATIONS[1] AT +25°C, Zo = 50Ω, (REFER TO CHARACTERIZATION CIRCUIT, FIG. 1)

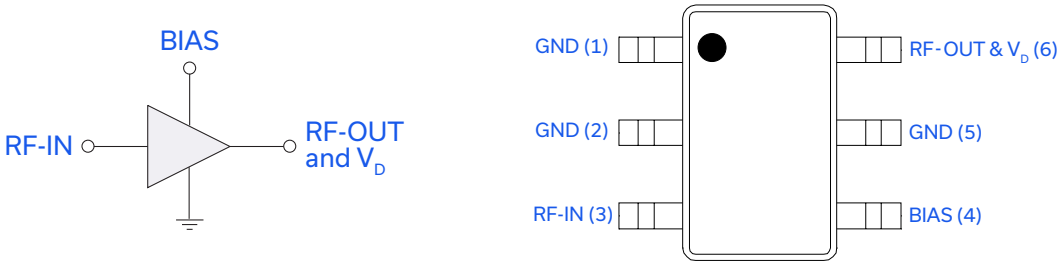
Parameter	Condition (GHz)	Min.	Typ.	Max.	Units
Frequency Range		0.05		4.0	GHz
DC Voltage (V_D)			+3.0		V
DC Current (I_D)[2]		40	60	71	mA
DC Current (I_{Rbias})			4.0		mA
Noise Figure	0.05 0.5 1.0 2.0 3.0 4.0		2.0 0.7 0.8 1.0 1.3 1.6	1.3	dB
Gain	0.05 0.5 1.0 2.0 3.0 4.0	13.2	24.4 23.3 19.6 14.7 11.9 10.2	16.2	dB
Input Return Loss	0.05-0.5 0.5-4.0		10.0 7.0		dB
Output Return Loss	0.05-0.5 0.5-4.0		10.0 20.0		dB
Output IP3	0.05 0.5 1.0 2.0 3.0 4.0		+32.1 +33.8 +35.2 +36.8 +37.1 +37.3		dBm
Output Power @ 1 dB Compression (P1dB)[3]	0.05 0.5 1.0 2.0 3.0 4.0		+19.0 +19.3 +19.4 +19.8 +19.9 +20.1		dBm
DC Current Variation vs. Temperature[4]			-0.066		mA/°C
Thermal Resistance			165		°C/W

ABSOLUTE MAXIMUM RATINGS[5]

Parameter	Ratings
Operating Temperature[6]	-40°C to +85°C
Storage Temperature	-65°C to +150°C
Channel Temperature	+150°C
DC Voltage (Pin 6)	+5 V
Power Dissipation	390 mW
DC Current (Pin 6)	130 mA
Bias Current (Pin 4)	10 mA
Input Power (CW)[7]	+15 dBm

1. Measured on Mini-Circuits Characterization test board TB-534-3+. See Characterization Test Circuit (Fig. 1).
2. Specified DC current consumption is under small signal conditions. Current will increase with input RF Power. To maintain maximum current consumption, external DC current limiting circuits are required on V_D line.
3. Specified with external current limiting of 70 mA. Capable of higher P1dB at higher currents (see Fig. 2).
4. Current at +85°C - Current at -45°C)/130.
5. Permanent damage may occur if any of these limits are exceeded. These maximum ratings are not intended for continuous normal operation.
6. Defined with reference to ground pad temperature.
7. Maximum input power is specified based upon external V_D current limiting of 80 mA. Maximum input power will degrade without external current limiting.

SIMPLIFIED SCHEMATIC AND PIN DESCRIPTION



Function	Pin Number	Description (See Application Circuit, Fig. 3)
RF-IN	3	RF input pin (connect to RF-IN via blocking cap C1 and Pin 4 via L2)
RF-OUT & V_D	6	RF output pin (connected to RF-out via blocking cap C2 and supply voltage V_D via RF Choke L1)
BIAS	4	Connected to V_S via Rbias. (Connect to ground via C4 & R1)
GND	1,2,5	Connections to ground.

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CHARACTERIZATION TEST CIRCUIT

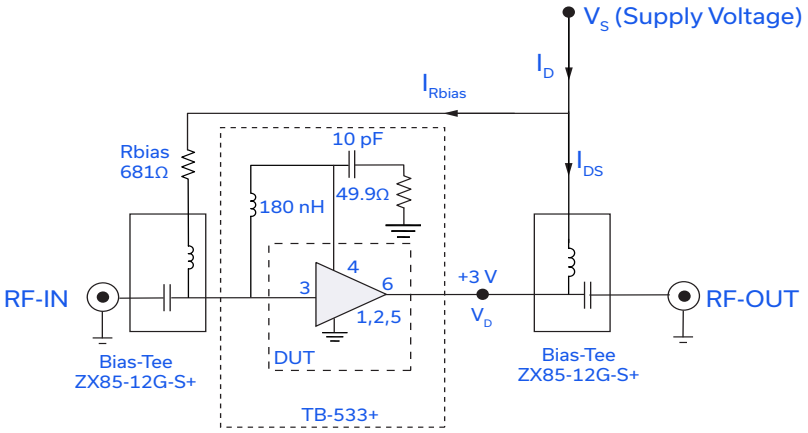
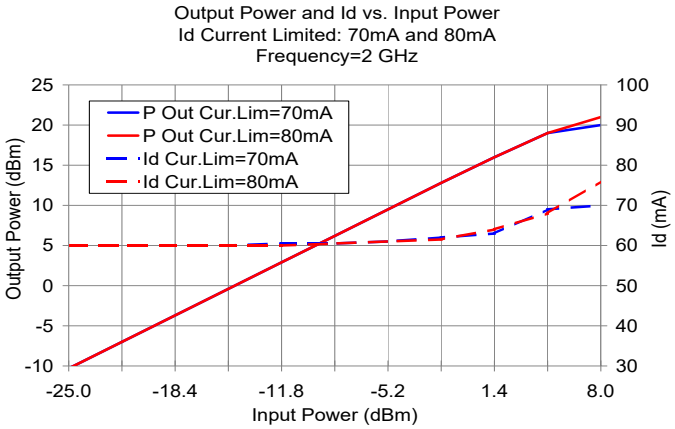


Fig 1. Block Diagram of Test Circuit used for characterization. (DUT soldered on Mini-Circuits Characterization Test Board TB-534-3+) Gain, Output Power at 1 dB Compression (P1dB), Output IP3 (OIP3) and Noise Figure measured using Agilent's N5242A PNA-X microwave network analyzer.

Conditions:
1. Gain: P_{IN} = -25 dBm
2. Output IP3 (OIP3): Two tones, spaced 1 MHz apart, 0 dBm/tone at output.
3. V_S adjusted for +3 V at device (V_D), compensating loss of bias tee.



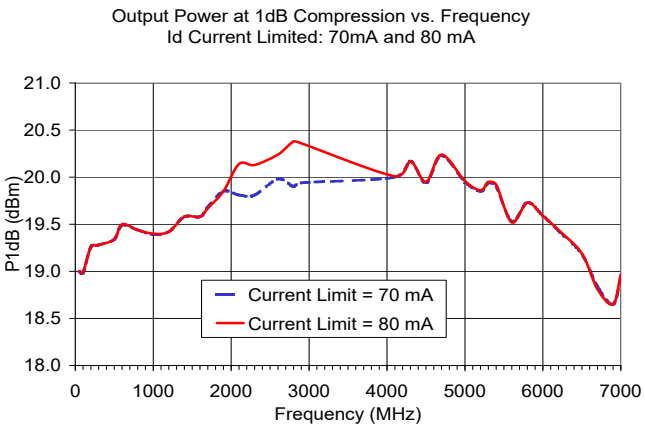


Fig 2. Output Power and I_D vs. Input Power and Frequency.

Performance measured on Mini-Circuits Characterization test board TB-534-3+. See Characterization Test Circuit (Fig. 1).
Measurements performed with current (I_D) limited as noted.

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RECOMMENDED APPLICATION CIRCUIT

(Refer to evaluation board for PCB Layout and component values)

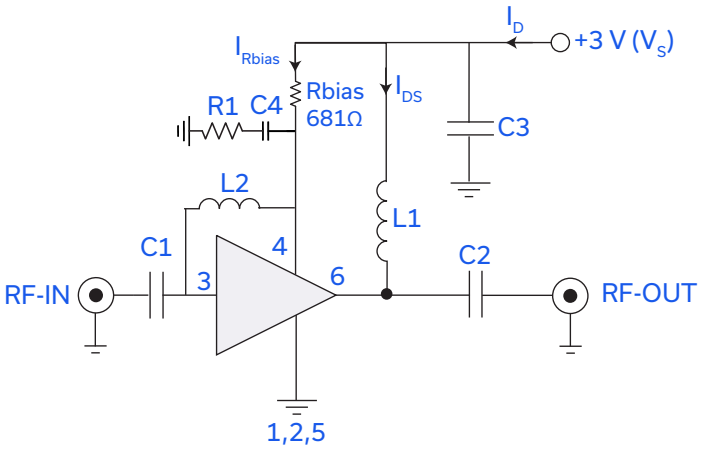


Fig 3. Recommended Application Circuit
Note: Resistance of L1, 0.1-0.2Ω typically

TYPICAL CURRENT (I_D) AS A FUNCTION OF RBIAS (V_S = +3 V)

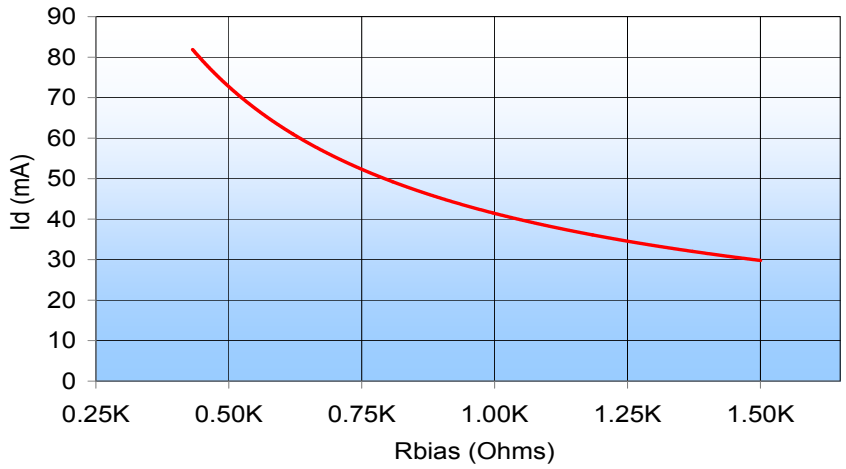
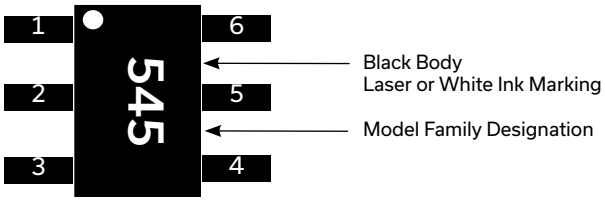


Fig 4. I_D varies as a function of Rbias. The I_D current range is defined based upon the specific Rbias value noted in the Application Circuit (Fig 3). Rbias may be adjusted to optimize I_D for a customers' application. RF performance will vary accordingly.

PRODUCT MARKING



Marking may contain other features or characters for internal lot control.

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ADDITIONAL DETAILED TECHNICAL INFORMATION IS AVAILABLE ON OUR DASHBOARD. TO ACCESS CLICK HERE

Performance Data	Data Table Swept Graphs S-Parameter (S2P Files) Data Set (.zip file)
Case Style	CA1389 Plastic molded SOT-363 package, Lead Finish: Matte-Tin
Tape & Reel **Standard Quantities Available on Reel**	F101 7" Reels with 20, 50, 100, 200, 500, 1000, or 2000 devices
Suggested Layout for PCB Design	PL-311
Evaluation Board	TB-534-3+
Environmental Ratings	ENV08T2

ESD RATING

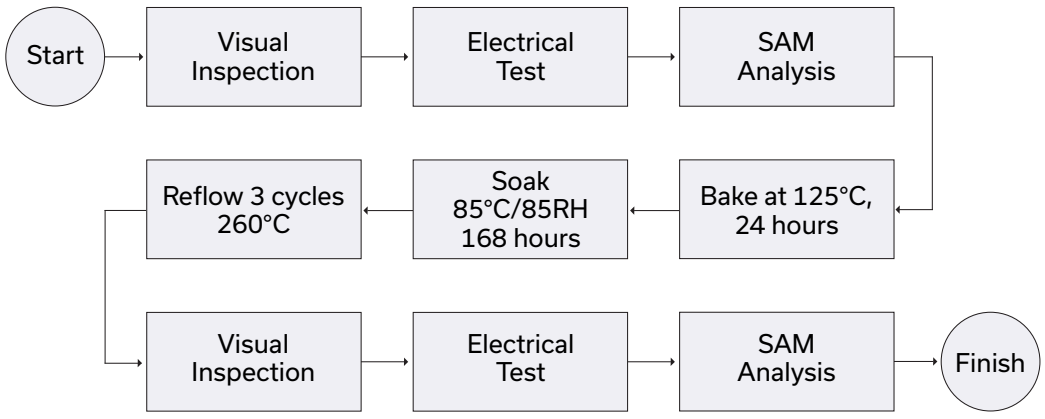
Human Body Model (HBM): Class 1A (250 to <500 V) in accordance with ANSI/ESD STM 5.1 - 2001

Machine Model (MM): Class M1 (<100 V) in accordance with ANSI/ESD STM5.2-1999; passes 40 V

MSL RATING

Moisture Sensitivity: MSL1 in accordance with IPC/JEDEC J-STD-020D

MSL TEST FLOW CHART



NOTES

A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.
C. The parts covered by this specification document are subject to Mini-Circuits standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the standard terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at www.minicircuits.com/terms/viewterm.html