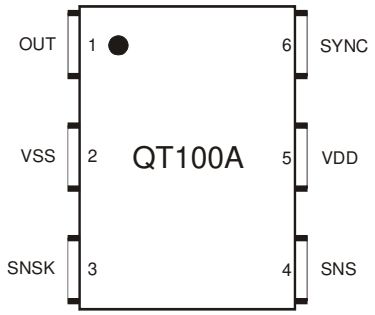
QT100A
CHARGE-TRANSFER QTOUCH™ IC

The QT100A charge-transfer ('QT') touch sensor is a self-contained digital IC capable of detecting near-proximity or touch. It will project a touch or proximity field through any dielectric like glass, plastic, stone, ceramic, and even most kinds of wood. It can also turn small metal-bearing objects into intrinsic sensors, making them responsive to proximity or touch. This capability, coupled with its ability to self-calibrate, can lead to entirely new product concepts.

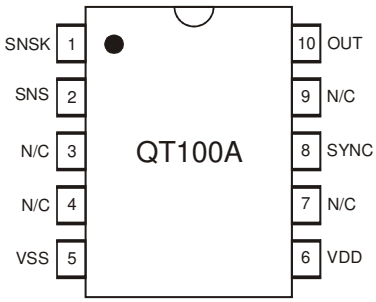
It is designed specifically for human interfaces, like control panels, appliances, toys, lighting controls, or anywhere a mechanical switch or button may be found.

This device can also project a proximity field to severa centimeters with the proper electrode and circuit design.

These devices are intended to replace the QT100. Two package types are offered, one of which can directly replace the QT100's SOT-23-6 package but with a reduced thickness.



6-WSON Package



10-MSOP Package

AT A GLANCE

Number of keys:	One
Technology:	Patented spread-spectrum charge-transfer.
Key outline sizes:	6mm x 6mm or larger (panel thickness dependent); widely different sizes and shapes possible
Electrode design:	Solid or ring electrode shapes
PCB Layers required:	One
Electrode materials:	Etched copper, silver, carbon, Indium Tin Oxide (ITO)
Electrode Substrates:	PCB, FPCB, plastic films, glass
Panel materials:	Plastic, glass, composites, painted surfaces (low particle density metallic paints possible)
Panel thickness:	Up to 50mm glass, 20mm plastic (electrode size and Cs dependent)
Key sensitivity:	Settable via capacitor (Cs)
Interface:	Digital output, active high
Moisture tolerance:	Good
Power:	2V ~ 5.5V; 6.5uA @ 2.0V typical
Package:	10 pin MSOP and 6 pin WSON; both are RoHS compliant
Signal processing:	Self-calibration, auto drift compensation, noise filtering
Applications:	Control panels, consumer appliances, toys, lighting controls, mechanical switch or button
Patents:	QTouch™ (patented Charge-transfer method)

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Table 1.1 Pinlist

Name	Pin 10MSOP	Pin 6WSON	Type	Function	Notes	If Unused
SNSK	1	3	I/O	Sense pin	To Cs + Key	-
SNS	2	4	I/O	Sense pin	To Cs	-
N/C	3	-	O	Unused		Leave open - do not connect
N/C	4	-	O	Unused		Leave open - do not connect
VSS	5	2	Pwr	Ground	Ground	-
VDD	6	5	Pwr	Power	+2.0 ~ +5.5V	-
N/C	7	- †	I	Unused		Always connect to Sync
SYNC	8	6 ‡	I	Sync & Mode Input		-
N/C	9	-	O	Unused		Leave open - do not connect
OUT	10	1	O	Output state		-

Pin Type

I	CMOS input only
I/O	CMOS I/O
O	CMOS push-pull output
Pwr	Power / ground

Notes

† In 6-WSON package this pin is internally connected to SYNC pin

‡ Pin is either Sync or Slow/Fast Mode depending on how strapped; see Section 2.1.

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1 Overview

1.1 Introduction

The QT100A is a digital burst mode charge-transfer (QT) sensor designed specifically for touch controls; it includes all hardware and signal processing functions necessary to provide stable sensing under a wide variety of changing conditions. Only a single low cost, noncritical capacitor is required for operation.

This device is designed to replace the QT100.

1.1.1 WSON package differences with QT100

A 6-pin WSON package version is available which uses the same basic PCB footprint as the QT100's SOT-23-6 package. The 6-WSON package has a reduced package thickness.

In converting an existing design from the QT100 to the QT100A, it should be noted that the 6-WSON package has a bottom 'center pad' which must be either grounded (connected to Vss) or isolated. This center pad is wide enough that it can possibly touch the copper pads of the QT100's SOT-23-6 footprint, potentially creating a short circuit between two or more pads. In order to prevent this the solder mask layer of the PCB under the device should be widened slightly, or, the copper PCB pads narrowed slightly to avoid contact with the center pad. The pinout and pad placement however are the same as for the QT100.

An application note is available to assist in this conversion process.

A 10-pin MSOP package version is also available.

1.1.2 Electrical differences with QT100

The QT100A is behaviorally identical to the QT100, and no change in the value of Cs is required to achieve the same sensitivity as the QT100.

1.2 Basic Operation

Figure 1.1 shows a basic circuit using the device.

The QT100A employs bursts of charge-transfer cycles to acquire its signal. Burst mode permits power consumption in the microamp range, dramatically reduces RF emissions, lowers susceptibility to EMI, and yet permits excellent response time. Internally the signals are digitally processed to reject impulse noise, using a 'consensus' filter which requires four consecutive confirmations of a detection before the output is activated.

The QT switches and charge measurement hardware functions are all internal to the QT100A.

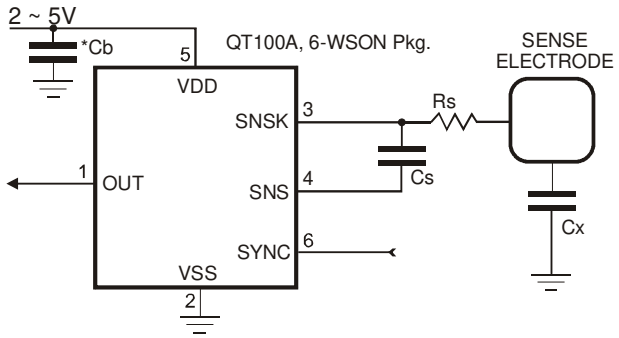
1.3 Electrode Drive

For optimum noise immunity, the electrode should only be connected to SNSK.

In all cases the rule Cs>>Cx must be observed for proper operation; a typical load capacitance (Cx) ranges from 2-20pF while Cs is usually from 2-50nF.

Increasing amounts of Cx destroy gain, therefore it is important to limit the amount of stray capacitance on both SNS terminals. This can be done, for example, by minimizing trace lengths and widths and keeping these traces away from power or ground traces or copper pours.

Figure 1.1 Basic Circuit Configuration



*Note: Bypass capacitor Cb should be tightly wired between Vdd and Vss

The traces and any components associated with SNS and SNSK will become touch sensitive and should be treated with caution to limit the touch area to the desired location.

A series resistor, Rs, should be placed in line with SNSK to the electrode to suppress ESD and EMC effects.

1.4 Sensitivity

1.4.1 Introduction

The sensitivity on the QT100A is a function of things like the value of Cs, electrode size and capacitance, electrode shape and orientation, the composition and aspect of the object to be sensed, the thickness and composition of any overlaying panel material, and the degree of ground coupling of both sensor and object.

1.4.2 Increasing Sensitivity

In some cases it may be desirable to increase sensitivity; for example, when using the sensor with very thick panels having a low dielectric constant. Sensitivity can often be increased by using a larger electrode or reducing panel thickness. Increasing electrode size can have diminishing returns, as high values of Cx will reduce sensor gain.

The value of Cs also has a dramatic effect on sensitivity, and this can be increased in value with the trade-off of slower response time and more power. Increasing the electrode's surface area will not substantially increase touch sensitivity if its diameter is already much larger in surface area than the object being detected. Panel material can also be changed to one having a higher dielectric constant, which will better help to propagate the field.

Ground planes around and under the electrode and its SNSK trace will cause high Cx loading and destroy gain. The possible signal-to-noise ratio benefits of ground area are more than negated by the decreased gain from the circuit, and so ground areas around electrodes are discouraged. Metal areas near the electrode will reduce the field strength and increase Cx loading and should be avoided, if possible. Keep ground away from the electrodes and traces.

1.4.3 Decreasing Sensitivity

In some cases the QT100A may be too sensitive. In this case gain can be easily lowered by decreasing Cs.

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Figure 2.1 Low Power Mode

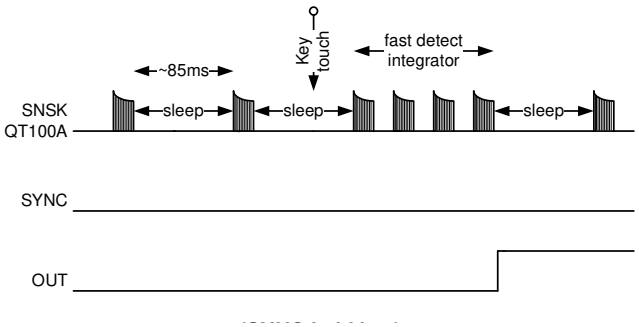


Figure 2.2 Fast Mode Bursts (SYNC held high)

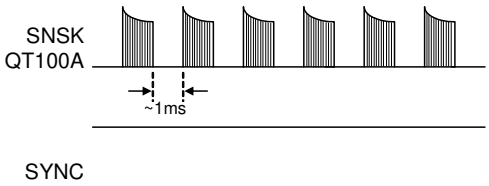


Figure 2.3 Sync Mode (triggered by negative edges on SYNC)

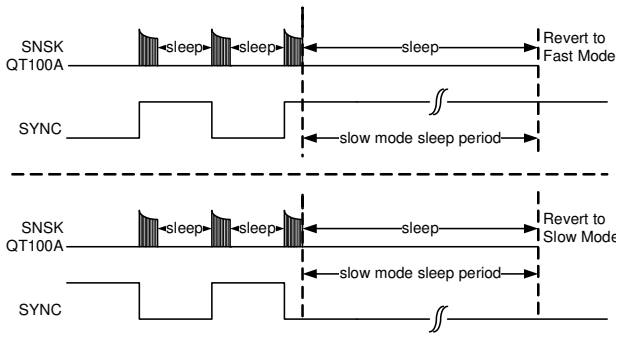
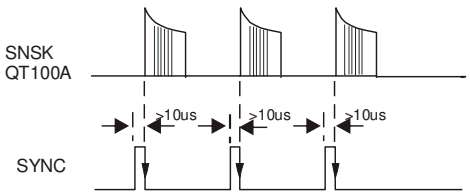


Figure 2.4 Sync Mode (Short Pulses)



2 Operation Specifics

2.1 Run Modes

2.1.1 Introduction

The QT100A has three running modes which depend on the logic level applied to the SYNC pin.

2.1.2 Fast Mode (SYNC = 1)

The QT100A runs in Fast mode if the SYNC pin is permanently high. In this mode the QT100A runs at maximum speed at the expense of increased current consumption. Fast mode is useful when speed of response is the prime design requirement. The delay between bursts in Fast mode is approximately 1ms, as shown in Figure 2.2.

2.1.3 Low Power Mode (SYNC = 0)

The QT100A runs in Low Power (LP) mode if the SYNC pin is held low. In this mode it sleeps for approximately 85ms at the end of each burst, saving power but slowing response. On detecting a possible key touch, it temporarily switches to Fast mode until either the key touch is confirmed or found to be spurious (via the detect integration process). It then returns to LP mode after the key touch is resolved as shown in Figure 2.1.

2.1.4 Sync Mode

It is possible to synchronize the device to an external clock source by placing an appropriate waveform on the SYNC pin. Sync mode can synchronize multiple QT100A devices to each other to prevent cross-interference, or it can be used to enhance noise immunity from low frequency sources such as 50Hz or 60Hz mains signals.

The Sync pin is sampled at the end of each burst. If the device is in Fast mode and the Sync pin is sampled high, then the device continues to operate in Fast mode (Figure 2.2). If SYNC is sampled low, then the device goes to sleep. From then on, it will operate in Sync mode (Figure 2.1). Therefore, to guarantee entry into Sync mode the low period of the Sync signal should be longer than the burst length (Figure 2.3).

However, once Sync mode has been entered, if the Sync signal consists of a series of short pulses (>10µs) then a burst will only occur on the falling edge of each pulse (Figure2.4).

In Sync mode, the device will sleep after each measurement burst (just as in LP mode) but will be awakened by the falling edge of the Sync signal, resulting in a new measurement burst. If Sync remains unchanged for a period longer than the LP mode sleep period (about 85ms), the device will resume operation in either Fast or LP mode depending on the level of the Sync pin (Figure 2.3).

There is no DI in Sync mode (each touch is a detection) but the Max On-duration will depend on the time between Sync pulses; see Sections 2.3 and 2.4. Recalibration timeout is a fixed number of measurements so will vary with the Sync period.

2.2 Threshold

The internal signal threshold level is fixed at 13 counts of change with respect to the internal reference level, which in turn adjusts itself slowly in accordance with the drift compensation mechanism.

The QT100A employs a hysteresis dropout of 3 counts of the delta between the reference and threshold levels.

2.3 Max On-duration

If an object or material obstructs the sense pad the signal may rise enough to create a detection, preventing further operation. To prevent this, the sensor includes a timer which monitors detections. If a detection exceeds the timer setting the sensor performs a full recalibration. This is known as the

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Max On-duration feature and is set to ~80s (at 3V). This will vary slightly with Cs and if Sync mode is used. As the internal timebase for Max On-duration is determined by the burst rate, the use of Sync can cause dramatic changes in this parameter depending on the Sync pulse spacing.

2.4 Detect Integrator

It is desirable to suppress detections generated by electrical noise or from quick brushes with an object. To accomplish this, the QT100A incorporates a 'detect integration' (DI) counter that increments with each detection until a limit is reached, after which the output is activated. If no detection is sensed prior to the final count, the counter is reset immediately to zero. In the QT100A, the required count is four. In LP mode the device will switch to Fast mode temporarily in order to resolve the detection more quickly; after a touch is either confirmed or denied the device will revert back to normal LP mode operation automatically.

The DI can also be viewed as a 'consensus' filter, that requires four successive detections to create an output.

2.5 Forced Sensor Recalibration

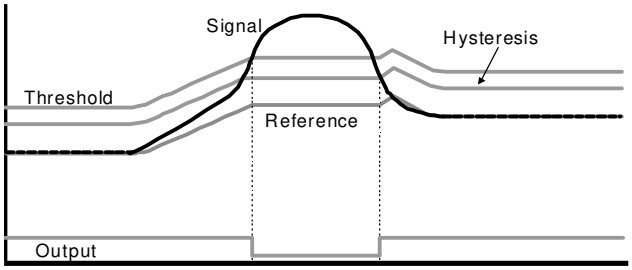
The QT100A has no recalibration pin; a forced recalibration is accomplished when the device is powered up or after the recalibration timeout. However, supply drain is low so it is a simple matter to treat the entire IC as a controllable load; driving the QT100A's Vdd pin directly from another logic gate or a microcontroller port will serve as both power and 'forced recal'. The source resistance of most CMOS gates and microcontrollers are low enough to provide direct power without problem.

2.6 Drift Compensation

Signal drift can occur because of changes in Cx and Cs over time. It is crucial that drift be compensated for, otherwise false detections, nondetections, and sensitivity shifts will follow.

Drift compensation (Figure 2.5). is performed by making the reference level track the raw signal at a slow rate, but only while there is no detection in effect. The rate of adjustment must be performed slowly, otherwise legitimate detections could be ignored. The QT100A drift compensates using a slew-rate limited change to the reference level; the threshold and hysteresis values are slaved to this reference.

Figure 2.5 Drift Compensation



Once an object is sensed, the drift compensation mechanism ceases since the signal is legitimately high, and therefore should not cause the reference level to change.

The QT100A's drift compensation is 'asymmetric'; the reference level drift-compensates in one direction faster than it does in the other. Specifically, it compensates faster for decreasing signals than for increasing signals. Increasing signals should not be compensated for quickly, since an approaching finger could be compensated for partially or entirely before even approaching the sense electrode. However, an obstruction over the sense pad, for which the sensor has already made full allowance, could suddenly be removed leaving the sensor with an artificially elevated reference level and thus become insensitive to touch. In this latter case, the sensor will compensate for the object's removal very quickly, usually in only a few seconds.

With large values of Cs and small values of Cx, drift compensation will appear to operate more slowly than with the converse. Note that the positive and negative drift compensation rates are different.

2.7 Response Time

The QT100A's response time is highly dependent on run mode and burst length, which in turn is dependent on Cs and Cx. With increasing Cs, response time slows, while increasing levels of Cx reduce response time. The response time will also be a lot slower in LP or Sync mode due to a longer time between burst measurements.

Figure 2.6
Getting HeartBeat pulses with a pull-up resistor

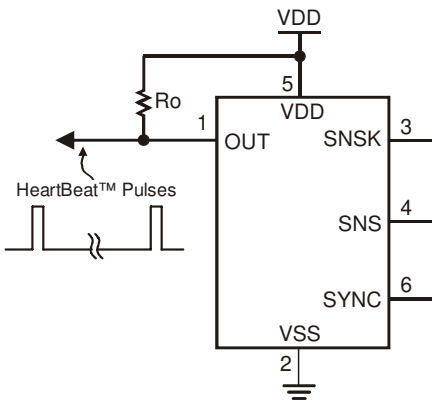
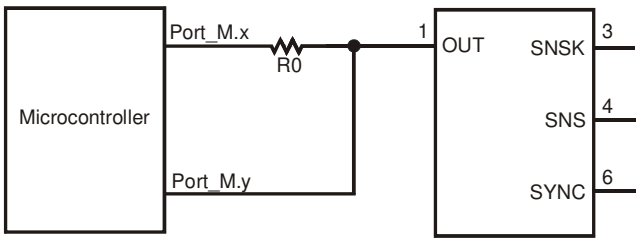


Figure 2.7
Using a micro to obtain HeartBeat pulses in either output state



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2.8 Spread Spectrum

The QT100A modulates its internal oscillator by ±7.5percent during the measurement burst. This spreads the generated noise over a wider band reducing emission levels. This also reduces susceptibility since there is no longer a single fundamental burst frequency.

2.9 Output Features

2.9.1 Output

The output of the QT100A is active-high upon detection. The output will remain active-high for the duration of the detection, or until the Max On-duration expires, whichever occurs first. If a Max On-duration timeout occurs first, the sensor performs a full recalibration and the output becomes inactive (low) until the next detection.

2.9.2 HeartBeat™ Output

The QT100A output has a HeartBeat™ 'health' indicator superimposed on it in all modes. This operates by taking the output pin into a three-state mode for 15µs once before every QT burst. This output state can be used to determine that the sensor is operating properly, or it can be ignored, using one of several simple methods.

The HeartBeat indicator can be sampled by using a pull-up resistor on the OUT pin, and feeding the resulting positive-going pulse into a counter, flip flop, one-shot, or other circuit. The pulses will only be visible when the chip is not detecting a touch.

If the sensor is wired to a microcontroller as shown in Figure2.7, the microcontroller can reconfigure the load resistor to either Vss or Vdd depending on the output state of the QT100A, so that the pulses are evident in either state.

Electromechanical devices like relays will usually ignore the short Heartbeat pulse. The pulse also has too low a duty cycle to visibly affect LEDs. It can be filtered completely if desired, by adding an RC filter to the output, or if interfacing directly and only to a high-impedance CMOS input, by doing nothing or at most adding a small noncritical capacitor from OUT to Vss.

2.9.3 Output Drive

The OUT pin is active high and can sink or source up to 2mA. When a large value of Cs (>20nF) is used the OUT current should be limited to <1mA to prevent gain-shifting side effects, which happen when the load current creates voltage drops on the die and bonding wires; these small shifts can materially influence the signal level to cause detection instability.

3 Circuit Guidelines

Refer to Application Note AN-KD02, downloadable from the Quantum website for more information on construction and design methods.

3.1 Sample Capacitor

Cs is the charge sensing sample capacitor. The required Cs value depends on the thickness of the panel and its dielectric constant. Thicker panels require larger values of Cs. Typical values are 2nF to 50nF depending on the sensitivity required; larger values of Cs demand higher stability and better dielectric to ensure reliable sensing.

The Cs capacitor should be a stable type, such as X7R ceramic or PPS film. For more consistent sensing from unit to unit, 5percent tolerance capacitors are recommended. X7R ceramic types can be obtained in 5percent tolerance at little or no extra cost. In applications where high sensitivity (long burst length) is required the use of PPS capacitors is recommended.

3.2 Power Supply, PCB Layout

The power supply can range between 2.0V and 5.5V. At 3V current drain averages less than 500µA in Fast mode.

If the power supply is shared with another electronic system, care should be taken to ensure that the supply is free of digital spikes, sags, and surges which can adversely affect the QT100A. The QT100A will track slow changes in Vdd, but it can be badly affected by rapid voltage fluctuations. It is highly recommended that a separate voltage regulator be used just for the QT100A to isolate it from power supply shifts caused by other components.

If desired, the supply can be regulated using a Low Dropout (LDO) regulator, although such regulators often have poor transient line and load stability. See Application Note AN-KD02 for further information on power supply considerations.

Parts placement: The chip should be placed to minimize the SNSK trace length to reduce low frequency pickup, and to reduce stray Cx which degrades gain. The Cs and Rs resistors (see Figure 1.1) should be placed as close to the body of the chip as possible so that the trace between Rs and the SNSK pin is very short, thereby reducing the antenna-like ability of this trace to pick up high frequency signals and feed them directly into the chip. A ground plane can be used under the chip and the associated discretes, but the trace from the Rs resistor and the electrode should not run near ground to reduce loading.

For best EMC performance the circuit should be made entirely with SMT components.

Electrode trace routing: Keep the electrode trace (and the electrode itself) away from other signal, power, and ground traces including over or next to ground planes. Adjacent switching signals can induce noise onto the sensing signal; any adjacent trace or ground plane next to, or under, the electrode trace will cause an increase in Cx load and desensitize the device.

Important Note: for proper operation a 100nF (0.1µF) ceramic bypass capacitor must be used directly between V_{DD} and Vss, to prevent latch-up if there are substantial V_{DD} transients; for example, during an ESD event. The bypass capacitor should be placed very close to the Vss and Vdd pins.

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4 Specifications

4.1 Absolute Maximum Specifications

Operating temp. . . . . -40°C to +85°C
Storage temp. . . . . -55°C to +125°C
V_{DD}. . . . . 0 to +6.0V
Max continuous pin current, any control or drive pin. . . . . ±20mA
Short circuit duration to V_{SS}, any pin. . . . . infinite
Short circuit duration to V_{DD}, any pin. . . . . infinite
Voltage forced onto any pin. . . . . -0.6V to (V_{DD} + 0.6) Volts



CAUTION: Stresses beyond those listed under 'Absolute Maximum Specifications' may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or other conditions beyond those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum specification conditions for extended periods may affect device reliability.

4.2 Recommended Operating Conditions

V_{DD}. . . . . +2.0 to 5.5V
Short-term supply ripple+noise. . . . . ±5mV
Long-term supply stability. . . . . ±100mV
Cs value. . . . . 2nF to 50nF
Cx value. . . . . 5 to 50pF

4.3 AC Specifications

V_{DD} = 3.0V, Cs = 10nF, Cx = 5pF, Ta = recommended range, unless otherwise noted

Parameter	Description	Min	Typ	Max	Units	Notes
T_{RC}	Recalibration time		250		ms	Cs, Cx dependent
T_{PC}	Charge duration		2		µs	±7.5% spread spectrum variation
T_{PT}	Transfer duration		2		µs	±7.5% spread spectrum variation
T_{G1}	Time between end of burst and start of the next (Fast mode)		1		ms	
T_{G2}	Time between end of burst and start of the next (LP mode)		85		ms	Increases with decreasing V_{DD}
T_{BL}	Burst length		20		ms	Vdd, Cs and Cx dependent
T_R	Response time			100	ms	
T_{HB}	Heartbeat pulse width		15		µs	

4.4 Signal Processing

Description	Min	Typ	Max	Units	Notes
Threshold differential		13		counts	See Note 1
Hysteresis		3		counts	See Note 1
Consensus filter length		4		samples	
Max on-duration		80		secs	Will vary in SYNC mode, and with V_{DD}

Note1: These threshold and hysteresis values exactly match the performance of the QT100.

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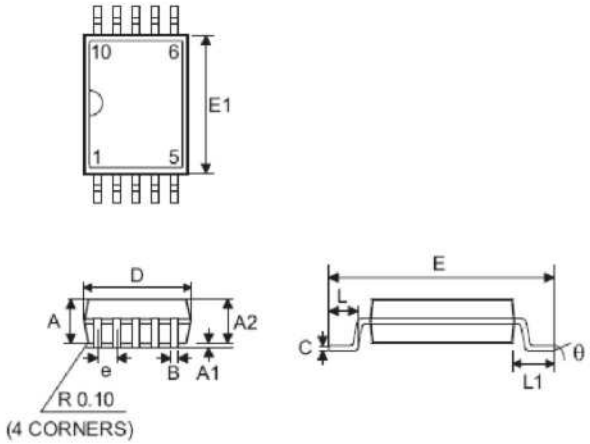
4.5 DC Specifications

V_{DD} = 3.0V, Cs = 10nF, Cx = 5pF, Ta = recommended range, unless otherwise noted

Parameter	Description	Min	Typ	Max	Units	Notes
I_{DD}	Supply current, Fast mode		600		µA	3V
Iddl	Supply current, LP Mode		6.5 17 45	12	µA	2V 3V 5V
V_{DDS}	Supply turn-on slope	100			V/s	Required for proper start-up
V_{IL}	Low input logic level			0.8	V	SYNC pin
V_{HL}	High input logic level	2.2			V	SYNC pin
V_{OL}	Low output voltage			0.6	V	OUT, 4mA sink
V_{OH}	High output voltage	V_{DD}-0.7			V	OUT, 1mA source
I_{IL}	Input leakage current			±1	µA	
Cx	Load capacitance range	0		100	pF	
AR	Acquisition resolution		9	14	bits	

4.6 Mechanical Dimensions

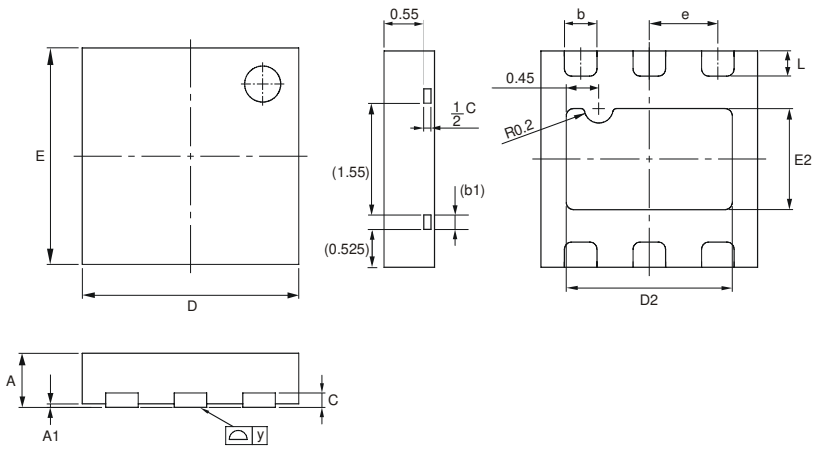
10-pin MSOP Outline Dimensions



Symbol	Dimensions in mm		
	Min.	Nom.	Max.
A	—	—	1.1
A1	0	—	0.15
A2	0.75	—	0.95
B	0.17	—	0.27
C	—	—	0.25
D	—	3	—
E	—	4.9	—
E1	—	3	—
e	—	0.5	—
L	0.4	—	0.8
L1	—	0.95	—
θ	0°	—	8°

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6-Pin WSON Package



Symbol	Dimensions in mm		
	Min.	Nom.	Max.
A	0.70	0.75	0.80
A1	0.00	0.02	0.05
b	0.40	0.45	0.50
b1	-	0.20	-
c	-	0.20 Ref	-
D	2.95	3.00	3.05
D2	2.25	2.30	2.35
E	2.95	3.00	3.05
E2	1.35	1.40	1.45
e	-	0.95	-
L	0.30	0.35	0.40
y	0.00	-	0.075

4.7 Part Marking

<TBD>

4.8 Order Codes

Device Package	Order Code	MSL Rating
10-MSOP Package	QT100A-ISMG	MSL1
6-WSON Package	QT100A-ISG	MSL3

4.9 Moisture Sensitivity Level (MSL)

MSL Rating	Peak Body Temperature	Specifications
MSL1	TBD	TBD
MSL3	260^{O}C	IPC/JEDEC J-STD-020C

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