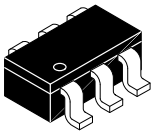
Constant Current Programmable LED Driver with 32 Dimming Levels

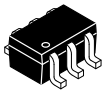
CAT4003B, CAT4004B



UDFN-8
HU2 SUFFIX
CASE 517AW



TSOT23-6
TD SUFFIX
CASE 419AF



SC70-6
SD SUFFIX
CASE 419AD



TSOP-6
TS SUFFIX
CASE 318G

Description

The CAT4003B and CAT4004B provide respectively three and four matched low dropout current sources to drive LEDs. The CAT400XB requires no external RSET resistor. The LED current is internally set to 25 mA when the device is first enabled. Each LED channel includes an individual control loop allowing the device to handle a wide range of LED forward voltages while still maintaining tight current matching.

The EN/DIM logic input supports the device enable and a digital dimming interface for setting the LED channel current with 32 linear dimming levels.

LEDs can be powered directly from a Lithium-ion battery due to the low dropout (75 mV at 20 mA) current sinks.

Package options are available in the 4-channel tiny 8-pad UDFN 2 mm x 2 mm with a max height of 0.55 mm, and 3-channel in the 6-lead TSOT-23, TSOP and SC-70.

Features

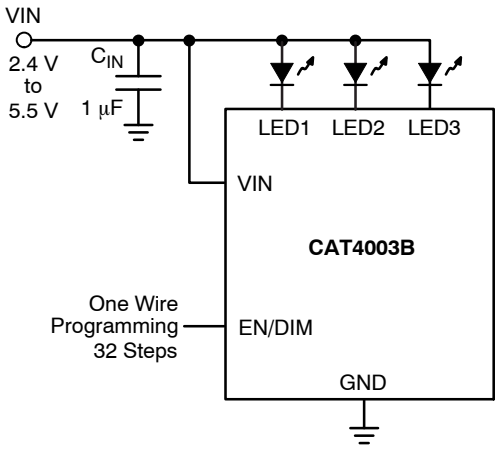
- 3, 4 LED Current Sinks with Tight Matching
- 32 Dimming Levels
- Low Dropout Driver 75 mV at 20 mA
- No Switching Noise
- Shutdown Current less than 1 μA
- 25 mA Max LED Current per Channel
- Dimming via 1-wire EZDim Interface
- Thermal Shutdown Protection
- RoHS Compliant
- 6-lead TSOT-23, TSOP, SC-70, and 8-pad UDFN 2 mm x 2 mm Packages

Typical Applications

- LCD Display Backlight
- Cellular Phones
- Digital Still Cameras

This document contains information on some products that are still under development. **onsemi** reserves the right to change or discontinue these products without notice.

TYPICAL APPLICATION CIRCUIT



ORDERING INFORMATION

See detailed ordering information on page 7 of this data sheet.

NOTE: Some of the devices on this data sheet have been **DISCONTINUED**. Please refer to the table on page 7.

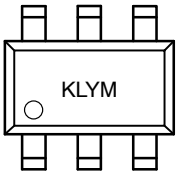
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MARKING DIAGRAMS



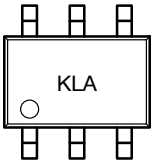
UDFN8 (2 x 2 mm)

BH = CAT4004B Device Code
A = Assembly Location Code
Y = Production Year (last digit)
M = Production Month: 1 – 9, O, N, D



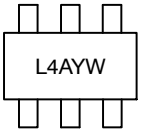
TSOT23-6L

KL = CAT4003B Device Code
Y = Production Year (last digit)
M = Production Month: 1 – 9, O, N, D



SC70-6L

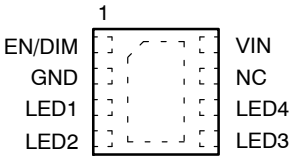
KL = CAT4003B Device Code
A = Assembly Location Code



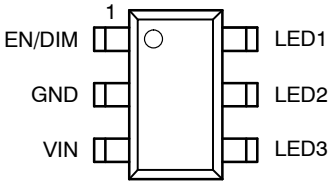
TSOP-6

L4 = CAT4003B Device Code
A = Assembly Location Code
YW = Year and Work Week

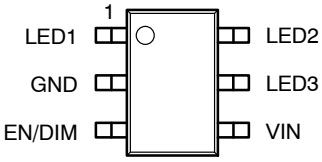
PIN CONNECTIONS (Top View)



CAT4004B
UDFN8 2 x 2 mm



CAT4003B
TSOT23-6L, TSOP-6



CAT4003B
SC70-6L

Table 1. PIN FUNCTIONS

Pin Name	Function
EN/DIM	Device Enable (active high) and Dimming Control
GND	Ground Reference
LED1	LED1 Cathode Terminal
LED2	LED2 Cathode Terminal
LED3	LED3 Cathode Terminal
LED4	LED4 Cathode Terminal
VIN	Device Supply Input, Connect to Battery or Supply
TAB	Connect to GND on the PCB, for CAT4004B only.

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Table 2. ABSOLUTE MAXIMUM RATINGS

Parameter	Value	Unit
VIN, LEDx Voltage	6	V
EN/DIM Voltage	VIN + 0.7	V
Storage Temperature Range	−65 to +150	°C
Junction Temperature Range	−40 to +125	°C
Lead Temperature	300	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

Table 3. RECOMMENDED OPERATING CONDITIONS

Parameter	Value	Unit
VIN	2.4 to 5.5	V
Ambient Temperature Range	−40 to +85	°C
LED Current Range	0 to 25	mA

1. Typical application circuit with external components is shown on page 1.

Table 4. ELECTRICAL OPERATING CHARACTERISTICS
(over recommended operating conditions unless specified otherwise) (V_{IN} = 4.0 V, EN = High, T_{AMB} = 25 °C) (Note 3)

Parameter	Conditions	Symbol	Min	Typ	Max	Units
Quiescent Current	I_{LED} = 25 mA/channel	I_Q	0.5	0.7	1.5	mA
Shutdown Current	V_{EN} = 0 V	I_{QSHDN}			1	μA
Full Scale LED Current (Average) (Note 2)		$I_{LED-FULL}$	24	25	26	mA
LED Channel Matching	$\frac{I_{LED} - I_{LEDAVG}}{I_{LEDAVG}}$	$I_{LED-DEV}$	−5	±1	+5	%
Dropout Voltage	I_{LED} = 20 mA I_{LED} = 1 mA	V_{DOUT}	−	75 45	−	mV
EN/DIM Pin – Internal pull-down resistor – Logic High Level – Logic Low Level		 $R_{EN/DIM}$ V_{HI} V_{LO}	 − 1.3 −	 200 − −	 − − 0.4	 kΩ V V
Thermal Shutdown		T_{SD}	−	150	−	°C
Thermal Hysteresis		T_{HYS}	−	20	−	°C
Undervoltage lockout (UVLO) threshold		V_{UVLO}	−	2.0	−	V

2. For the CAT4003B, $I_{LEDAVG} = (I_{LED,CH1} + I_{LED,CH2} + I_{LED,CH3}) / 3$
3. The Min/Max limits apply across the −40 °C to +85 °C ambient temperature range and are assured by design, characterization and correlation with statistical analysis.

Table 5. RECOMMENDED EN/DIM TIMING (For 3 V ≤ V_{IN} ≤ 5.5 V, over full ambient temperature range −40 °C to +85 °C.) (Note 4)

Parameter	Conditions	Symbol	Min	Typ	Max	Units
Power-up Setup Time		T_{SETUP}	10	−	−	μs
EN/DIM program low time		T_{LO}	0.2	−	500	μs
EN/DIM program high time		T_{HI}	0.2	−	−	μs
LED current settling time		T_{LED}		10		μs
EN/DIM low time to shutdown		T_{PWRDWN}	2	3	5	ms

4. The Min/Max limits apply across the −40 °C to +85 °C ambient temperature range and are assured by design, characterization and correlation with statistical analysis.

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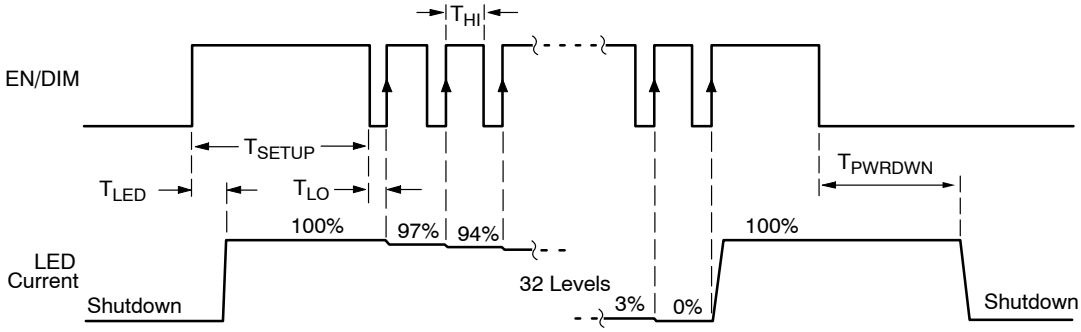


Figure 1. CAT400XB EN/DIM Dimming Timing Diagram

LED Current Setting

On the CAT400XB, the full scale LED current is internally set to 25 mA (no external resistor).

When the EN/DIM is first enabled, the CAT400XB sets the LED channel current to the full scale current. Each consecutive rising edge on the EN/DIM decreases the LED current by one step until it goes to zero, as shown on Figure 1.

TYPICAL CHARACTERISTICS

(CAT4003B, V_{IN} = 4 V, V_F = 3.3 V, I_{OUT} = 75 MA (3 LEDS AT 25 MA), C_{IN} = 1 MF, T_{AMB} = 25 °C UNLESS OTHERWISE SPECIFIED.)

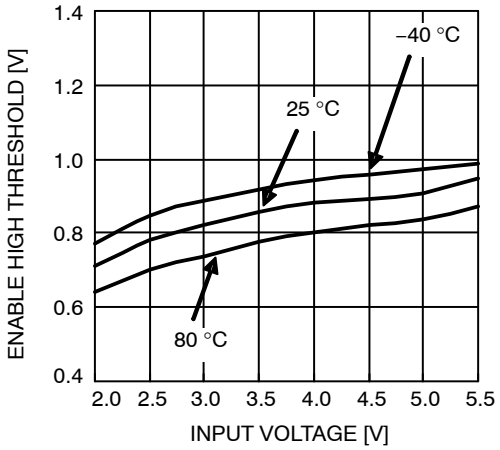


Figure 2. EN High Threshold vs. Input Voltage

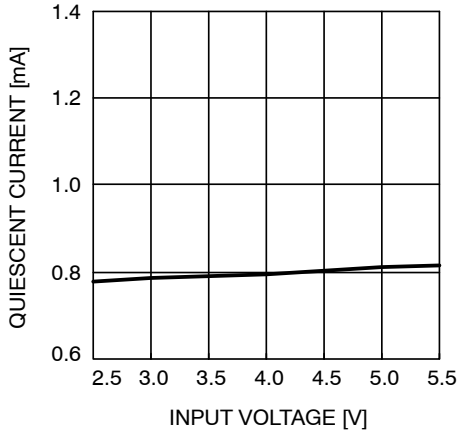


Figure 3. Quiescent Current vs. Input Voltage (full load)

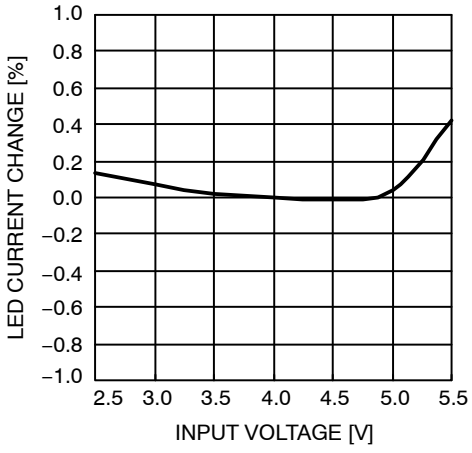


Figure 4. LED Current Change vs. Input Voltage

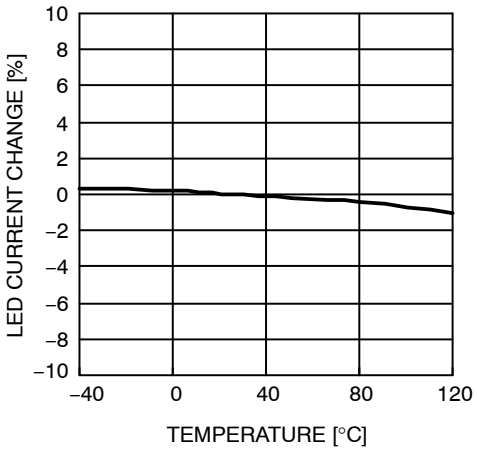


Figure 5. LED Current Change vs. Temperature

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TYPICAL CHARACTERISTICS

(CAT4003B, V_{IN} = 4 V, V_F = 3.3 V, I_{OUT} = 75 MA (3 LEDS AT 25 MA), C_{IN} = 1 MF, T_{AMB} = 25 °C UNLESS OTHERWISE SPECIFIED.)

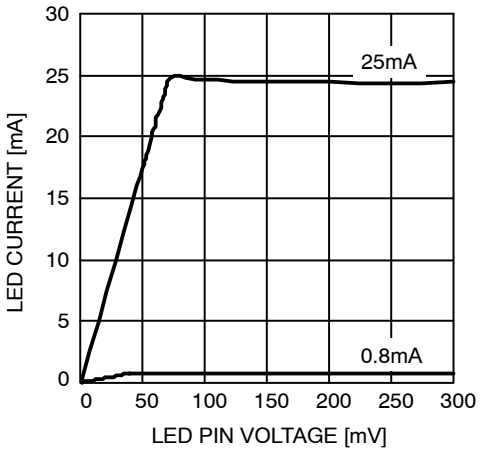


Figure 6. Dropout Characteristics

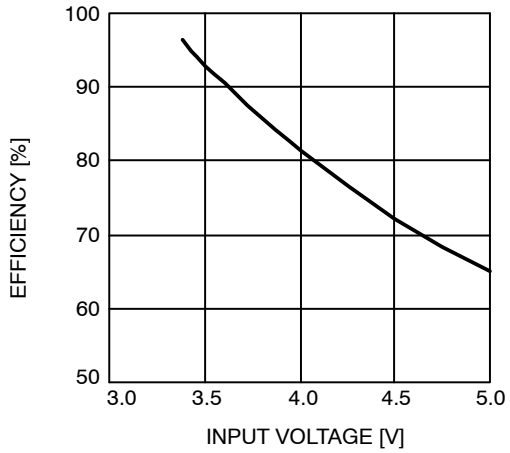


Figure 7. Efficiency vs. Input Voltage

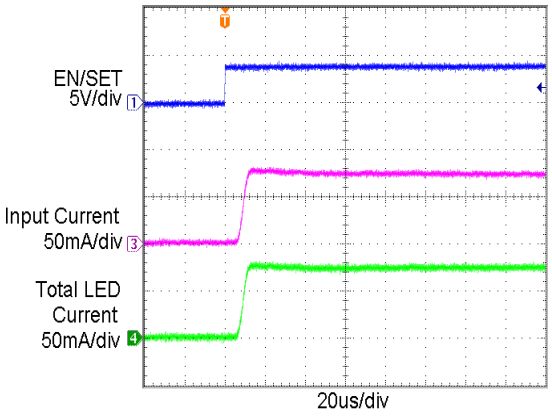


Figure 8. Power Up Waveform

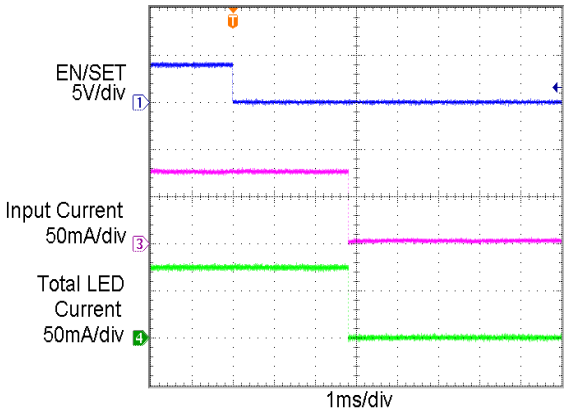


Figure 9. Power Down Waveform

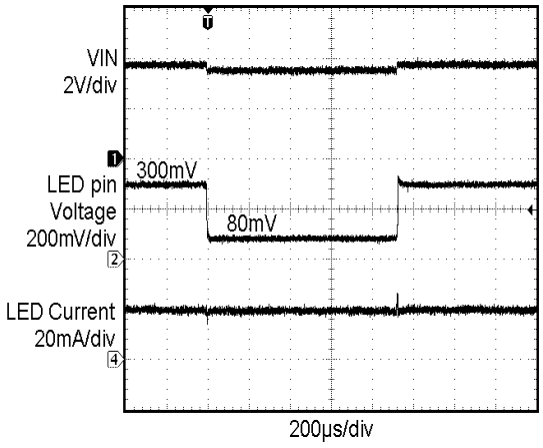


Figure 10. Line Transient Waveform

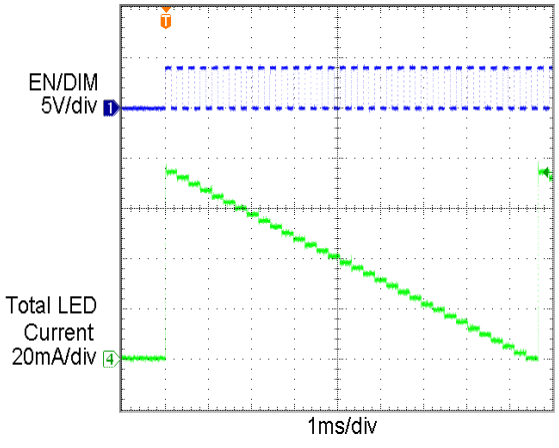


Figure 11. Dimming Levels

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Pin Functions

VIN is the supply pin for the charge pump. A small 1 μF ceramic bypass capacitor is required between the VIN pin and ground near the device. The operating input voltage range is from 2.4 V to 5.5 V. Whenever the input supply falls below the under-voltage threshold (2.0 V), all the LED channels are disabled and the device enters shutdown mode.

EN/DIM is the enable and one wire dimming input for all LED channels. Levels of logic high and logic low are set at 1.3 V and 0.4 V respectively. When EN/DIM is initially taken high, the CAT400XB becomes enabled and all LED currents are set to the full scale 25 mA. To place the device into "zero current" shutdown mode, the EN/DIM pin must be held low for 3 ms typical

LED1 to LED4 provide the internal regulated current for each of the LED cathodes. The pins enter a high impedance zero current state whenever the device is placed in shutdown mode.

GND is the ground reference for the device. The pin must be connected to the ground plane on the PCB.

TAB (CAT4004B only) is the exposed pad underneath the package. For best thermal performance, the tab should be soldered to the PCB and connected to the ground plane.

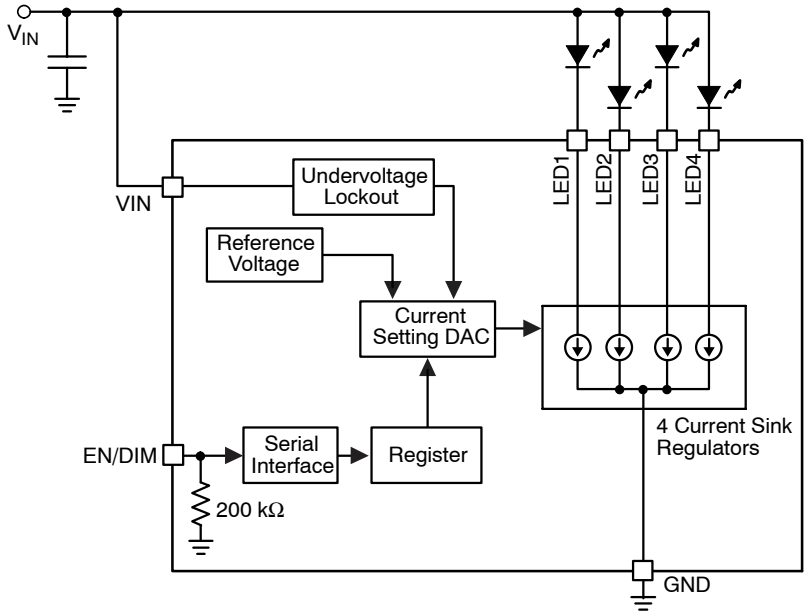


Figure 12. CAT4004B Functional Block Diagram

Basic Operation

The CAT400XB uses tightly matched current sinks to accurately regulate LED current in each channel.

There are 32 different settings for LED brightness that can be programmed through the EN/DIM pin. Tight current regulation for all channels is possible over a wide range of input and LED voltages due to independent current sensing circuitry on each channel.

Each LED channel needs a minimum of 75 mV headroom to sink a constant regulated current of 20 mA. If the input supply falls below 2.0 V, the under-voltage lockout circuit disables all LED channels and resets the circuit to default values. Any unused LED channels should be left open.

CAT400XB LED Current Selection

After power-up and once enabled, the LED current is set initially to the full scale currrent of 25 mA. The number of pulses (n) on the EN/DIM input does decrease the current value as follows:

$$\text{LED current [mA]} = 25 \times \left(\frac{31 - n}{31}\right)$$

The full scale current is calculated from the above formula with n equal to zero.

The EN/DIM pin has two primary functions. One function enables and disables the device. The other function is LED current dimming with 32 different levels by pulsing the input signal, as shown on Figure 1. On each successive pulse rising edge, the LED current is decreased by about 3.2% (1/31st of the full scale value). After 30 pulses, the LED current is 3.2% of the full scale current. On the 31st pulse, the current drops to zero, and then goes back to full scale on the following pulse.

Initially once the EN/DIM input is first pulled high, it must remain high for at least T_{SETUP} delay (10 μs minimum) to allow the LED driver to complete its power-up. After this delay, EN/DIM can be pulsed in order to set the LED current to the desired level. Each pulse width should be between 1 μs and 500 μs. Pulses faster than the minimum T_{LO} may be ignored and filtered by the device. Pulses longer than the maximum T_{LO} may shutdown the device. By pulsing the EN/DIM signal at a high frequency, the LED current can quickly be set to zero or to any other level.

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The LED driver enters a “zero current” shutdown mode if EN/DIM is held low for longer than 5 ms.

The dimming level is set by the number of pulses on the EN/DIM after the power-up, as shown in Table 6.

Table 6. DIMMING LEVELS

Full Scale Current in %	Dimming Pulses [n]
100	0
97	1
94	2
90	3
87	4
84	5
81	6
77	7
74	8
71	9
68	10
65	11
61	12
58	13
55	14
52	15
48	16
45	17
42	18
39	19
35	20
32	21
29	22
26	23
23	24
19	25
16	26
13	27
10	28
6	29
3	30
0	31
100	32

ORDERING INFORMATION (Note 6)

Orderable Part Number	Package	Finish	**Shipping** (Note 7)
CAT4004BHU2-GT3	UDFN, 8-Pad, 2 x 2 mm	NiPdAu (RoHS Compliant)	3,000 / Tape & Reel

DISCONTINUED (Note 5)

CAT4003BTD-GT3	TSOT-23, 6-Lead	NiPdAu (RoHS Compliant)	3,000 / Tape & Reel
CAT4003BTS-T3	TSOP, 6-Lead	Matte-Tin (RoHS Compliant)	3,000 / Tape & Reel
CAT4003BSD-GT3	SC-70, 6-Lead	NiPdAu (RoHS Compliant)	3,000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

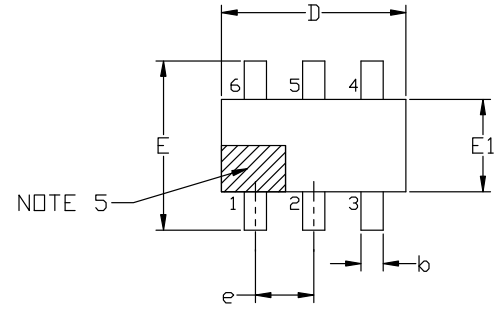
5. **DISCONTINUED:** These devices are not recommended for new design. Please contact your **onsemi** representative for information. The most current information on these devices may be available on www.onsemi.com.
6. For additional package and temperature options, please contact your nearest **onsemi** Sales office.
7. For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.
8. For detailed information and a breakdown of device nomenclature and numbering systems, please see the **onsemi** Device Nomenclature document, TND310/D, available a www.onsemi.com

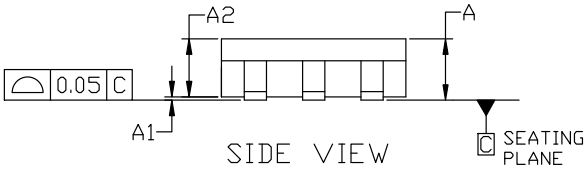
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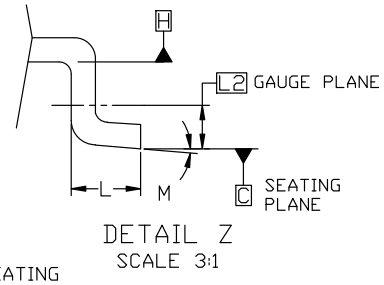


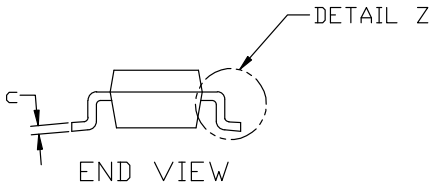
TSOP−6 3.00x1.50x0.90, 0.95P
CASE 318G
ISSUE W

DATE 26 FEB 2024



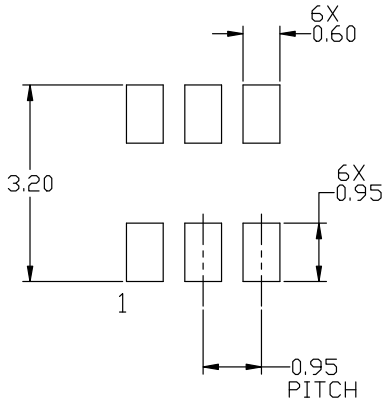






NOTES:
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2018.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
4. DIMENSIONS D AND E1 DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS. MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL NOT EXCEED 0.15 PER SIDE. DIMENSIONS D AND E1 ARE DETERMINED AT DATUM H.
5. PIN 1 INDICATOR MUST BE LOCATED IN THE INDICATED ZONE

MILLIMETERS			
DIM	MIN	NOM	MAX
A	0.90	1.00	1.10
A1	0.01	0.06	0.10
A2	0.80	0.90	1.00
b	0.25	0.38	0.50
c	0.10	0.18	0.26
D	2.90	3.00	3.10
E	2.50	2.75	3.00
E1	1.30	1.50	1.70
e	0.85	0.95	1.05
L	0.20	0.40	0.60
L2	0.25 BSC		
M	0°	---	10°



RECOMMENDED MOUNTING FOOTPRINT

*For additional information on our Pb-Free strategy and soldering details, please download th e ON Semiconductor Soldering and Mounting Techniques Reference manual, SOLDERRM/D.

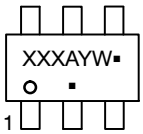
DOCUMENT NUMBER:	**98ASB14888C**	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.	
DESCRIPTION:	**TSOP−6 3.00x1.50x0.90, 0.95P**		**PAGE 1 OF 2**

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TSOP−6 3.00x1.50x0.90, 0.95P
CASE 318G
ISSUE W

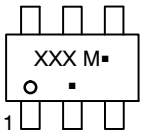
DATE 26 FEB 2024

GENERIC MARKING DIAGRAM*



IC

XXX = Specific Device Code
A = Assembly Location
Y = Year
W = Work Week
▪ = Pb−Free Package



STANDARD

XXX = Specific Device Code
M = Date Code
▪ = Pb−Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb−Free indicator, “G” or microdot “▪”, may or may not be present. Some products may not follow the Generic Marking.

STYLE 1:
PIN 1. DRAIN
2. DRAIN
3. GATE
4. SOURCE
5. DRAIN
6. DRAIN

STYLE 2:
PIN 1. EMITTER 2
2. BASE 1
3. COLLECTOR 1
4. EMITTER 1
5. BASE 2
6. COLLECTOR 2

STYLE 3:
PIN 1. ENABLE
2. N/C
3. R BOOST
4. Vz
5. V in
6. V out

STYLE 4:
PIN 1. N/C
2. V in
3. NOT USED
4. GROUND
5. ENABLE
6. LOAD

STYLE 5:
PIN 1. EMITTER 2
2. BASE 2
3. COLLECTOR 1
4. EMITTER 1
5. BASE 1
6. COLLECTOR 2

STYLE 6:
PIN 1. COLLECTOR
2. COLLECTOR
3. BASE
4. EMITTER
5. COLLECTOR
6. COLLECTOR

STYLE 7:
PIN 1. COLLECTOR
2. COLLECTOR
3. BASE
4. N/C
5. COLLECTOR
6. EMITTER

STYLE 8:
PIN 1. Vbus
2. D(in)
3. D(in)+
4. D(out)+
5. D(out)
6. GND

STYLE 9:
PIN 1. LOW VOLTAGE GATE
2. DRAIN
3. SOURCE
4. DRAIN
5. DRAIN
6. HIGH VOLTAGE GATE

STYLE 10:
PIN 1. D(OUT)+
2. GND
3. D(OUT)−
4. D(IN)−
5. VBUS
6. D(IN)+

STYLE 11:
PIN 1. SOURCE 1
2. DRAIN 2
3. DRAIN 2
4. SOURCE 2
5. GATE 1
6. DRAIN 1/GATE 2

STYLE 12:
PIN 1. I/O
2. GROUND
3. I/O
4. I/O
5. VCC
6. I/O

STYLE 13:
PIN 1. GATE 1
2. SOURCE 2
3. GATE 2
4. DRAIN 2
5. SOURCE 1
6. DRAIN 1

STYLE 14:
PIN 1. ANODE
2. SOURCE
3. GATE
4. CATHODE/DRAIN
5. CATHODE/DRAIN
6. CATHODE/DRAIN

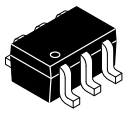
STYLE 15:
PIN 1. ANODE
2. SOURCE
3. GATE
4. DRAIN
5. N/C
6. CATHODE

STYLE 16:
PIN 1. ANODE/CATHODE
2. BASE
3. EMITTER
4. COLLECTOR
5. ANODE
6. CATHODE

STYLE 17:
PIN 1. EMITTER
2. BASE
3. ANODE/CATHODE
4. ANODE
5. CATHODE
6. COLLECTOR

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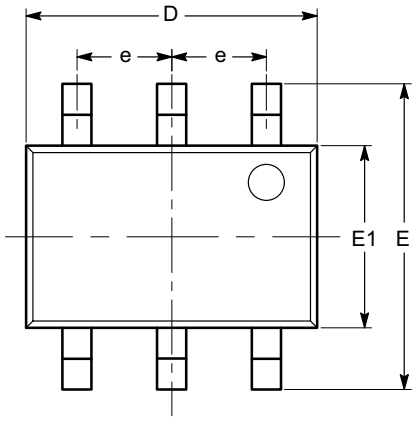
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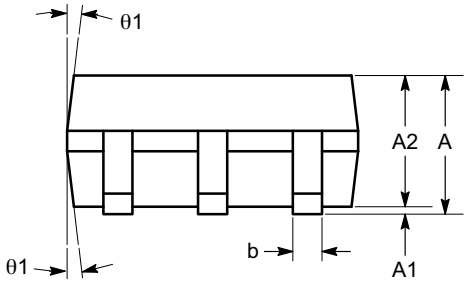
SC−88 (SC−70 6 Lead), 1.25x2
CASE 419AD
ISSUE A

DATE 07 JUL 2010

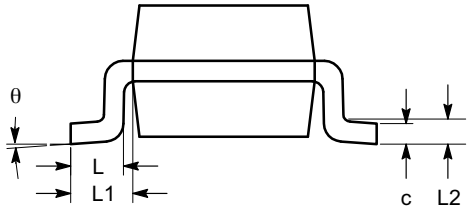


TOP VIEW

SYMBOL	MIN	NOM	MAX
A	0.80		1.10
A1	0.00		0.10
A2	0.80		1.00
b	0.15		0.30
c	0.10		0.18
D	1.80	2.00	2.20
E	1.80	2.10	2.40
E1	1.15	1.25	1.35
e		0.65 BSC	
L	0.26	0.36	0.46
L1		0.42 REF	
L2		0.15 BSC	
θ	0°		8°
θ1	4°		10°



SIDE VIEW



END VIEW

Notes:
(1) All dimensions are in millimeters. Angles in degrees.
(2) Complies with JEDEC MO-203.

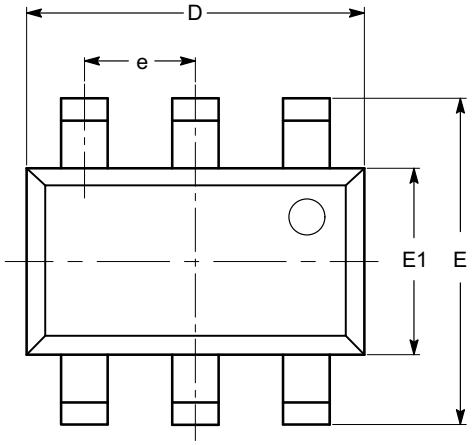
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DESCRIPTION:	**SC−88 (SC−70 6 LEAD), 1.25X2**		**PAGE 1 OF 1**

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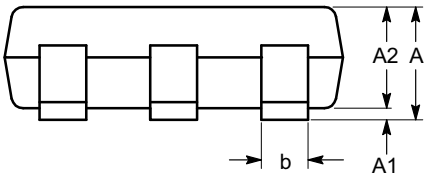
TSOT−23, 6 LEAD
CASE 419AF−01
ISSUE O

DATE 19 DEC 2008

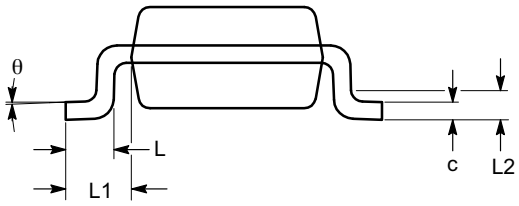


TOP VIEW

SYMBOL	MIN	NOM	MAX
A			1.00
A1	0.01	0.05	0.10
A2	0.80	0.87	0.90
b	0.30		0.45
c	0.12	0.15	0.20
D		2.90 BSC	
E		2.80 BSC	
E1		1.60 BSC	
e		0.95 TYP	
L	0.30	0.40	0.50
L1		0.60 REF	
L2		0.25 BSC	
θ	0°		8°



SIDE VIEW



END VIEW

Notes:
(1) All dimensions are in millimeters. Angles in degrees.
(2) Complies with JEDEC MO-193.

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DESCRIPTION:	TSOT−23, 6 LEAD	PAGE 1 OF 1

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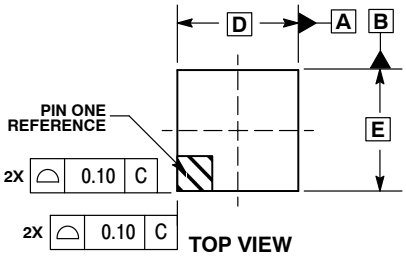
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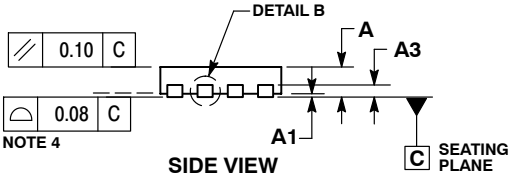


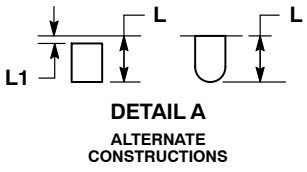
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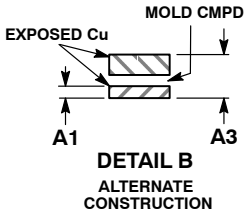
UDFN8, 2x2
CASE 517AW
ISSUE A

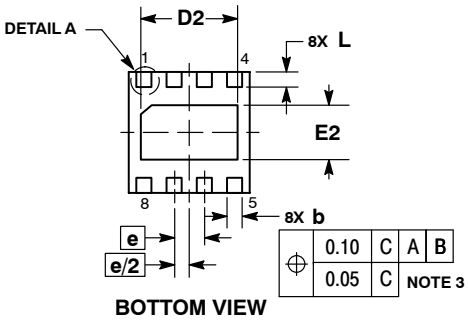
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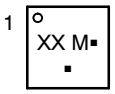




NOTES:
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b APPLIES TO PLATED TERMINALS AND IS MEASURED BETWEEN 0.15 AND 0.30 MM FROM THE TERMINAL TIP.
4. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.
5. FOR DEVICE OPN CONTAINING W OPTION, DETAIL B ALTERNATE CONSTRUCTION IS NOT APPLICABLE.

	MILLIMETERS	
DIM	MIN	MAX
A	0.45	0.55
A1	0.00	0.05
A3	0.13 REF	
b	0.18	0.30
D	2.00 BSC	
D2	1.50	1.70
E	2.00 BSC	
E2	0.80	1.00
e	0.50 BSC	
L	0.20	0.45
L1	---	0.15

GENERIC MARKING DIAGRAM*

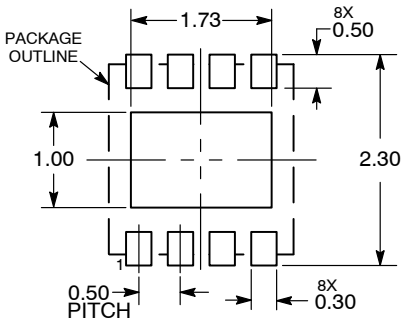


XX = Specific Device Code
M = Date Code
▪ = Pb−Free Package

(Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking. Pb−Free indicator, “G” or microdot “ ▪”, may or may not be present.

RECOMMENDED SOLDERING FOOTPRINT*



DIMENSIONS: MILLIMETERS

*For additional information on our Pb−Free strategy and soldering details, please download the **onsemi** Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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DESCRIPTION:	**UDFN8, 2X2**		**PAGE 1 OF 1**

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