MOSFETs Silicon N-Channel MOS (π-MOSⅦ)

TK4P60DB

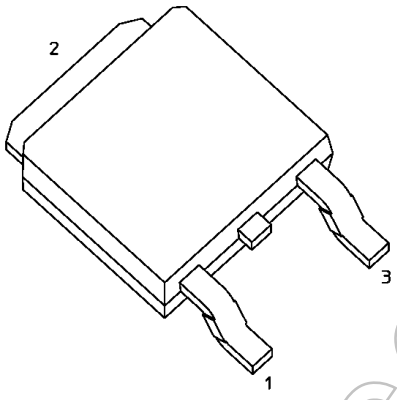
1. Applications

- Switching Voltage Regulators

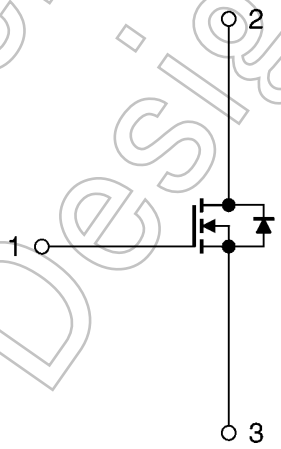
2. Features

(1) Low drain-source on-resistance : $R_{DS(ON)}$ = 1.6 Ω (typ.)
(2) High forward transfer admittance : $|Y_{fs}|$ = 2.2 S (typ.)
(3) Low leakage current : I_{DSS} = 10 μA (max) (V_{DS} = 600 V)
(4) Enhancement mode : V_{th} = 2.4 to 4.4 V (V_{DS} = 10 V, I_D = 1 mA)

3. Packaging and Internal Circuit



DPAK



1: Gate
2: Drain (Heatsink)
3: Source

Not Recommended for New Design

Start of commercial production
2009-12

4. Absolute Maximum Ratings (Note) (T_a = 25 °C unless otherwise specified)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	600	V
Gate-source voltage		V_{GSS}	±30	
Drain current (DC)	(Note 1)	I_D	3.7	A
Drain current (pulsed)	(Note 1)	I_{DP}	14.8	
Power dissipation	(T_c = 25 °C)	P_D	80	W
Single-pulse avalanche energy	(Note 2)	E_{AS}	147	mJ
Avalanche current	(Note 3)	I_{AR}	3.7	A
Repetitive avalanche energy	(Note 3)	E_{AR}	8	mJ
Reverse drain current (DC)	(Note 1)	I_{DR}	3.7	A
Reverse drain current (pulsed)	(Note 1)	I_{DRP}	14.8	
Channel temperature		T_{ch}	150	°C
Storage temperature		T_{stg}	-55 to 150	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

5. Thermal Characteristics

Characteristics	Symbol	Max	Unit
Channel-to-case thermal resistance	$R_{th(ch-c)}$	1.56	°C/W
Channel-to-ambient thermal resistance	$R_{th(ch-a)}$	125	

Note 1: Ensure that the channel temperature does not exceed 150 °C.
Note 2: V_{DD} = 90 V, T_{ch} = 25 °C (initial), L = 18.7 mH, R_G = 25 Ω, I_{AR} = 3.7 A
Note 3: Repetitive rating; pulse width limited by maximum channel temperature.

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.

Not Recommended for New Design

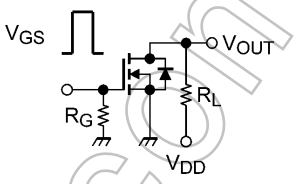
6. Electrical Characteristics

6.1. Static Characteristics (T_a = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Gate leakage current	I_{GSS}	V_{GS} = ±30 V, V_{DS} = 0 V	—	—	±1	μA
Drain cut-off current	I_{DSS}	V_{DS} = 600 V, V_{GS} = 0 V	—	—	10	
Drain-source breakdown voltage	$V_{(BR)DSS}$	I_D = 10 mA, V_{GS} = 0 V	600	—	—	V
Gate threshold voltage	V_{th}	V_{DS} = 10 V, I_D = 1 mA	2.4	—	4.4	
Drain-source on-resistance	$R_{DS(ON)}$	V_{GS} = 10 V, I_D = 1.9 A	—	1.6	2	Ω
Forward transfer admittance	$\|Y_{fs}\|$	V_{DS} = 10 V, I_D = 1.9 A	0.6	2.2	—	S

6.2. Dynamic Characteristics (T_a = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Input capacitance	C_{iss}	V_{DS} = 25 V, V_{GS} = 0 V, f = 1 MHz	—	540	—	pF
Reverse transfer capacitance	C_{rss}		—	3	—	
Output capacitance	C_{oss}		—	60	—	
Switching time (rise time)	t_r	See Fig.6.2.1	—	18	—	ns
Switching time (turn-on time)	t_{on}		—	40	—	
Switching time (fall time)	t_f		—	8	—	
Switching time (turn-off time)	t_{off}		—	55	—	



V_{DD} ≈ 200 V
V_{GS} = 0 V / 10 V
I_D = 1.9 A
R_L = 105 Ω
R_G = 50 Ω
Duty ≤ 1 %, t_w = 10 μs

Fig. 6.2.1 Switching Time Test Circuit

6.3. Gate Charge Characteristics (T_a = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Total gate charge (gate-source plus gate-drain)	Q_g	V_{DD} ≈ 400 V, V_{GS} = 10 V, I_D = 3.7 A	—	11	—	nC
Gate-source charge	Q_{gs}		—	6	—	
Gate-drain charge	Q_{gd}		—	5	—	

6.4. Source-Drain Characteristics (T_a = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Diode forward voltage	V_{DSF}	I_{DR} = 3.7 A, V_{GS} = 0 V	—	—	-1.7	V
Reverse recovery time	t_{rr}	I_{DR} = 3.7 A, V_{GS} = 0 V, -di/dt = 100 A/μs	—	1000	—	ns
Reverse recovery charge	Q_{rr}		—	5.5	—	μC

Not Recommended for New Design

7. Marking

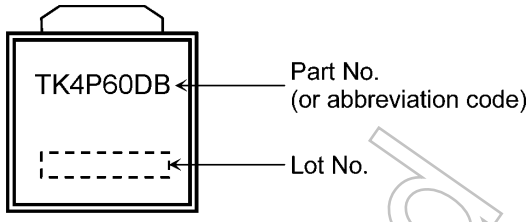


Fig. 7.1 Marking

Not Recommended for New Design

8. Characteristics Curves (Note)

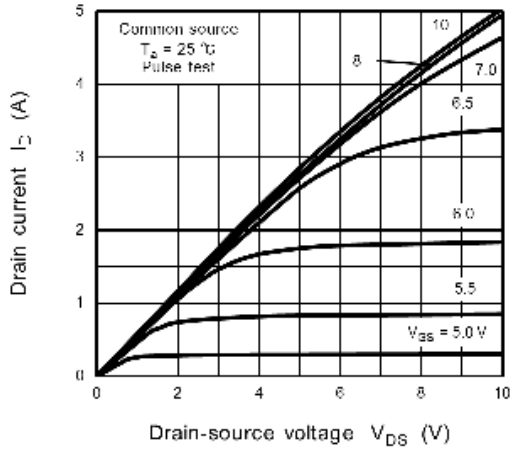


Fig. 8.1 I_D - V_{DS}

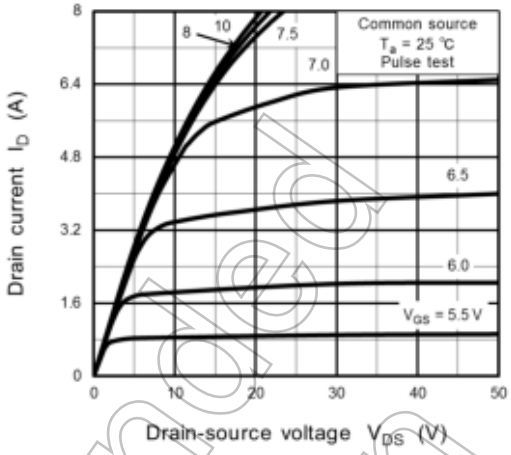


Fig. 8.2 I_D - V_{DS}

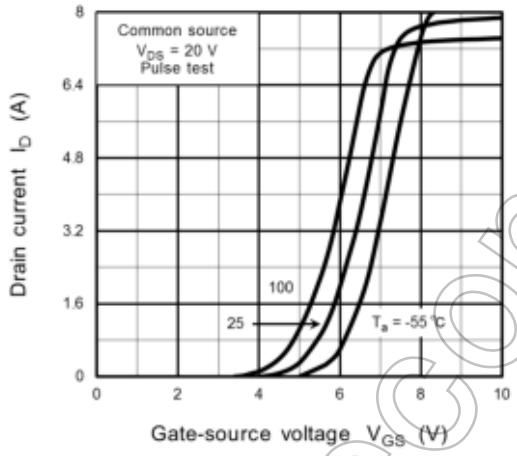


Fig. 8.3 I_D - V_{GS}

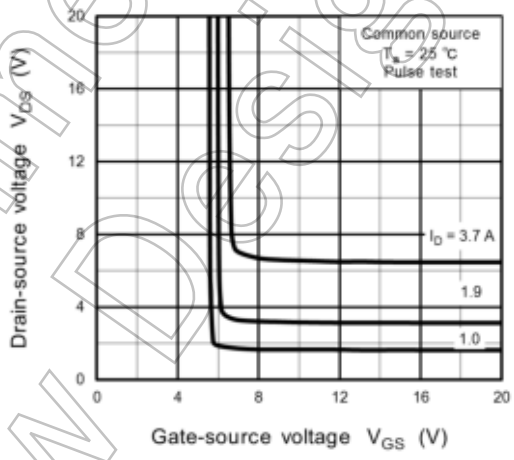


Fig. 8.4 V_{DS} - V_{GS}

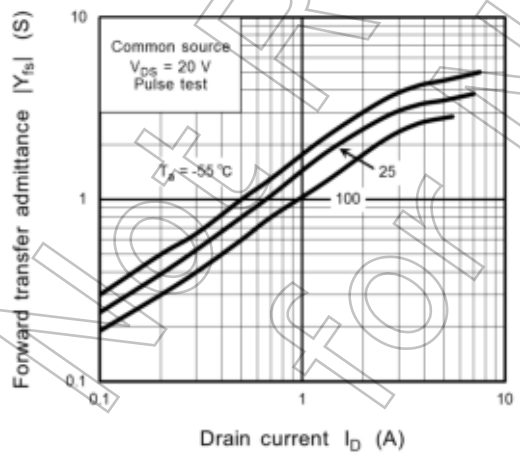


Fig. 8.5 $|Y_{fs}|$ - I_D

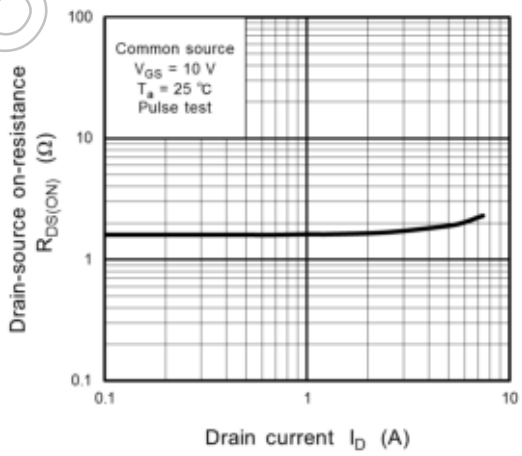


Fig. 8.6 $R_{DS(ON)}$ - I_D

Not Recommended for New Design

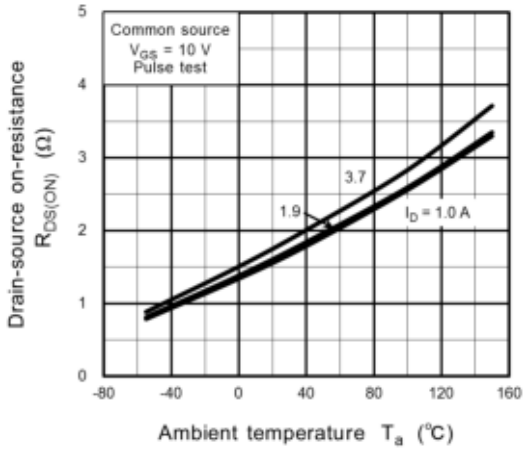


Fig. 8.7 $R_{DS(ON)}$ - T_a

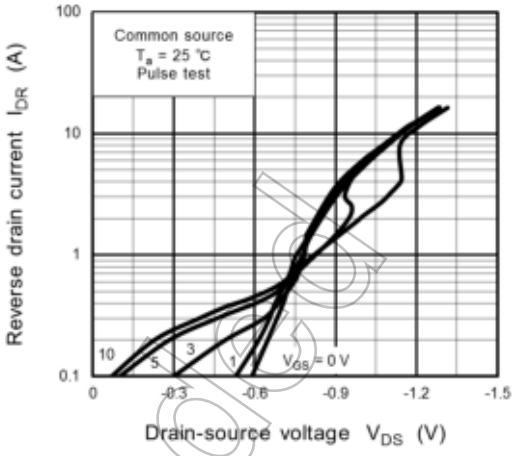


Fig. 8.8 I_{DR} - V_{DS}

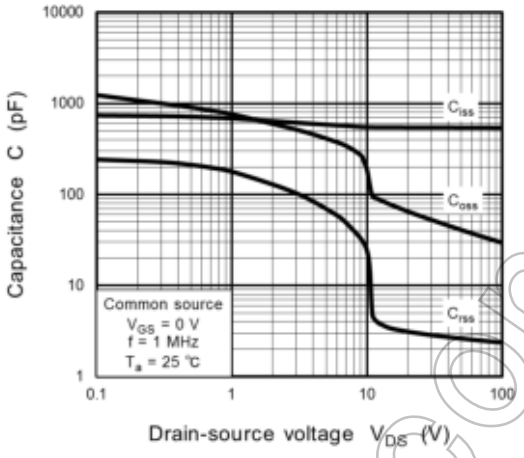


Fig. 8.9 C - V_{DS}

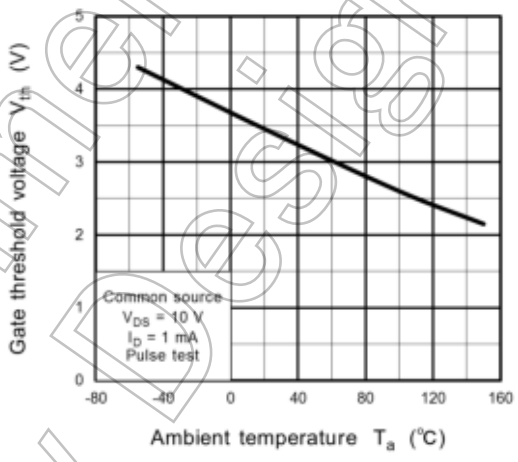


Fig. 8.10 V_{th} - T_a

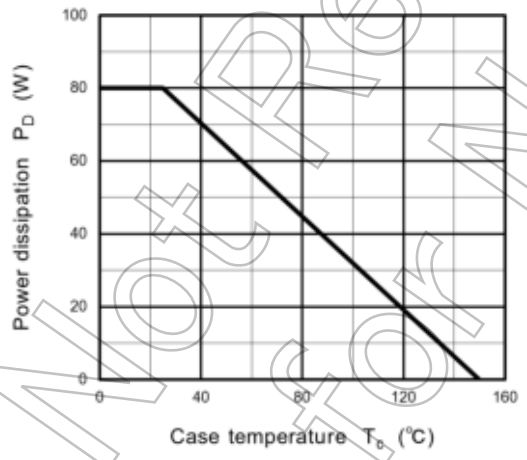


Fig. 8.11 P_D - T_c
(Guaranteed Maximum)

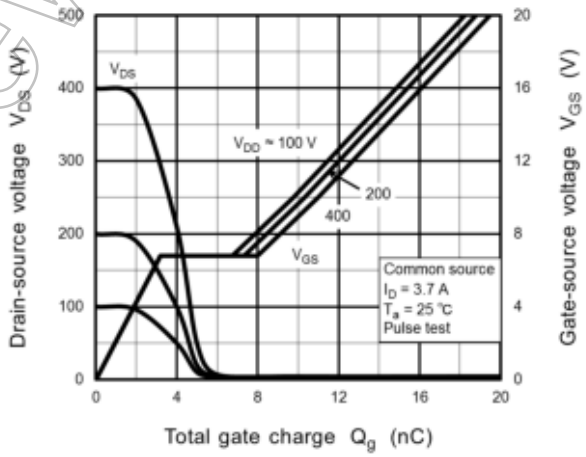


Fig. 8.12 Dynamic Input/Output Characteristics

Not Recommended for New Design

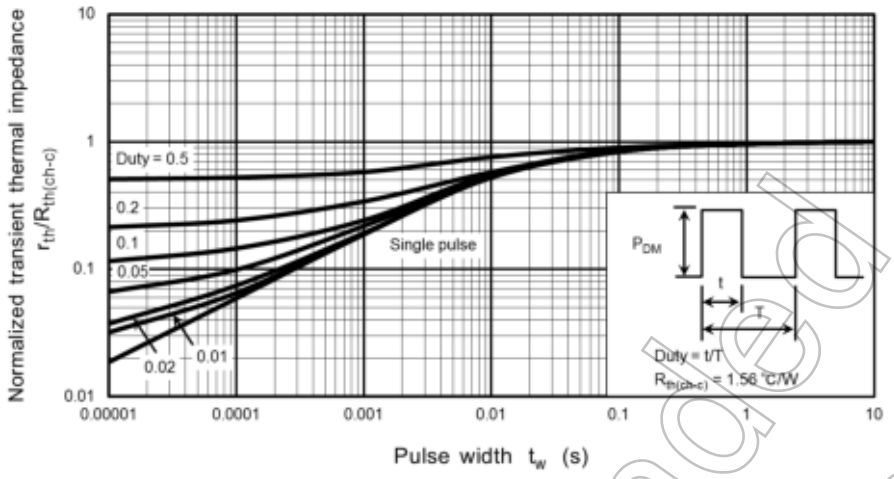


Fig. 8.13 $r_{th}/R_{th(ch-c)}$ - t_w (Guaranteed Maximum)

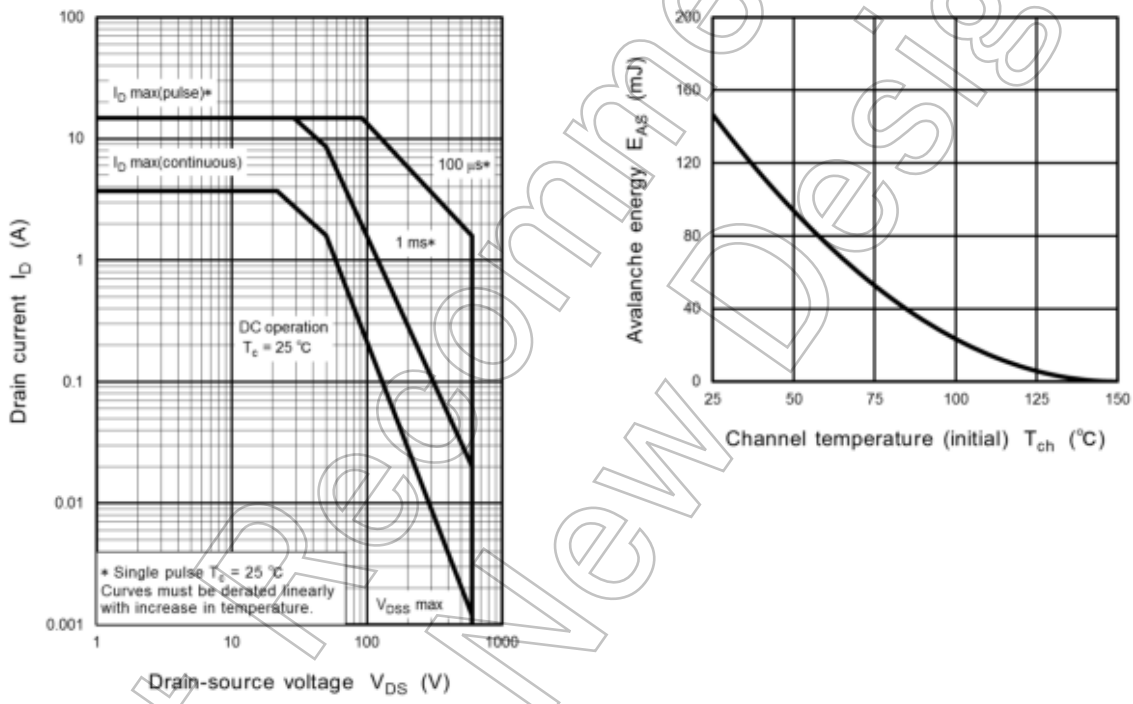
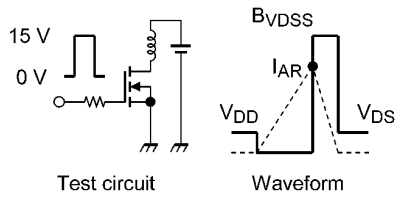


Fig. 8.14 Safe Operating Area (Guaranteed Maximum)

Fig. 8.15 E_{AS} - T_{ch} (Guaranteed Maximum)



$R_G = 25\ \Omega$, $V_{DD} = 90$ V $\quad E_{AS} = \frac{1}{2} \cdot L \cdot I_{AR}^2 \cdot \left(\frac{B_{VDSS}}{B_{VDSS} - V_{DD}} \right)$

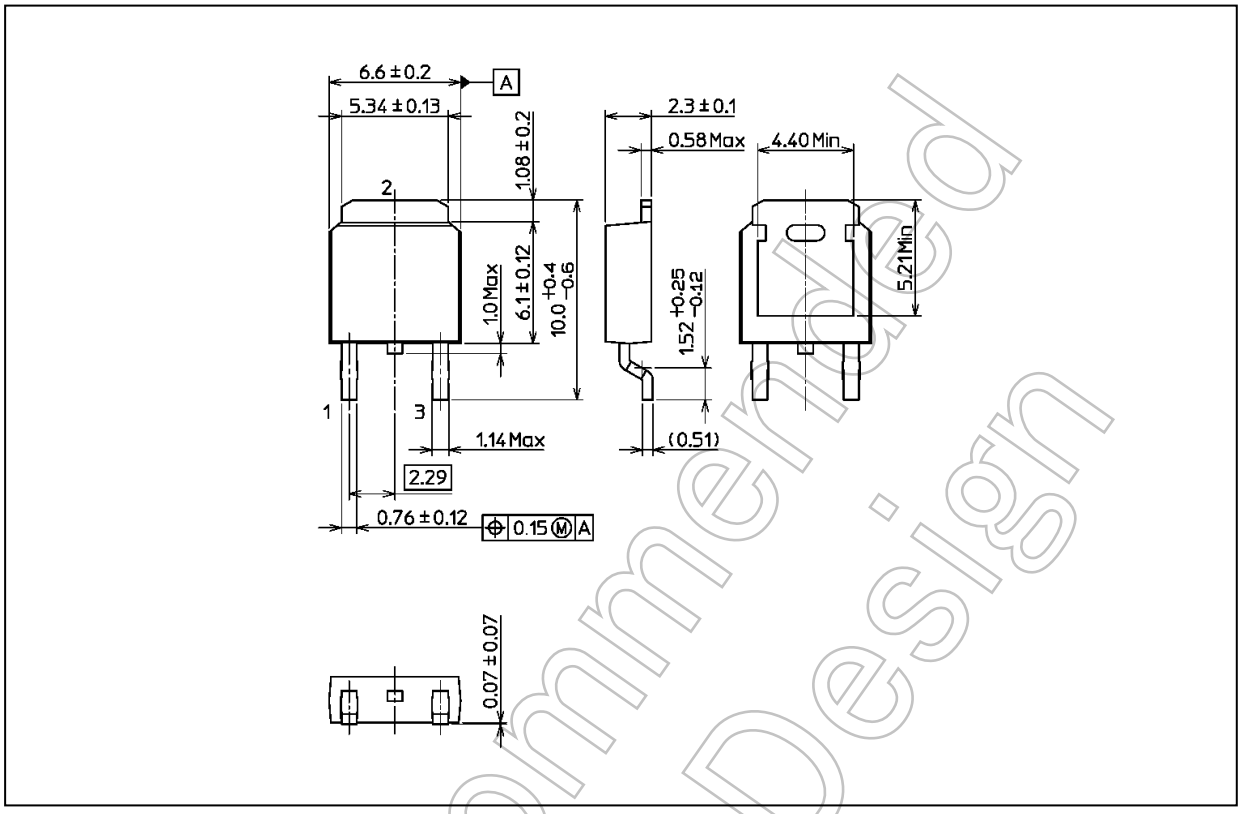
Fig. 8.16 Test Circuit/Waveform

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

Not Recommended for New Design

Package Dimensions

Unit: mm



Both products are compliant with the JEDEC: TO-252 Package specification. Please contact the Toshiba sales representative for further details.

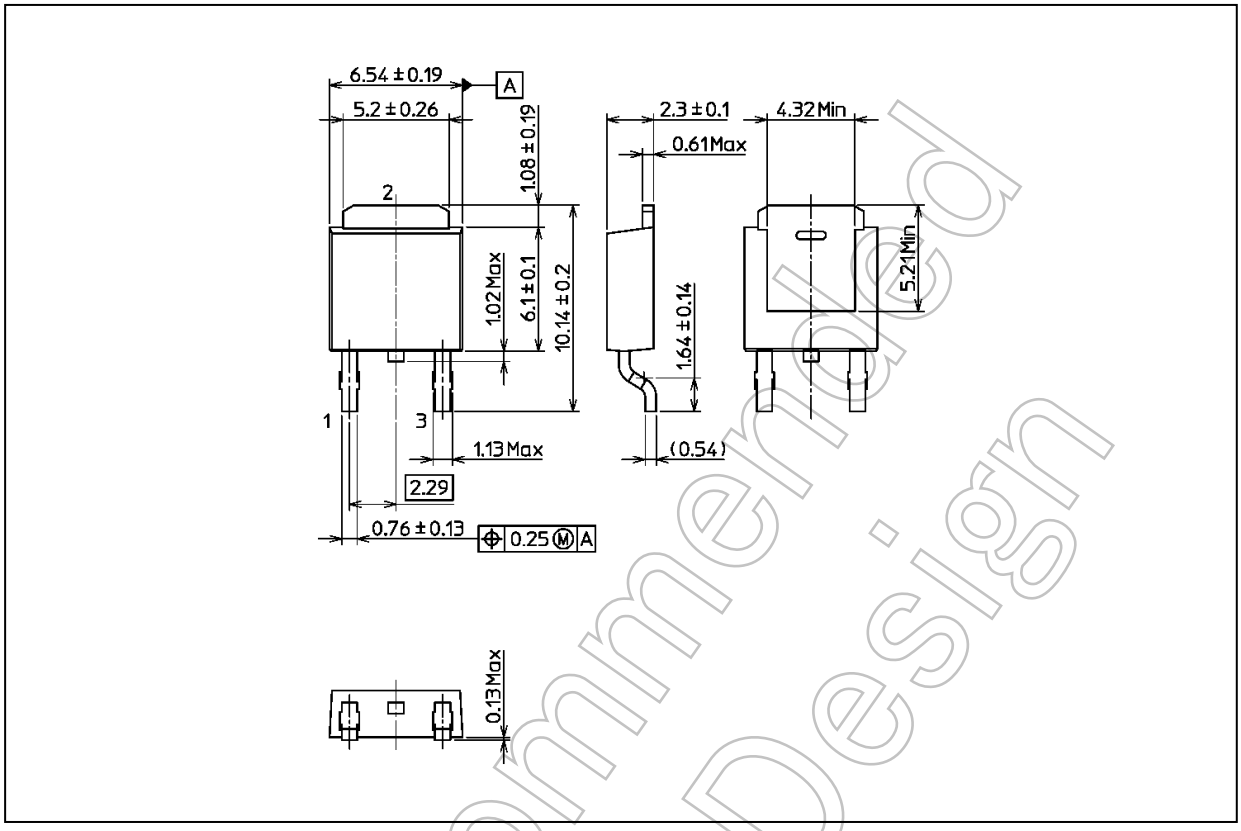
Weight: 0.36 g (typ.)

Package Name(s)
JEDEC: TO-252
TOSHIBA: 2-7K1S
Nickname: DPAK

Not Recommended for New Design

Package Dimensions

Unit: mm



Both products are compliant with the JEDEC: TO-252 Package specification. Please contact the Toshiba sales representative for further details.

Weight: 0.389 g (typ.)

Package Name(s)
JEDEC: TO-252
TOSHIBA: 2-7N1S
Nickname: DPAK

Not Recommended for New Design

RESTRICTIONS ON PRODUCT USE

- Toshiba Corporation, and its subsidiaries and affiliates (collectively "TOSHIBA"), reserve the right to make changes to the information in this document, and related hardware, software and systems (collectively "Product") without notice.
- This document and any information herein may not be reproduced without prior written permission from TOSHIBA. Even with TOSHIBA's written permission, reproduction is permissible only if reproduction is without alteration/omission.
- Though TOSHIBA works continually to improve Product's quality and reliability, Product can malfunction or fail. Customers are responsible for complying with safety standards and for providing adequate designs and safeguards for their hardware, software and systems which minimize risk and avoid situations in which a malfunction or failure of Product could cause loss of human life, bodily injury or damage to property, including data loss or corruption. Before customers use the Product, create designs including the Product, or incorporate the Product into their own applications, customers must also refer to and comply with (a) the latest versions of all relevant TOSHIBA information, including without limitation, this document, the specifications, the data sheets and application notes for Product and the precautions and conditions set forth in the "TOSHIBA Semiconductor Reliability Handbook" and (b) the instructions for the application with which the Product will be used with or for. Customers are solely responsible for all aspects of their own product design or applications, including but not limited to (a) determining the appropriateness of the use of this Product in such design or applications; (b) evaluating and determining the applicability of any information contained in this document, or in charts, diagrams, programs, algorithms, sample application circuits, or any other referenced documents; and (c) validating all operating parameters for such designs and applications. **TOSHIBA ASSUMES NO LIABILITY FOR CUSTOMERS' PRODUCT DESIGN OR APPLICATIONS.**
- **PRODUCT IS NEITHER INTENDED NOR WARRANTED FOR USE IN EQUIPMENTS OR SYSTEMS THAT REQUIRE EXTRAORDINARILY HIGH LEVELS OF QUALITY AND/OR RELIABILITY, AND/OR A MALFUNCTION OR FAILURE OF WHICH MAY CAUSE LOSS OF HUMAN LIFE, BODILY INJURY, SERIOUS PROPERTY DAMAGE AND/OR SERIOUS PUBLIC IMPACT ("UNINTENDED USE").** Except for specific applications as expressly stated in this document, Unintended Use includes, without limitation, equipment used in nuclear facilities, equipment used in the aerospace industry, Class 3 medical devices, equipment used for automobiles, and military vehicles and munitions. **IF YOU USE PRODUCT FOR UNINTENDED USE, TOSHIBA ASSUMES NO LIABILITY FOR PRODUCT.** For details, please contact your TOSHIBA sales representative or contact us via our website.
- Do not disassemble, analyze, reverse-engineer, alter, modify, translate or copy Product, whether in whole or in part.
- Product shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable laws or regulations.
- The information contained herein is presented only as guidance for Product use. No responsibility is assumed by TOSHIBA for any infringement of patents or any other intellectual property rights of third parties that may result from the use of Product. No license to any intellectual property right is granted by this document, whether express or implied, by estoppel or otherwise.
- **ABSENT A WRITTEN SIGNED AGREEMENT, EXCEPT AS PROVIDED IN THE RELEVANT TERMS AND CONDITIONS OF SALE FOR PRODUCT, AND TO THE MAXIMUM EXTENT ALLOWABLE BY LAW, TOSHIBA (1) ASSUMES NO LIABILITY WHATSOEVER, INCLUDING WITHOUT LIMITATION, INDIRECT, CONSEQUENTIAL, SPECIAL, OR INCIDENTAL DAMAGES OR LOSS, INCLUDING WITHOUT LIMITATION, LOSS OF PROFITS, LOSS OF OPPORTUNITIES, BUSINESS INTERRUPTION AND LOSS OF DATA, AND (2) DISCLAIMS ANY AND ALL EXPRESS OR IMPLIED WARRANTIES AND CONDITIONS RELATED TO SALE, USE OF PRODUCT, OR INFORMATION, INCLUDING WARRANTIES OR CONDITIONS OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, ACCURACY OF INFORMATION, OR NONINFRINGEMENT.**
- Do not use or otherwise make available Product or related software or technology for any military purposes, including without limitation, for the design, development, use, stockpiling or manufacturing of nuclear, chemical, or biological weapons or missile technology products (mass destruction weapons). Product and related software and technology may be controlled under the applicable export laws and regulations including, without limitation, the Japanese Foreign Exchange and Foreign Trade Law and the U.S. Export Administration Regulations. Export and re-export of Product or related software or technology are strictly prohibited except in compliance with all applicable export laws and regulations.
- Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. Please use Product in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive. **TOSHIBA ASSUMES NO LIABILITY FOR DAMAGES OR LOSSES OCCURRING AS A RESULT OF NONCOMPLIANCE WITH APPLICABLE LAWS AND REGULATIONS.**

Not Recommended for New Design