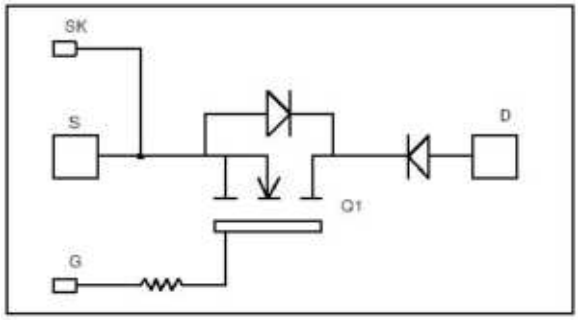
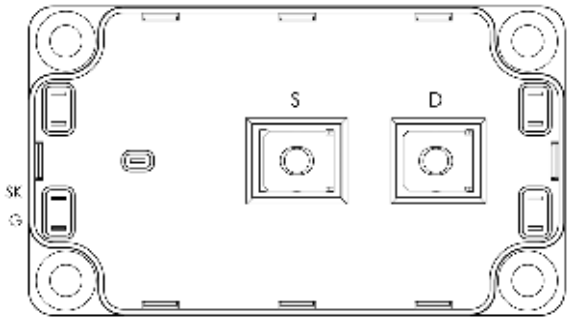
# APTM100UM65DAG

## *Single switch with Series diode MOSFET Power Module*

**$V_{DSS}$ = 1000V**
**$R_{DSon}$ = 65mΩ typ @ Tj = 25°C**
**$I_D$ = 145A @ Tc = 25°C**




**Application**

- Zero Current Switching resonant mode

**Features**

- Power MOS 7® MOSFETs
  - Low $R_{DSon}$
  - Low input and Miller capacitance
  - Low gate charge
  - Avalanche energy rated
  - Very rugged
- Kelvin source for easy drive
- Very low stray inductance
  - Symmetrical design
  - M5 power connectors
- High level of integration
- AlN substrate for improved thermal performance

**Benefits**

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Low profile
- RoHS Compliant

## Absolute maximum ratings

| *Symbol* | *Parameter* | | *Max ratings* | *Unit* |
|---|---|---|---|---|
| $V_{DSS}$ | Drain - Source Breakdown Voltage | | 1000 | V |
| $I_D$ | Continuous Drain Current | $T_c$ = 25°C | 145 | A |
| | | $T_c$ = 80°C | 110 | |
| $I_{DM}$ | Pulsed Drain current | | 580 | |
| $V_{GS}$ | Gate - Source Voltage | | ±30 | V |
| $R_{DSon}$ | Drain - Source ON Resistance | | 78 | mΩ |
| $P_D$ | Maximum Power Dissipation | $T_c$ = 25°C | 3250 | W |
| $I_{AR}$ | Avalanche current (repetitive and non repetitive) | | 30 | A |
| $E_{AR}$ | Repetitive Avalanche Energy | | 50 | mJ |
| $E_{AS}$ | Single Pulse Avalanche Energy | | 3200 | |

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com

**All ratings @ $T_j$ = 25°C unless otherwise specified**

## Electrical Characteristics

| Symbol | Characteristic | Test Conditions | | Min | Typ | Max | Unit |
|---|---|---|---|---|---|---|---|
| $I_{DSS}$ | Zero Gate Voltage Drain Current | $V_{GS}$ = 0V, $V_{DS}$= 1000V | $T_j$ = 25°C | | | 400 | µA |
| | | $V_{GS}$ = 0V, $V_{DS}$= 800V | $T_j$ = 125°C | | | 2 | mA |
| $R_{DS(on)}$ | Drain – Source on Resistance | $V_{GS}$ = 10V, $I_D$ = 72.5A | | | 65 | 78 | mΩ |
| $V_{GS(th)}$ | Gate Threshold Voltage | $V_{GS}$ = $V_{DS}$, $I_D$ = 20mA | | 3 | | 5 | V |
| $I_{GSS}$ | Gate – Source Leakage Current | $V_{GS}$ = ±30 V, $V_{DS}$ = 0V | | | | ±400 | nA |

## Dynamic Characteristics

| Symbol | Characteristic | Test Conditions | Min | Typ | Max | Unit |
|---|---|---|---|---|---|---|
| $C_{iss}$ | Input Capacitance | $V_{GS}$ = 0V<br>$V_{DS}$ = 25V<br>f = 1MHz | | 28.5 | | nF |
| $C_{oss}$ | Output Capacitance | | | 5.08 | | |
| $C_{rss}$ | Reverse Transfer Capacitance | | | 0.9 | | |
| $Q_g$ | Total gate Charge | $V_{GS}$ = 10V<br>$V_{Bus}$ = 500V<br>$I_D$ = 145A | | 1068 | | nC |
| $Q_{gs}$ | Gate – Source Charge | | | 136 | | |
| $Q_{gd}$ | Gate – Drain Charge | | | 692 | | |
| $T_{d(on)}$ | Turn-on Delay Time | $V_{GS}$ = 15V<br>$V_{Bus}$ = 500V<br>$I_D$ = 145A<br>$R_G$ = 0.75Ω | | 18 | | ns |
| $T_r$ | Rise Time | | | 14 | | |
| $T_{d(off)}$ | Turn-off Delay Time | | | 140 | | |
| $T_f$ | Fall Time | | | 55 | | |
| $E_{on}$ | Turn-on Switching Energy | **Inductive switching @ 25°C**<br>$V_{GS}$ = 15V, $V_{Bus}$ = 670V<br>$I_D$ = 145A, $R_G$ = 0.75Ω | | 4.8 | | mJ |
| $E_{off}$ | Turn-off Switching Energy | | | 2.9 | | |
| $E_{on}$ | Turn-on Switching Energy | **Inductive switching @ 125°C**<br>$V_{GS}$ = 15V, $V_{Bus}$ = 670V<br>$I_D$ = 145A, $R_G$ = 0.75Ω | | 8 | | mJ |
| $E_{off}$ | Turn-off Switching Energy | | | 3.9 | | |

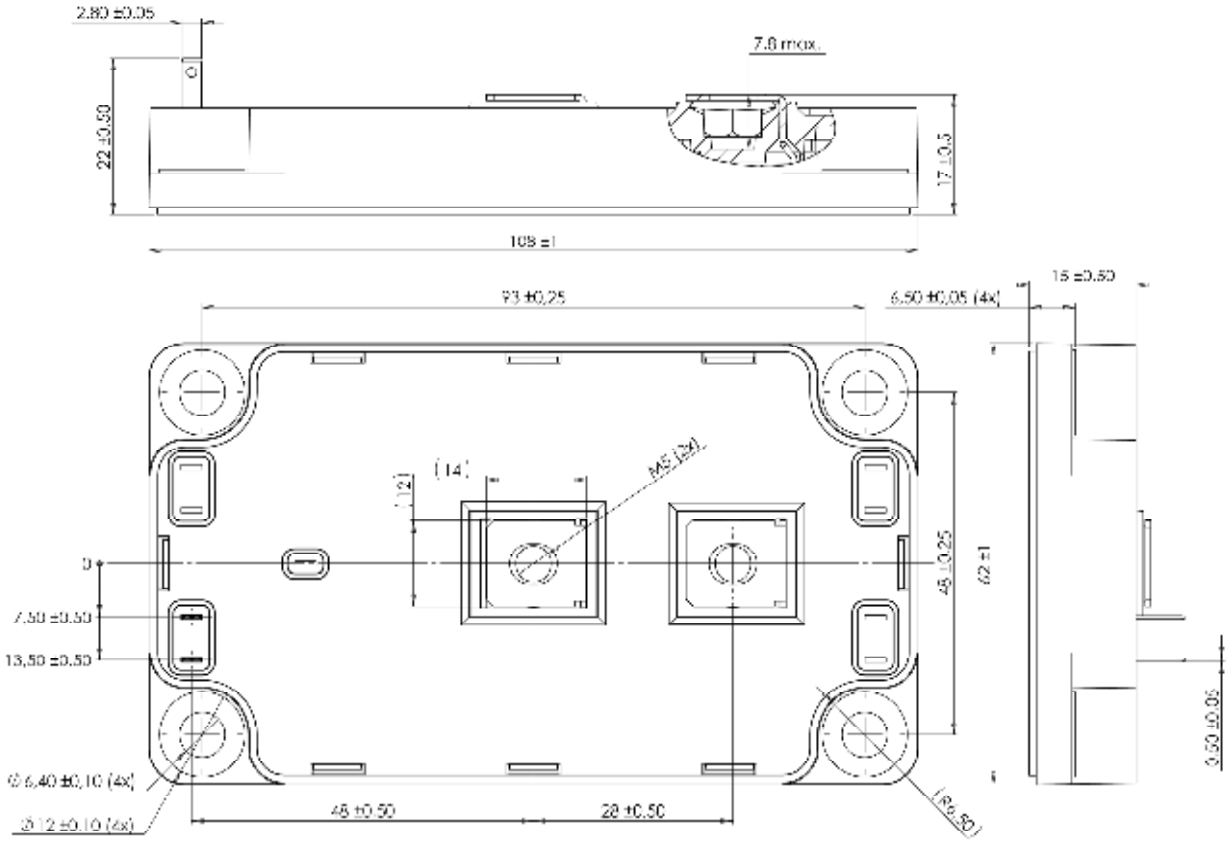
## Series diode ratings and characteristics

| Symbol | Characteristic | Test Conditions | | Min | Typ | Max | Unit |
|---|---|---|---|---|---|---|---|
| $V_{RRM}$ | Maximum Peak Repetitive Reverse Voltage | | | 1000 | | | V |
| $I_{RM}$ | Maximum Reverse Leakage Current | $V_R$=1000V | $T_j$ = 25°C | | | 750 | µA |
| | | | $T_j$ = 125°C | | | 1000 | |
| $I_F$ | DC Forward Current | | Tc = 80°C | | 240 | | A |
| $V_F$ | Diode Forward Voltage | $I_F$ = 240A | | | 2 | 2.5 | V |
| | | $I_F$ = 480A | | | 2.2 | | |
| | | $I_F$ = 240A | $T_j$ = 125°C | | 1.7 | | |
| $t_{rr}$ | Reverse Recovery Time | $I_F$ = 240A<br>$V_R$ = 667V<br>di/dt = 800A/µs | $T_j$ = 25°C | | 280 | | ns |
| | | | $T_j$ = 125°C | | 350 | | |
| $Q_{rr}$ | Reverse Recovery Charge | | $T_j$ = 25°C | | 3.04 | | µC |
| | | | $T_j$ = 125°C | | 14.4 | | |

## Thermal and package characteristics

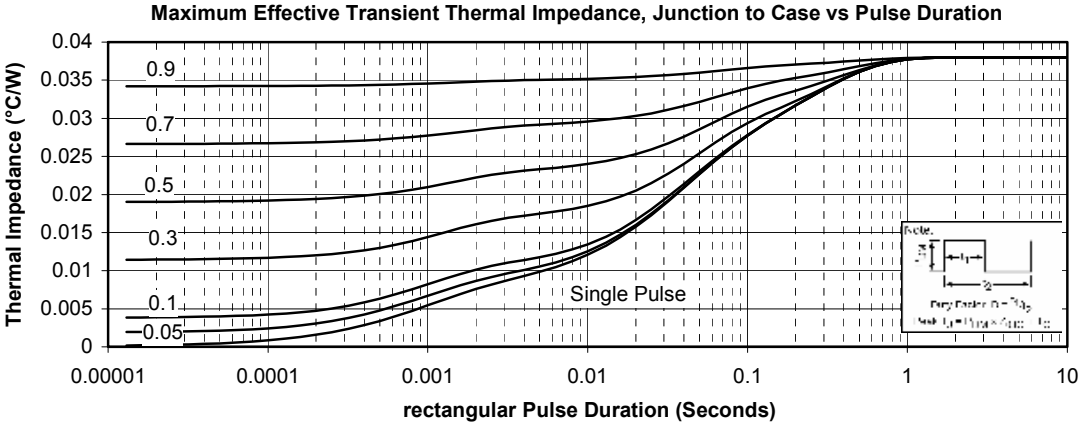
| Symbol | Characteristic | | | Min | Typ | Max | Unit |
|---|---|---|---|---|---|---|---|
| $R_{thJC}$ | Junction to Case Thermal Resistance | | Transistor | | | 0.038 | °C/W |
| | | | Series diode | | | 0.23 | |
| $V_{ISOL}$ | RMS Isolation Voltage, any terminal to case t =1 min, 50/60Hz | | | 4000 | | | V |
| $T_J$ | Operating junction temperature range | | | -40 | | 150 | °C |
| $T_{STG}$ | Storage Temperature Range | | | -40 | | 125 | |
| $T_C$ | Operating Case Temperature | | | -40 | | 100 | |
| Torque | Mounting torque | To Heatsink | M6 | 3 | | 5 | N.m |
| | | For teminals | M5 | 2 | | 3.5 | |
| Wt | Package Weight | | | | | 300 | g |

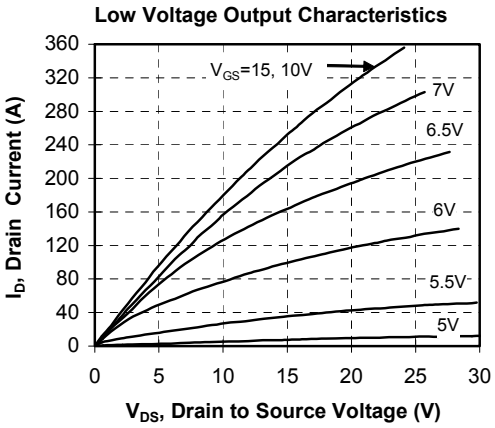
## SP6 Package outline (dimensions in mm)

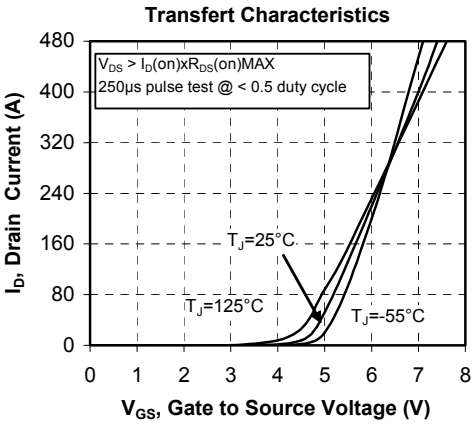


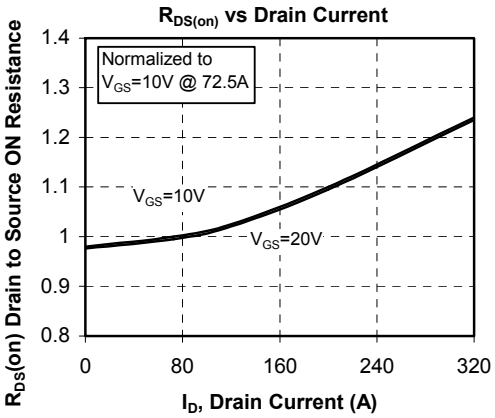

See application note APT0601 - Mounting Instructions for SP6 Power Modules on www.microsemi.com

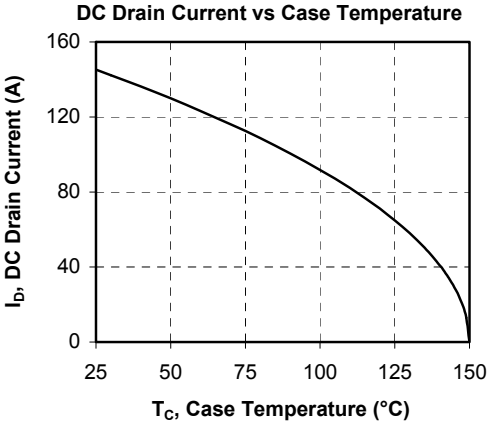
## Typical Performance Curve

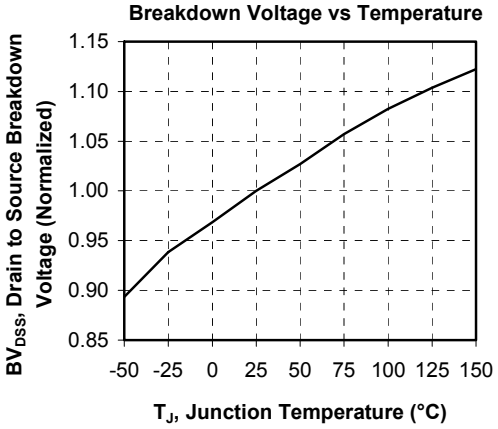


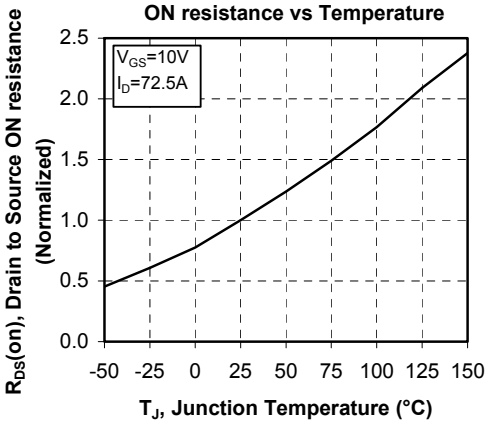



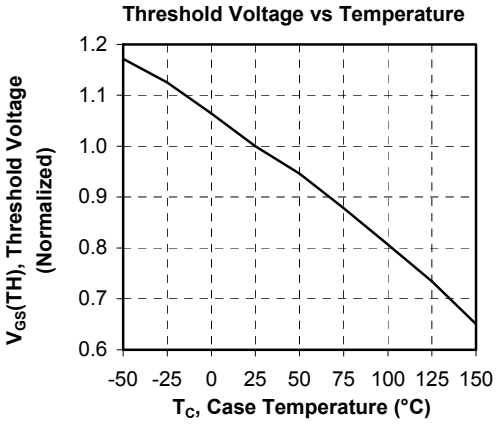



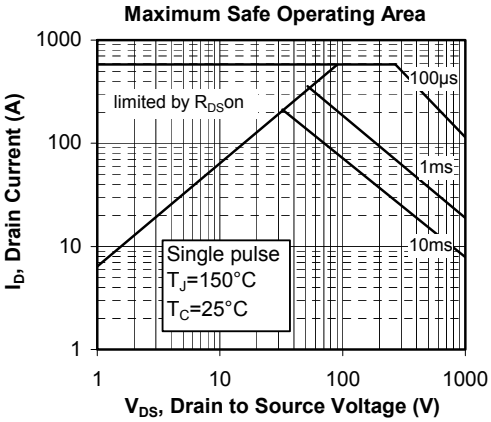



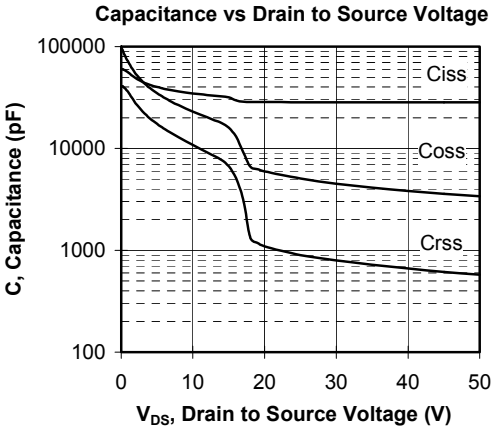



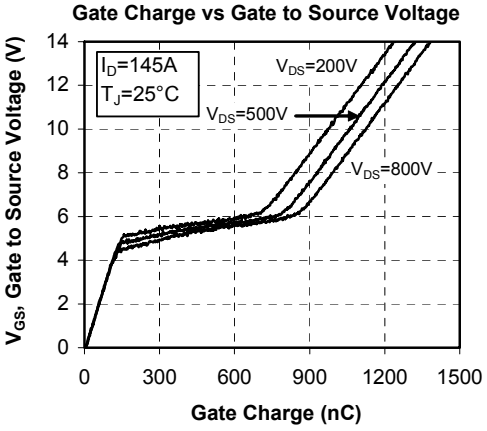

Breakdown Voltage vs Temperature
BVDSS, Drain to Source Breakdown Voltage (Normalized)
1.15
1.10
1.05
1.00
0.95
0.90
0.85
-50 -25 0 25 50 75 100 125 150
TJ, Junction Temperature (°C)

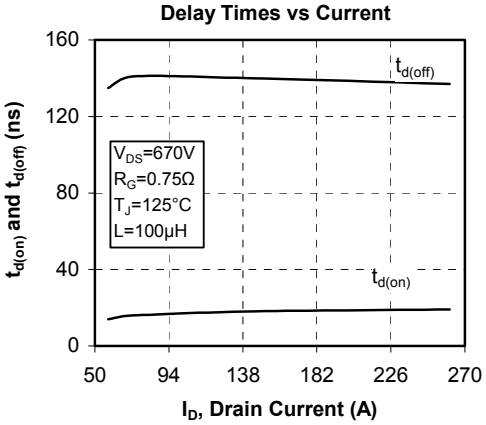

ON resistance vs Temperature
VGS=10V
ID=72.5A
RDS(on), Drain to Source ON resistance (Normalized)
2.5
2.0
1.5
1.0
0.5
0.0
-50 -25 0 25 50 75 100 125 150
TJ, Junction Temperature (°C)

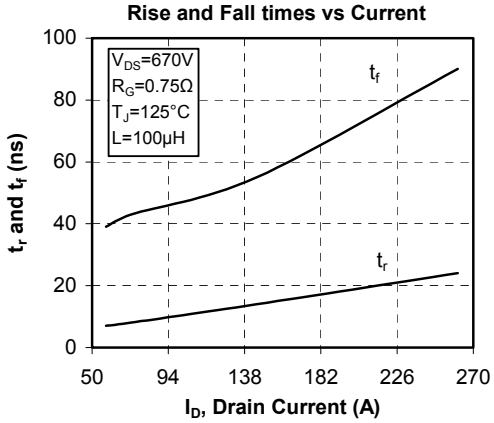

Threshold Voltage vs Temperature
VGS(TH), Threshold Voltage (Normalized)
1.2
1.1
1.0
0.9
0.8
0.7
0.6
-50 -25 0 25 50 75 100 125 150
TC, Case Temperature (°C)

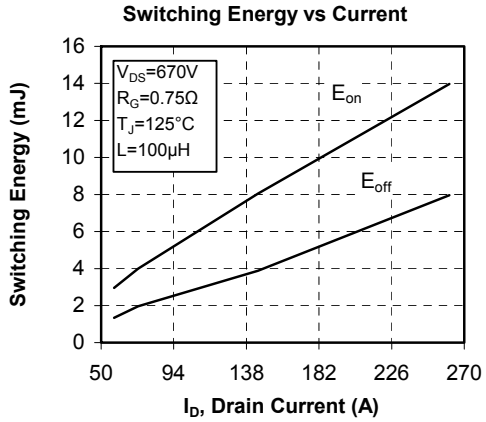

Maximum Safe Operating Area
ID, Drain Current (A)
1000
100
10
1
limited by RDSon
100µs
1ms
10ms
Single pulse
TJ=150°C
TC=25°C
1 10 100 1000
VDS, Drain to Source Voltage (V)

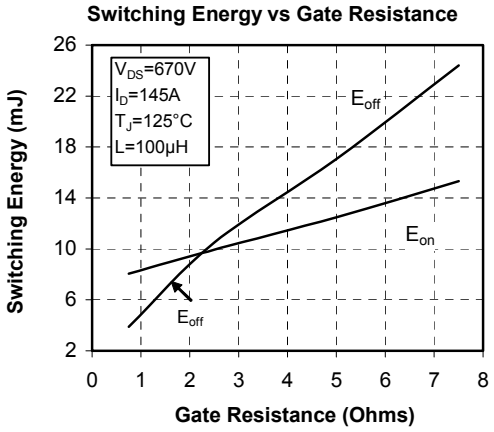

Capacitance vs Drain to Source Voltage
C, Capacitance (pF)
100000
10000
1000
100
Ciss
Coss
Crss
0 10 20 30 40 50
VDS, Drain to Source Voltage (V)

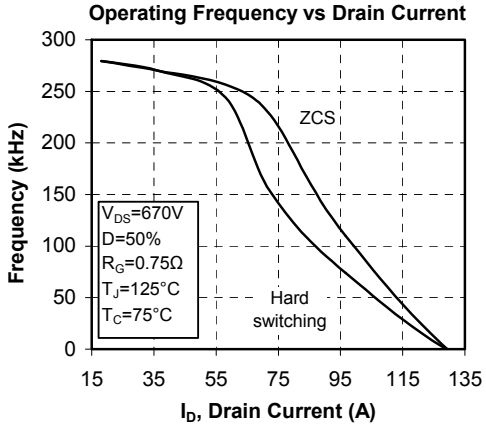

Gate Charge vs Gate to Source Voltage
ID=145A
TJ=25°C
VDS=200V
VDS=500V
VDS=800V
VGS, Gate to Source Voltage (V)
14
12
10
8
6
4
2
0
0 300 600 900 1200 1500
Gate Charge (nC)

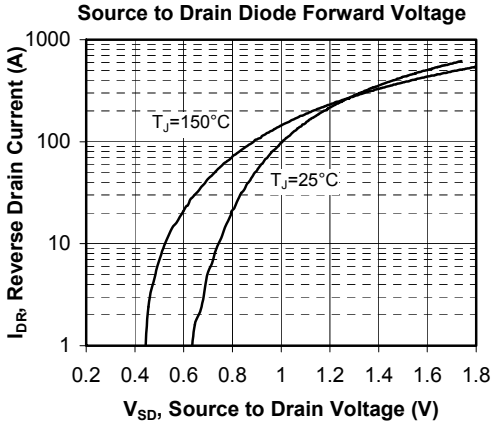

Delay Times vs Current
$t_{d(on)}$ and $t_{d(off)}$ (ns)
$I_D$, Drain Current (A)
$t_{d(off)}$
$t_{d(on)}$
$V_{DS}$=670V
$R_G$=0.75Ω
$T_J$=125°C
L=100µH


Rise and Fall times vs Current
$t_r$ and $t_f$ (ns)
$I_D$, Drain Current (A)
$t_f$
$t_r$
$V_{DS}$=670V
$R_G$=0.75Ω
$T_J$=125°C
L=100µH


Switching Energy vs Current
Switching Energy (mJ)
$I_D$, Drain Current (A)
$E_{on}$
$E_{off}$
$V_{DS}$=670V
$R_G$=0.75Ω
$T_J$=125°C
L=100µH


Switching Energy vs Gate Resistance
Switching Energy (mJ)
Gate Resistance (Ohms)
$E_{off}$
$E_{on}$
$E_{off}$
$V_{DS}$=670V
$I_D$=145A
$T_J$=125°C
L=100µH


Operating Frequency vs Drain Current
Frequency (kHz)
$I_D$, Drain Current (A)
ZCS
Hard
switching
$V_{DS}$=670V
D=50%
$R_G$=0.75Ω
$T_J$=125°C
$T_C$=75°C


Source to Drain Diode Forward Voltage
$I_{DR}$, Reverse Drain Current (A)
$V_{SD}$, Source to Drain Voltage (V)
$T_J$=150°C
$T_J$=25°C

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