1. Scope

The present specifications shall apply to an EK19.

2. Outline

Type	Silicon Schottky Barrier Diode
Structure	Resin Molded
Applications	High Frequency Rectification

3. Flammability

UL94V-0(Equivalent)

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4. Absolute maximum ratings

No.	Item	Symbol	Unit	Rating	Conditions
1	Transient Peak Reverse Voltage	V_{RSM}	V	90	
2	Peak Reverse Voltage	V_{RM}	V	90	
3	Average Forward Current	$I_{F(AV)}$	A	1.5	Refer to Derating of 7
4	Peak Surge Forward Current	I_{FSM}	A	40	10msec. Half sinewave, one shot
5	I^2t Limiting Value	I^2t	A^2s	8.0	1msec≤t≤10msec
6	Junction Temperature	T_j	°C	-40~+150	
7	Storage Temperature	T_{stg}	°C	-40~+150	

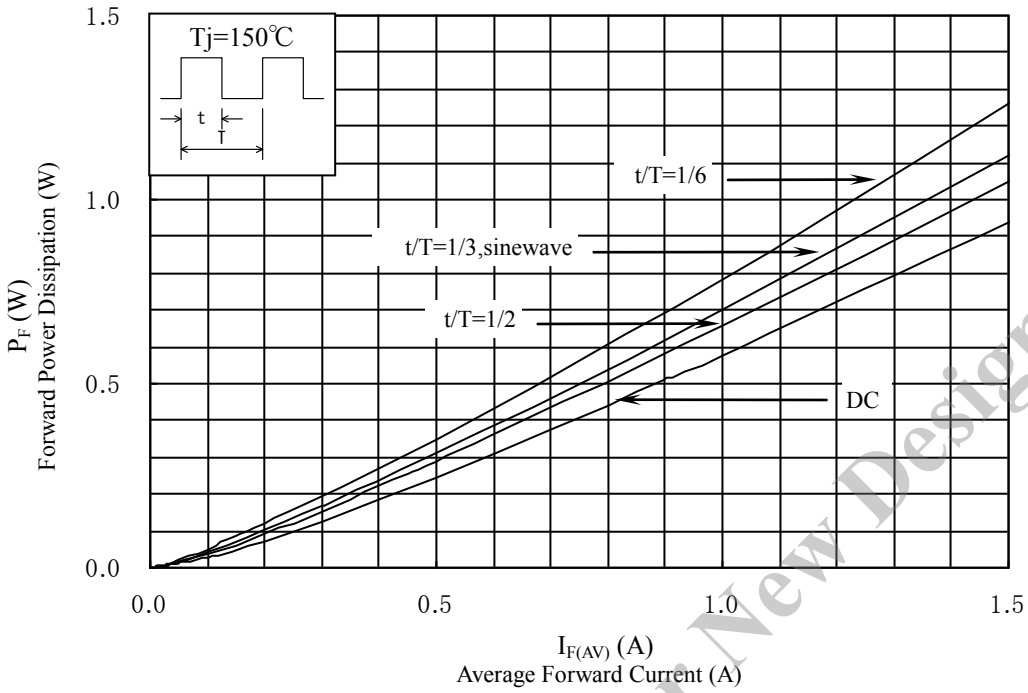
5. Electrical characteristics (Ta=25°C ， unless otherwise specified)

No.	Item	Symbol	Unit	Value	Conditions
1	Forward Voltage Drop	V_F	V	0.81 max.	I_F=1.5A
2	Reverse Leakage Current	I_R	mA	2.0 max.	$V_R=V_{RM}$
3	Reverse Leakage Current Under High Temperature	$H\cdot I_{R1}$	mA	15 max.	$V_R=V_{RM}$, T_j=125°C
		$H\cdot I_{R2}$	mA	55 max.	$V_R=V_{RM}$, T_j=150°C
4	Thermal Resistance	$R_{th(j-l)}$	°C /W	17 max.	Between Junction and Lead

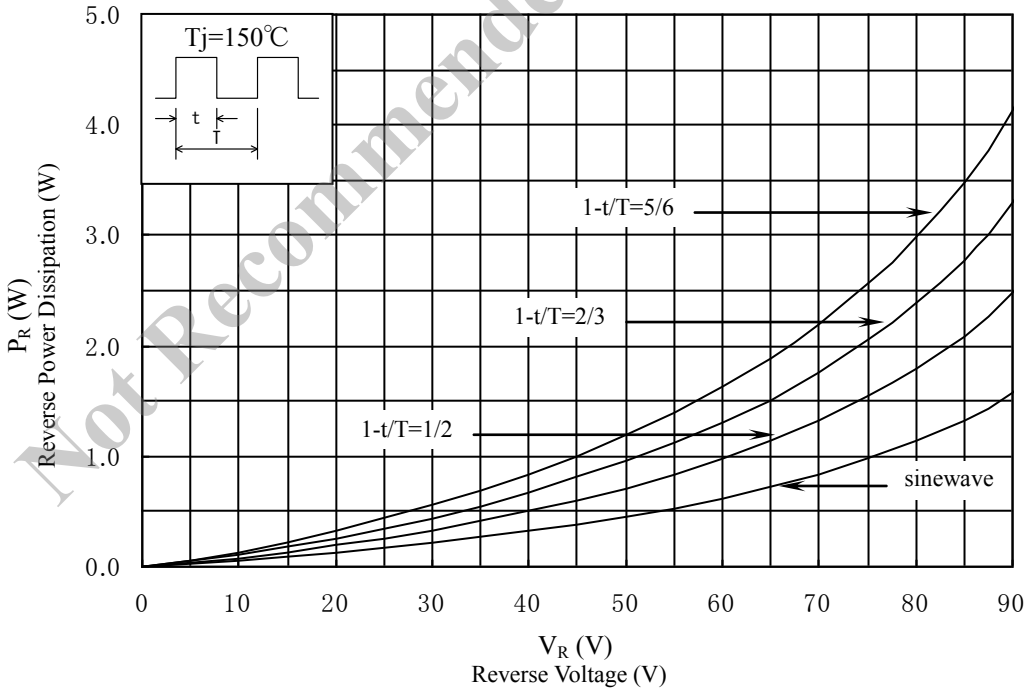
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6. Characteristics

$I_{F(AV)}$ – P_F

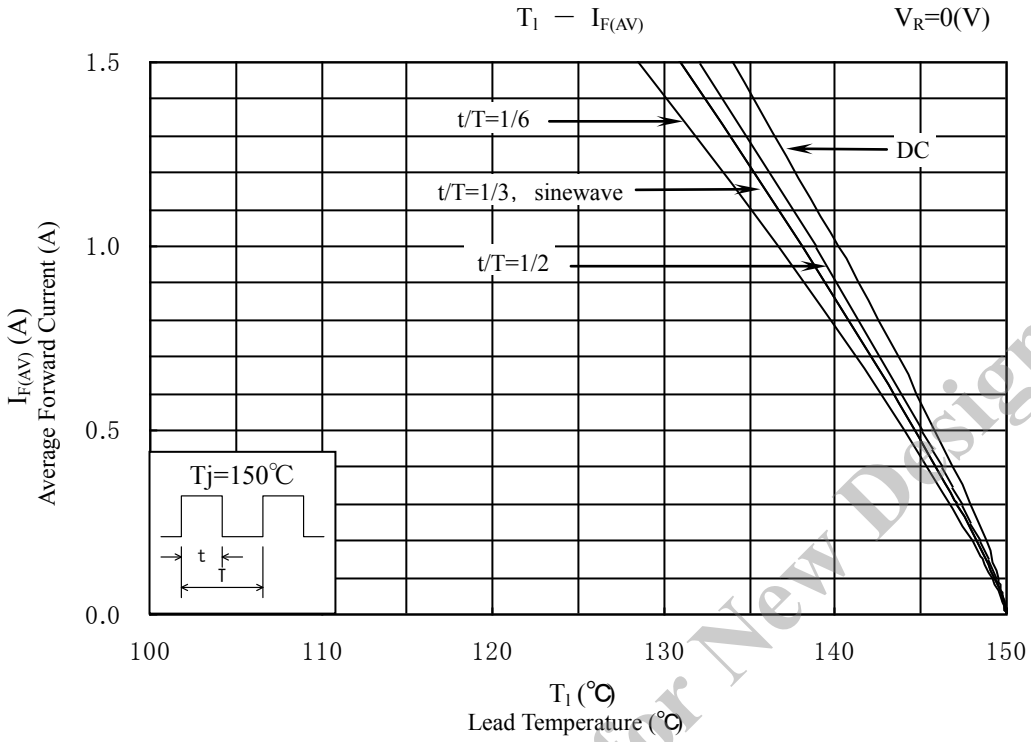


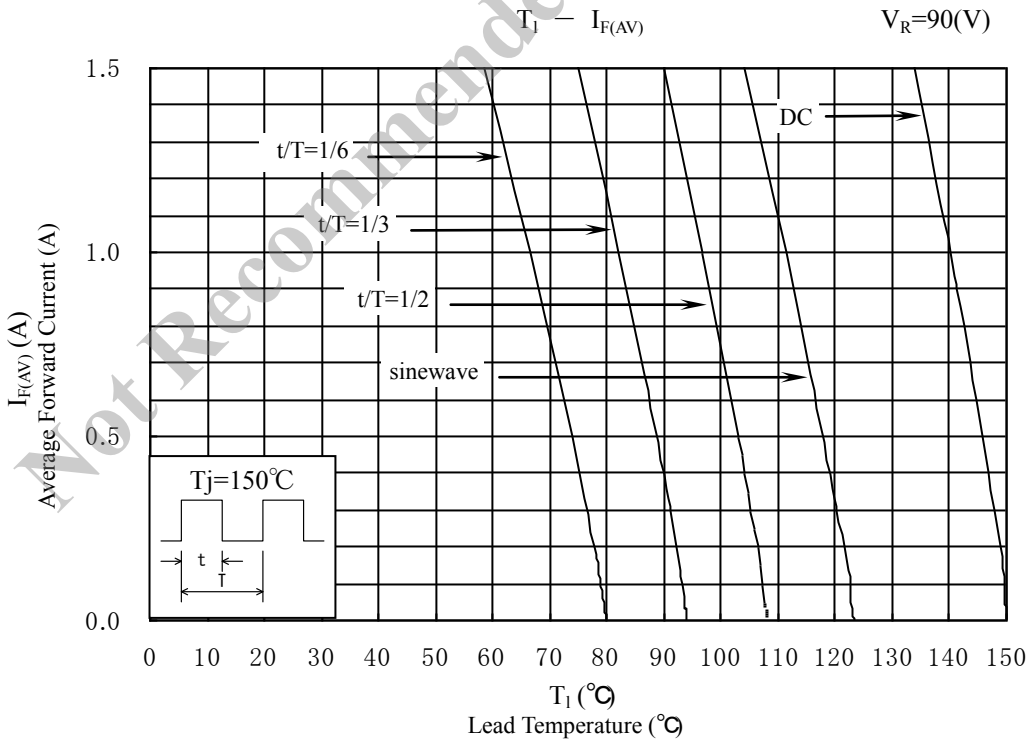
V_R – P_R



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7. Derating

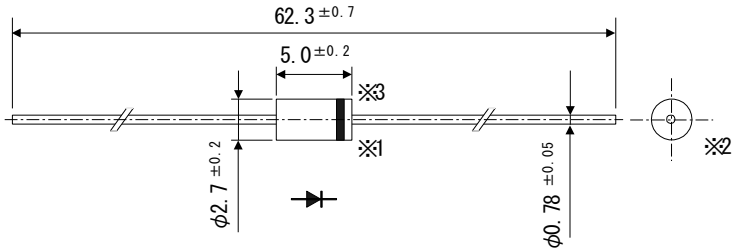




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8. Package information

8-1 Package type, physical dimensions and material



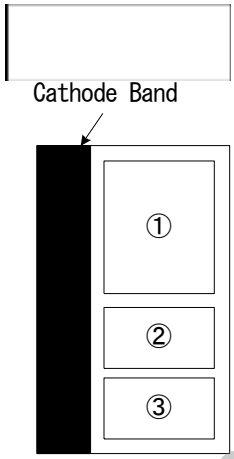
※1 The allowance position of Body against the center of whole lead wire is 0.5mm(max.)
※2 The centric allowance of lead wire against center of physical body is 0.2mm(max.)
※3 The burr may exit up to 2mm from the body of lead

Dimensions in mm

8-2 Appearance

The body shall be clean and shall not bear any stain, rust or flaw.

8-3 Marking



① Type number EK19 is abbreviated as $^{K1}_{9}$

② Lot number 1
First digit: Last digit of Year
Second digit: Month
From 1 to 9 for Jan. to Sep.
O for Oct., N for Nov., and D for Dec.

③ Lot number 2 (ten days)
・ : Top of the month
・・ : Middle of month
・・・ : End of month

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