1. Scope

The present specifications shall apply to an FMB-24.

2. Outline

Type	Silicon Schottky Barrier Diode
Structure	Resin Molded
Applications	High Frequency Rectification

3. Flammability

UL94V-0(Equivalent)

Not Recommended for New Designs

4. Absolute maximum ratings

No.	Item	Symbol	Unit	Rating	Conditions
1	Transient Peak Reverse Voltage	V_{RSM}	V	48	
2	Peak Reverse Voltage	V_{RM}	V	40	
3	Average Forward Current	$I_{F(AV)}$	A	4.0	Refer to Derating of 7
4	Peak Surge Forward Current	I_{FSM}	A	50	Half sinewave, one shot
5	I^2t Limiting Value	I^2t	A^2s	12.5	1msec≤t≤10msec
6	Junction Temperature	T_j	°C	-40 ~ +150	
7	Storage Temperature	T_{stg}	°C	-40 ~ +150	

No.1,2,4&5 show ratings per one chip.

5. Electrical characteristics (Ta=25°C , unless otherwise specified)

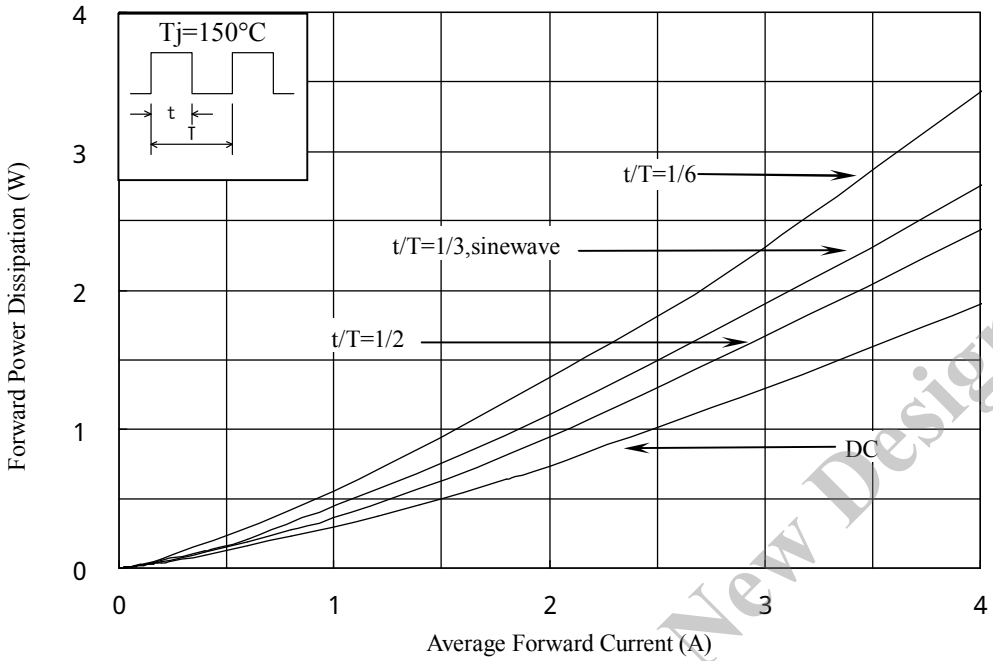
No.	Item	Symbol	Unit	Value	Conditions
1	Forward Voltage Drop	V_F	V	0.55 max.	I_F=2.0A
2	Reverse Leakage Current	I_R	mA	2.0 max.	$V_R=V_{RM}$
3	Reverse Leakage Current Under High Temperature	H-I_R	mA	20 max.	$V_R=V_{RM}$, T_j=125°C
				70 max.	$V_R=V_{RM}$, T_j=150°C
4	Thermal Resistance	$R_{th(j-c)}$	°C/W	4.0 max.	Between Junction and case

No.1,2,&3 show characteristics per one chip.

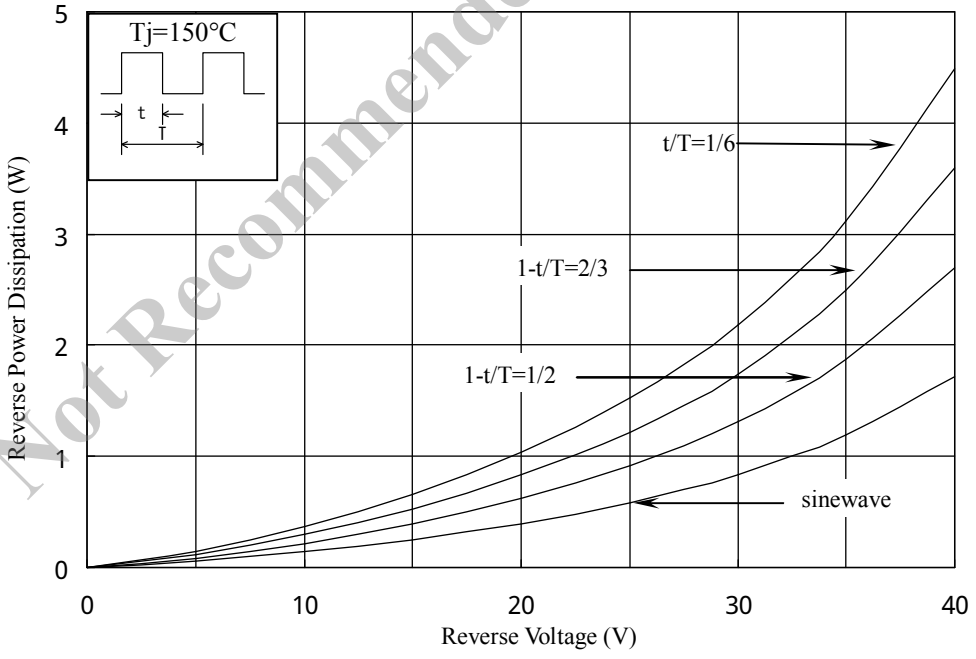
Not Recommended for New Designs

6. Characteristics

$I_{F(AV)}$ - P_F

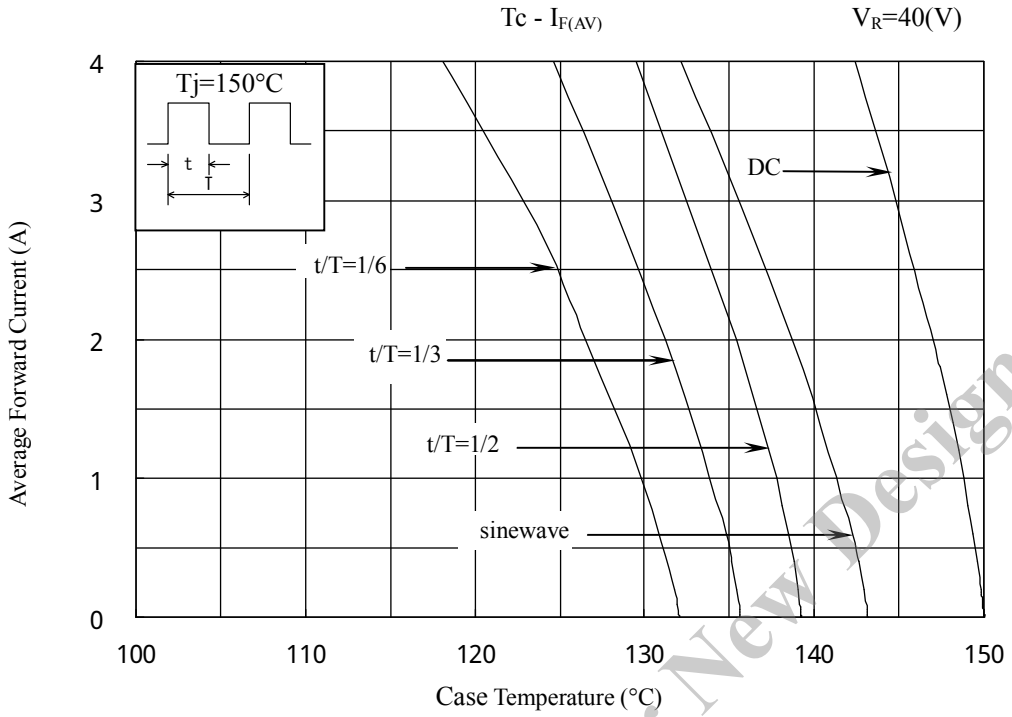


V_R - P_R



Not Recommended for New Designs

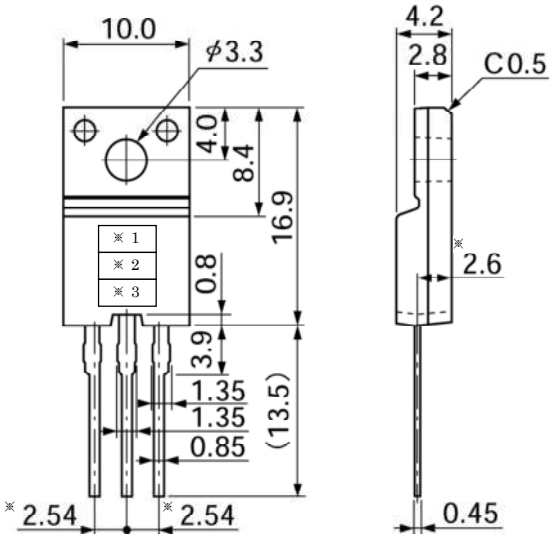
7. Derating



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8. Package information

8-1 Package type, physical dimensions and material



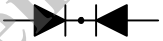
※: root dimension

tolerance ±0.2 Dimensions in mm

8-2 Appearance

The body shall be clean and shall not bear any stain, rust or flaw.

8-3 Marking

Type Name	Marking		
	*1 Type Name	*2 Polarity	*3 Lot number
FMB-24	FMB24		1st letter: Last digit of year 2nd letter: Month From 1 to 9 for Jan. to Sep., O for Oct., N for Nov., D for Dec. 3rd & 4th letter: Day ex. 4O13 (Oct.13, 2004)

Not Recommended for New Designs