LOW NOISE 150mA LDO REGULATOR

NO.EA-057-120206

OUTLINE

The R1111N Series are CMOS-based voltage regulator ICs with high output voltage accuracy, extremely low supply current, low ON-resistance, and high Ripple Rejection. Each of these voltage regulator ICs consists of a voltage reference unit, an error amplifier, resistors, a current limit circuit, and a chip enable circuit.

These ICs perform with low dropout voltage and a chip enable function. The line transient response and load transient response of the R1111N Series are excellent, thus these ICs are very suitable for the power supply for hand-held communication equipment.

The output voltage of these ICs is fixed with high accuracy. Since the package for these ICs is SOT-23-5 (Mini-mold) package , high density mounting of the ICs on boards is possible.

FEATURES

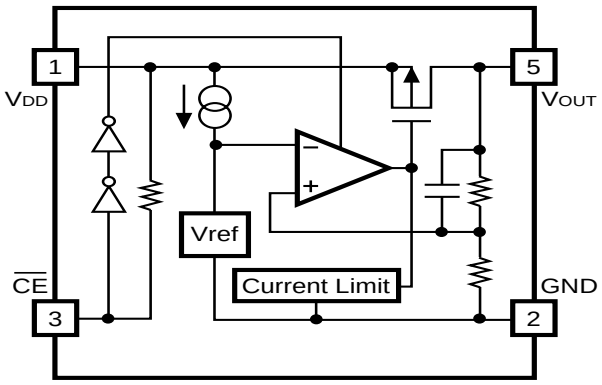
- Supply Current ..... Typ. 35μA
- Standby Mode ..... Typ. 0.1μA
- Dropout Voltage ..... Typ. 0.2V (I_{OUT}=100mA)
- Ripple Rejection ..... Typ. 70dB(f=1kHz)
- Output Voltage ..... 1.5V to 5.0V (0.1V steps)
  (For other voltages, please refer to MARK INFORMATIONS.)
- Output Voltage Accuracy ..... ±2.0%
- Low Temperature-Drift Coefficient of Output Voltage .. Typ. ±100ppm/°C
- Line Regulation ..... Typ. 0.05%/V
- Package SOT-23-5
- Built-in chip enable circuit (2 types; A: active “L”, B: active “H”)
- Built-in Fold Back Protection Circuit ..... Typ. 50mA (Current at short mode)
- Pin-out ..... Similar to the LP2980

APPLICATIONS

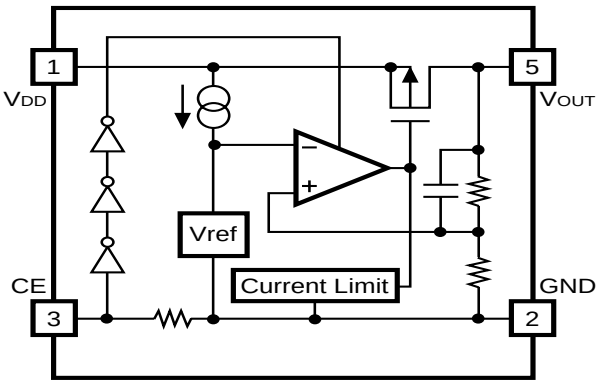
- Power source for cellular phones such as GSM, CDMA and various kinds of PCSs.
- Power source for electrical appliances such as cameras, VCRs and camcorders.
- Power source for battery-powered equipment.

BLOCK DIAGRAM

R1111Nxx1A



R1111Nxx1B



SELECTION GUIDE

The output voltage, the active type for the ICs can be selected at the user's request.

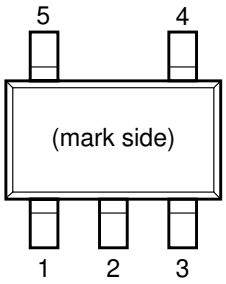
Product Name	Package	Quantity per Reel	Pb Free	Halogen Free
R1111Nxx1∗-TR-FE	SOT-23-5	3,000 pcs	Yes	Yes

xx: The output voltage can be designated in the range from 1.5V(15) to 5.0V(50) in 0.1V steps.
(For other voltages, please refer to MARK INFORMATIONS.)

∗ : Designation of Active Type
(A) "L" active
(B) "H" active

PIN CONFIGURATION

- **SOT-23-5**



PIN DESCRIPTION

- **SOT-23-5**

Pin No	Symbol	Pin Description
1	V_{DD}	Input Pin
2	GND	Ground Pin
3	$\overline{CE}$ or CE	Chip Enable Pin
4	NC	No Connection
5	V_{OUT}	Output pin

ABSOLUTE MAXIMUM RATINGS

Symbol	Item	Rating	Unit
V_{IN}	Input Voltage	9.0	V
V_{CE}	Input Voltage($\overline{CE}$ or CE Pin)	-0.3 ~ V_{IN}+0.3	V
V_{OUT}	Output Voltage	-0.3 ~ V_{IN}+0.3	V
I_{OUT}	Output Current	200	mA
P_D	Power Dissipation *	420	mW
Topt	Operating Temperature Range	-40 ~ 85	°C
Tstg	Storage Temperature Range	-55 ~ 125	°C

∗) For Power Dissipation, please refer to PACKAGE INFORMATION.

ABSOLUTE MAXIMUM RATINGS
Electronic and mechanical stress momentarily exceeded absolute maximum ratings may cause the permanent damages and may degrade the life time and safety for both device and system using the device in the field. The functional operation at or over these absolute maximum ratings is not assured.

ELECTRICAL CHARACTERISTICS

- **R1111Nxx1A**

(Topt=25°C)

Symbol	Item	Conditions	Min.	Typ.	Max.	Unit
V_{OUT}	Output Voltage	$V_{IN}=V_{SET}+1V$, $1mA \leq I_{OUT} \leq 30mA$	×0.98		×1.02	V
I_{OUT}	Output Current	Refer to ELECTRICAL CHARACTERISTICS BY OUTPUT VOLTAGE.				
ΔV_{OUT} /ΔI_{OUT}	Load Regulation	$V_{IN}=V_{SET}+1V$, $1mA \leq I_{OUT} \leq 80mA$		12	40	mV
V_{DIF}	Dropout Voltage	Refer to ELECTRICAL CHARACTERISTICS BY OUTPUT VOLTAGE.				
I_{SS}	Supply Current	$V_{IN}=V_{SET}+1V$, $I_{OUT}=0A$		35	70	μA
Istandby	Standby Current	$V_{IN}=V_{CE}$, $V_{IN}=V_{SET}+1V$		0.1	1.0	μA
ΔV_{OUT} /ΔV_{IN}	Line Regulation	$V_{SET}+0.5V \leq V_{IN} \leq 8.0V$, $I_{OUT}=30mA$		0.05	0.20	%/V
RR	Ripple Rejection	f=1kHz, Ripple 0.5Vp-p, $V_{IN}=V_{SET}+1V$		70		dB
V_{IN}	Input Voltage		2.0		8.0	V
ΔV_{OUT} /ΔTopt	Output Voltage Temperature Coefficient	$I_{OUT}=10mA$, $-40°C \leq Topt \leq 85°C$		±100		Ppm /°C
I_{SC}	Short Current Limit	$V_{OUT}=0V$		50		mA
R_{PU}	$\overline{CE}$ Pull-up Resistance		2.5	5.0	10.0	MΩ
V_{CEH}	$\overline{CE}$ Input Voltage "H"		1.5		V_{IN}	V
V_{CEL}	$\overline{CE}$ Input Voltage "L"		0		0.25	V
en	Output Noise	BW=10Hz to 100kHz		30		μVrms

RECOMMENDED OPERATING CONDITIONS (ELECTRICAL CHARACTERISTICS)

All of electronic equipment should be designed that the mounted semiconductor devices operate within the recommended operating conditions. The semiconductor devices cannot operate normally over the recommended operating conditions, even if when they are used over such conditions by momentary electronic noise or surge. And the semiconductor devices may receive serious damage when they continue to operate over the recommended operating conditions.

● **R1111Nxx1B**

(Topt=25°C)

Symbol	Item	Conditions	Min.	Typ.	Max.	Unit
V_{OUT}	Output Voltage	$V_{IN}=V_{SET}+1V$, $1mA \leq I_{OUT} \leq 30mA$	×0.98		×1.02	V
I_{OUT}	Output Current	Refer to ELECTRICAL CHARACTERISTICS BY OUTPUT VOLTAGE.				
ΔV_{OUT} /ΔI_{OUT}	Load Regulation	$V_{IN}=V_{SET}+1V$, $1mA \leq I_{OUT} \leq 80mA$		12	40	mV
V_{DIF}	Dropout Voltage	Refer to ELECTRICAL CHARACTERISTICS BY OUTPUT VOLTAGE.				
I_{SS}	Supply Current	$V_{IN}=V_{SET}+1V$, $I_{OUT}=0A$		35	70	μA
Istandby	Standby Current	$V_{IN}=V_{SET}+1V$, $V_{CE}=GND$		0.1	1.0	μA
ΔV_{OUT} /ΔV_{IN}	Line Regulation	$V_{SET}+0.5V \leq V_{IN} \leq 8.0V$, $I_{OUT}=30mA$		0.05	0.20	%/V
RR	Ripple Rejection	f=1KHz, Ripple 0.5Vp-p, $V_{IN}=V_{SET}+1V$		70		dB
V_{IN}	Input Voltage		2.0		8.0	V
ΔV_{OUT} /ΔTopt	Output Voltage Temperature Coefficient	$I_{OUT}=10mA$, $-40°C \leq Topt \leq 85°C$		±100		Ppm /°C
I_{SC}	Short Current Limit	$V_{OUT}=0V$		50		mA
R_{PD}	CE Pull-up Resistance		2.5	5.0	10.0	MΩ
V_{CEH}	CE Input Voltage “H”		1.5		V_{IN}	V
V_{CEL}	CE Input Voltage “L”		0		0.25	V
en	Output Noise	BW=10Hz to 100kHz		30		μVrms

RECOMMENDED OPERATING CONDITIONS (ELECTRICAL CHARACTERISTICS)

All of electronic equipment should be designed that the mounted semiconductor devices operate within the recommended operating conditions. The semiconductor devices cannot operate normally over the recommended operating conditions, even if when they are used over such conditions by momentary electronic noise or surge. And the semiconductor devices may receive serious damage when they continue to operate over the recommended operating conditions.

ELECTRICAL CHARACTERISTICS by OUTPUT VOLTAGE

Topt = 25°C

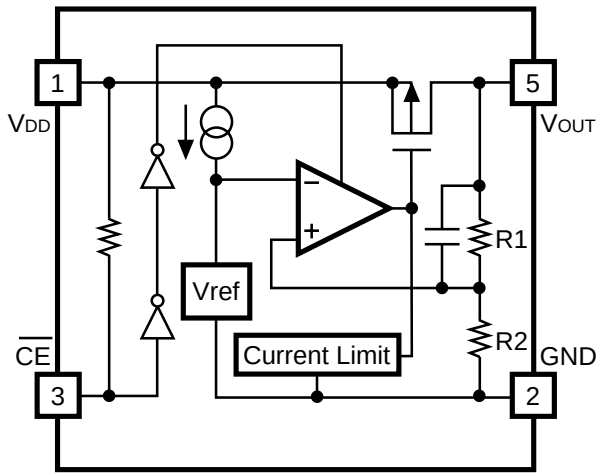
Output Voltage V_{OUT} (V)	Output Current I_{OUT} (mA)	
	Condition	Min.
$1.5 \le V_{SET} \le 1.7$	$V_{IN}=V_{SET}+1V$	100
$1.8 \le V_{SET} \le 5.0$		150

Topt = 25°C

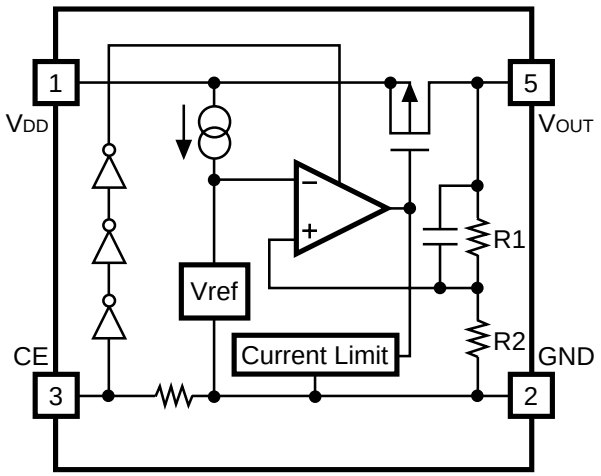
Output Voltage V_{OUT} (V)	Dropout Voltage V_{DIF} (V)			
	Condition	Min.	Typ.	Max.
1.5	I_{OUT} = 100mA	0.5		
1.6		0.4		
1.7		0.3		
$1.8 \le V_{SET} \le 1.9$			0.60	1.40
$2.0 \le V_{SET} \le 2.4$			0.35	0.70
$2.5 \le V_{SET} \le 2.7$			0.24	0.35
$2.8 \le V_{SET} \le 3.3$			0.20	0.30
$3.4 \le V_{SET} \le 5.0$			0.17	0.26

OPERATION

R1111Nxx1A



R1111Nxx1B



In these ICs, fluctuation of output voltage, V_{OUT} is detected by feed-back registers R1, R2, and the result is compared with a reference voltage by the error amplifier, so that a constant voltage is output. A current limit circuit for protection at short mode and a chip enable circuit, are included.

TEST CIRCUITS

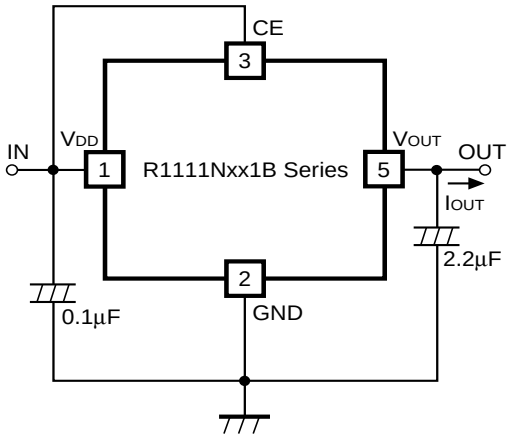


Fig.1 Standard test Circuit

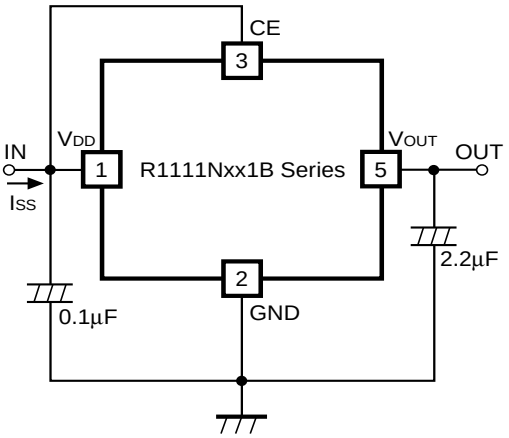


Fig.2 Supply Current Test Circuit

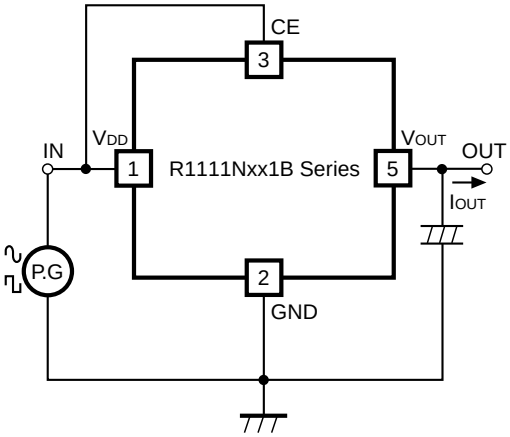


Fig.3 Ripple Rejection, Line Transient Response Test Circuit

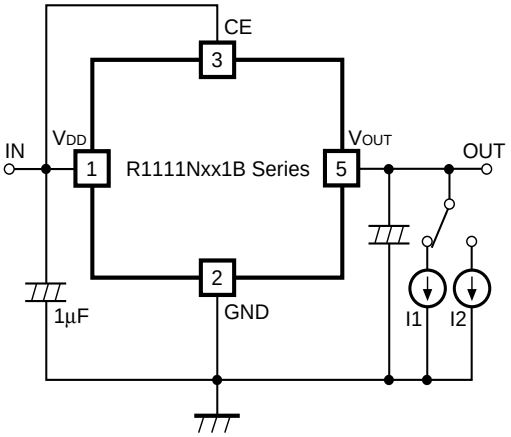
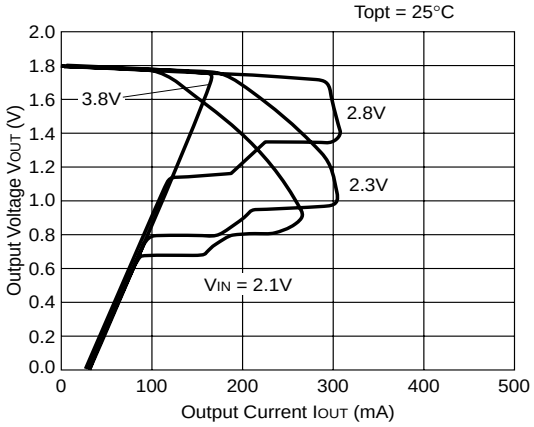


Fig.4 Load Transient Response Test Circuit

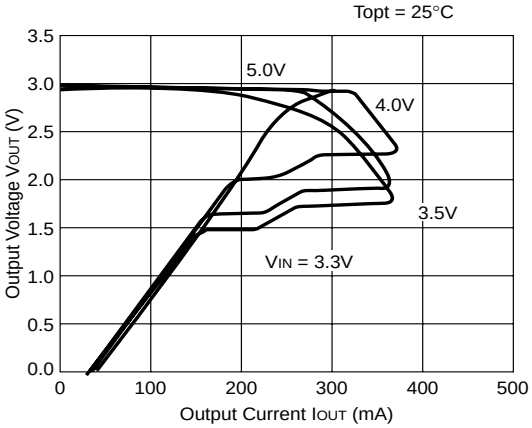
TYPICAL CHARACTERISTICS

1) Output Voltage vs. Output Current

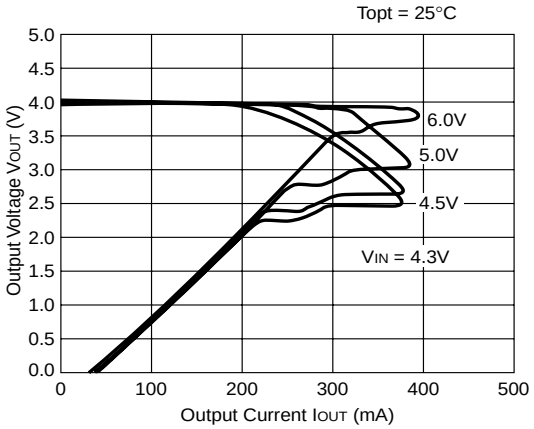
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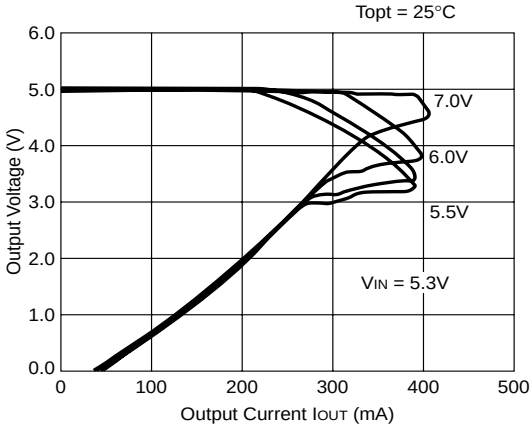
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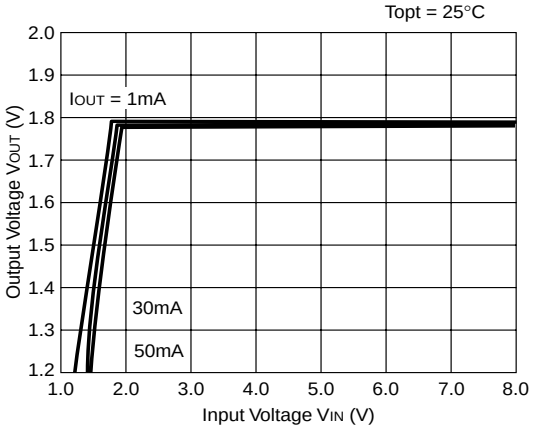


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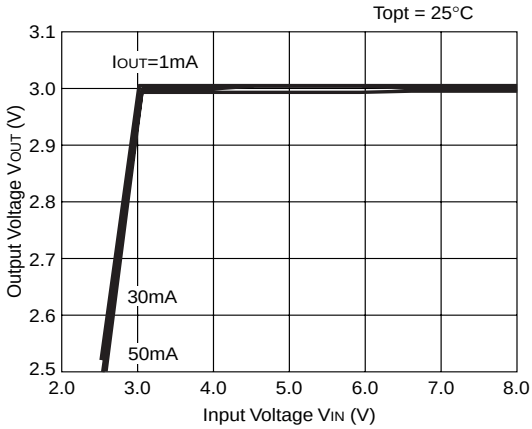


2) Output Voltage vs. Input Voltage

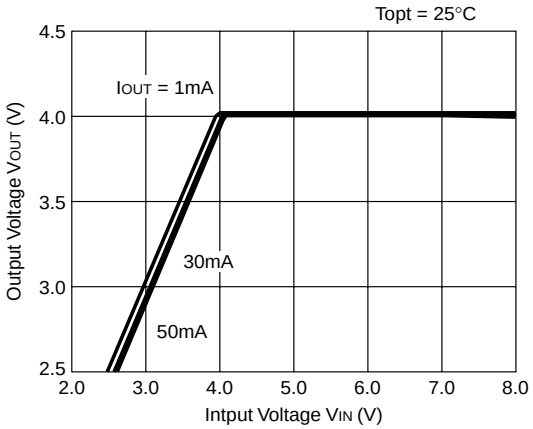
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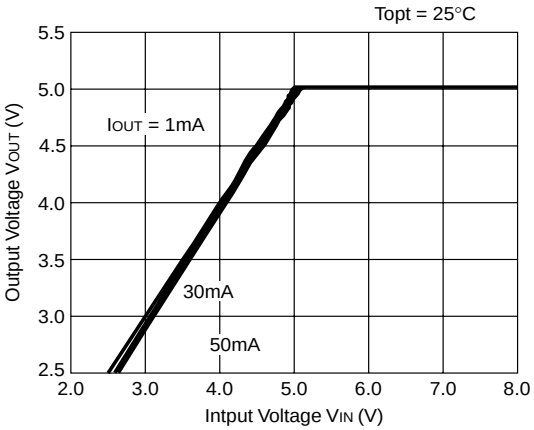
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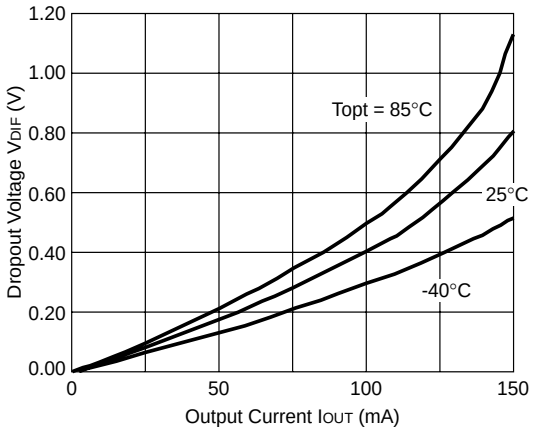


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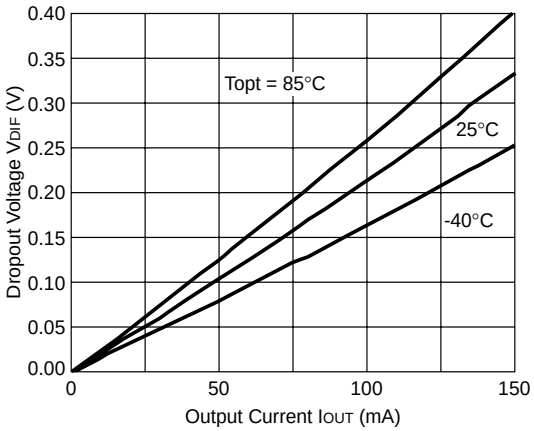


3) Dropout Voltage vs. Output Current

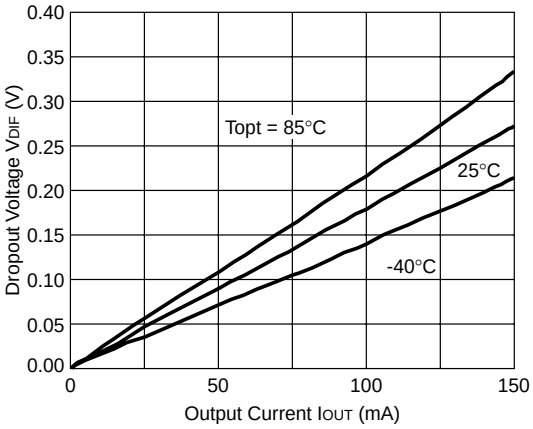
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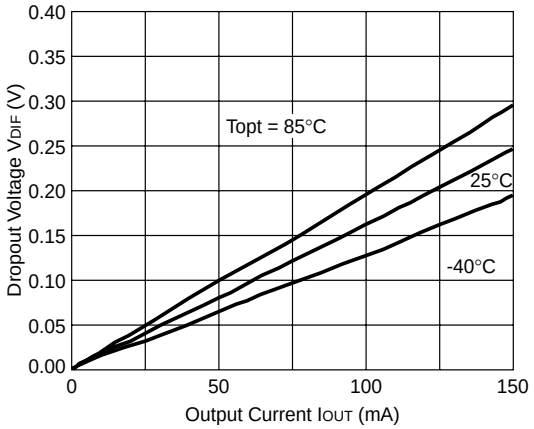
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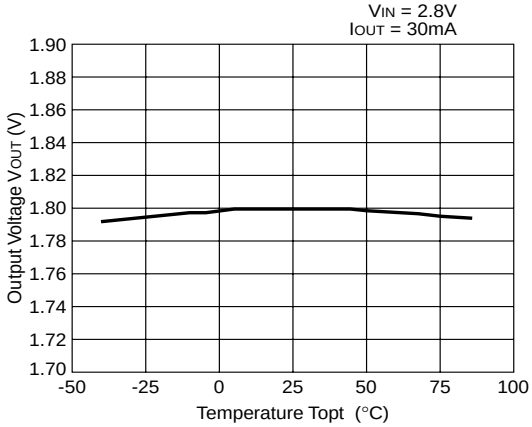


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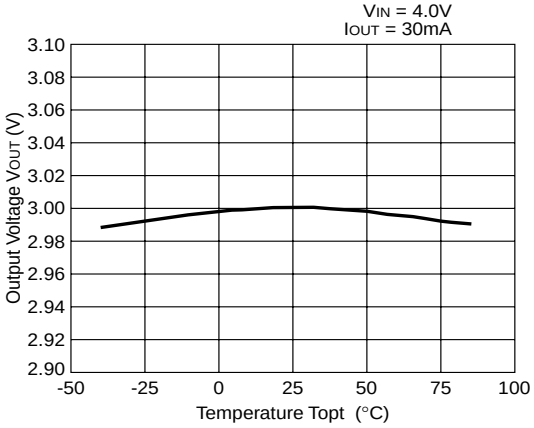


4) Output Voltage vs. Temperature

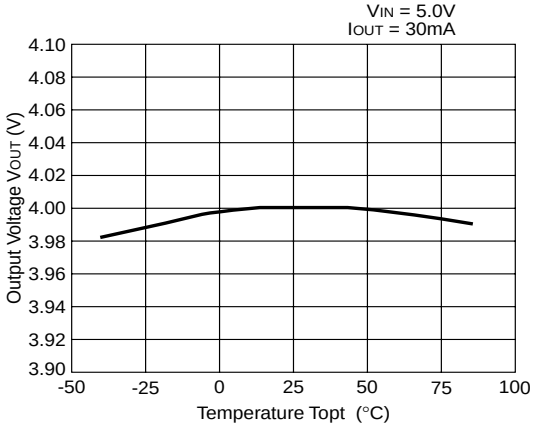
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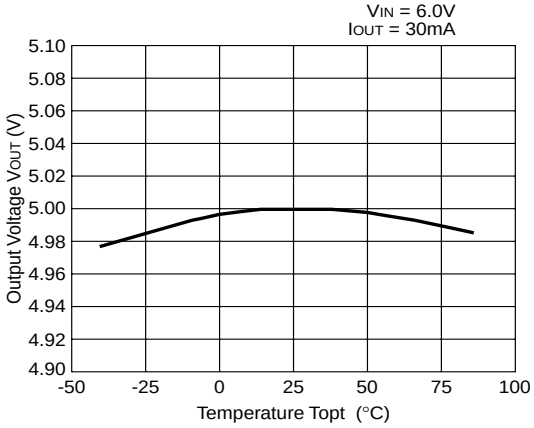
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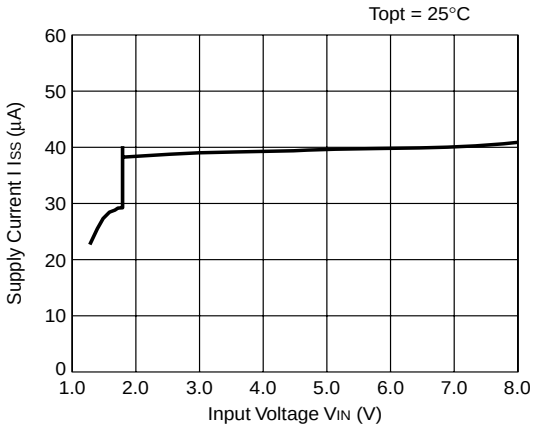


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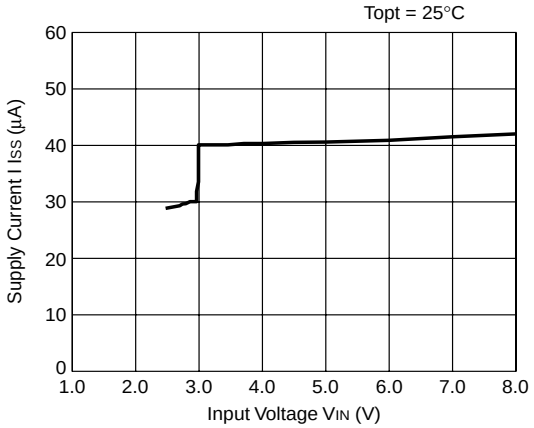


5) Supply Current vs. Input Voltage

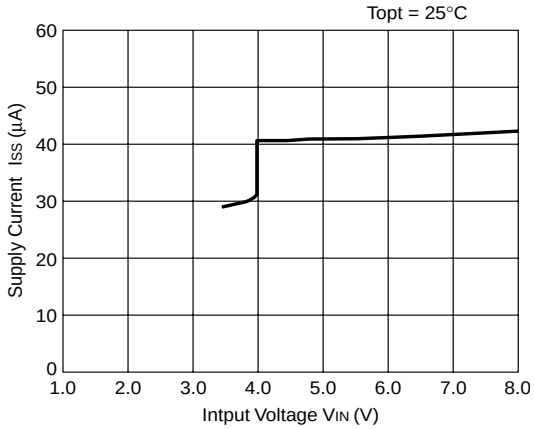
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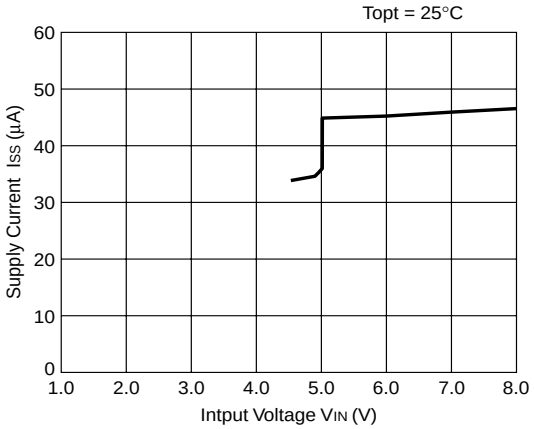
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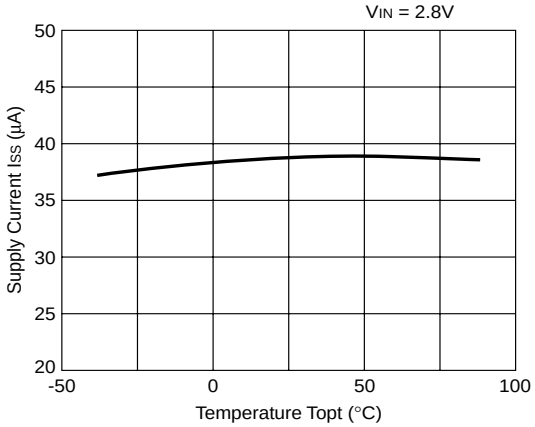


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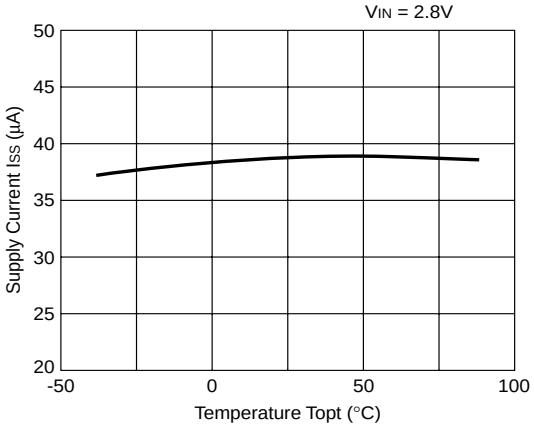


6) Supply Current vs. Temperature

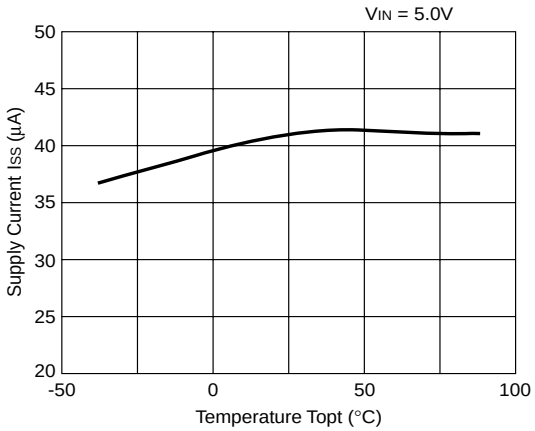
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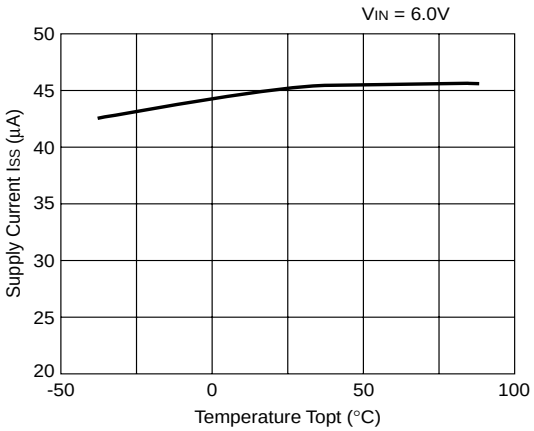
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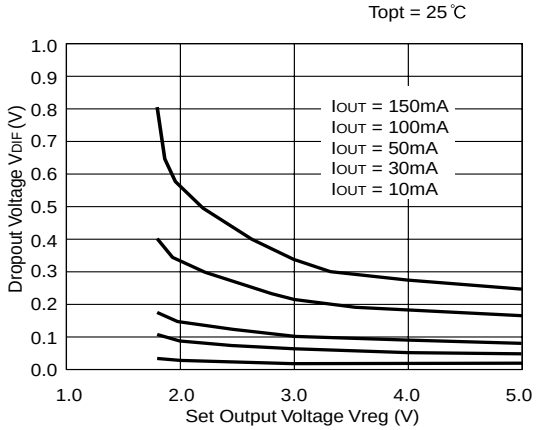


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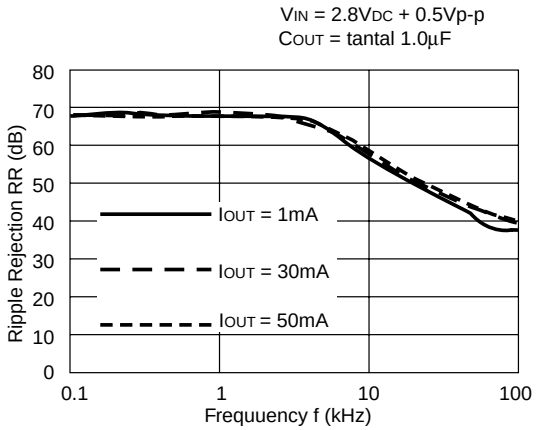
7) Dropout Voltage vs. Set Output Voltage

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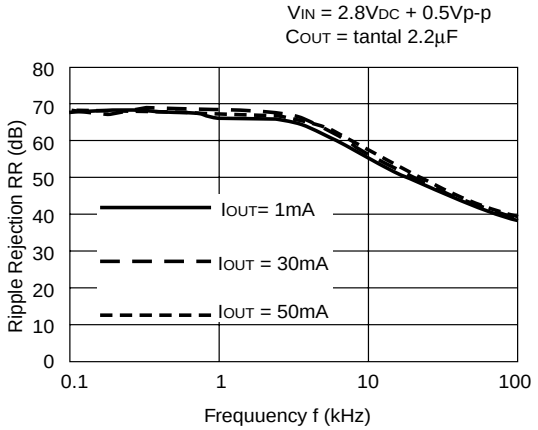


8) Ripple Rejection vs. Frequency

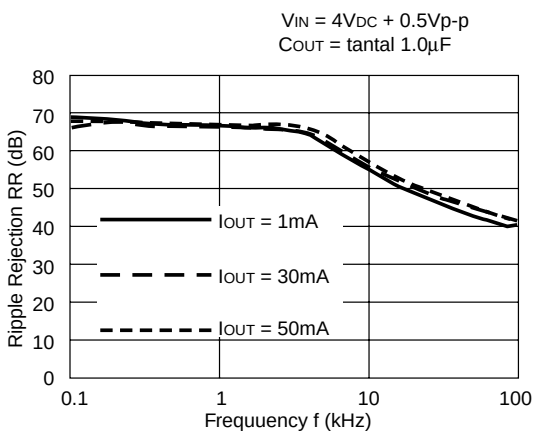
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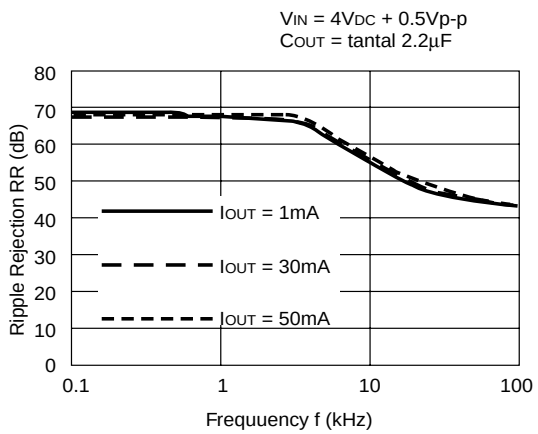
R1111N181B



R1111N301B

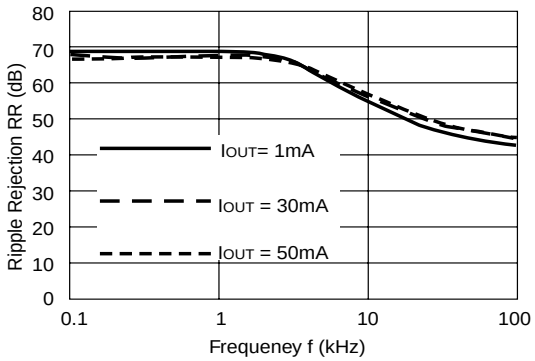


R1111N301B



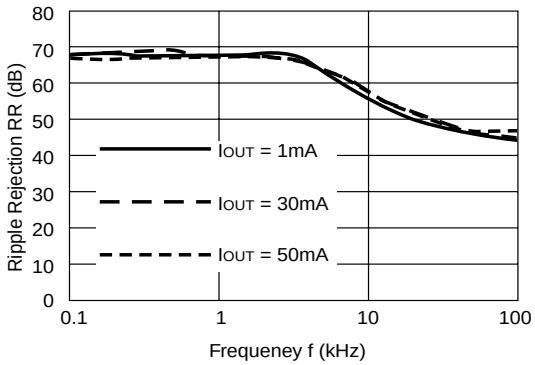
R1111N401B

V_{IN} = 5.0V_{DC} + 0.5Vp-p
C_{OUT} = tantal 1.0µF



R1111N401B

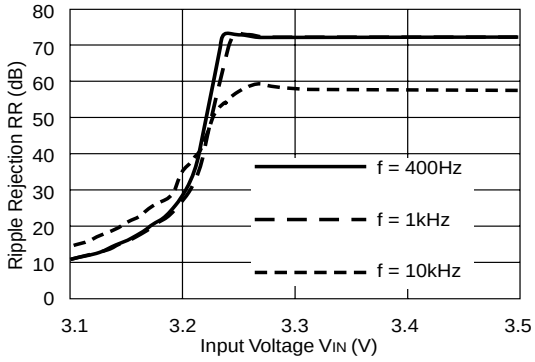
V_{IN} = 5.0V_{DC} + 0.5Vp-p
C_{OUT} = tantal 2.2µF



9) Ripple Rejection vs. Input Voltage (DC bias)

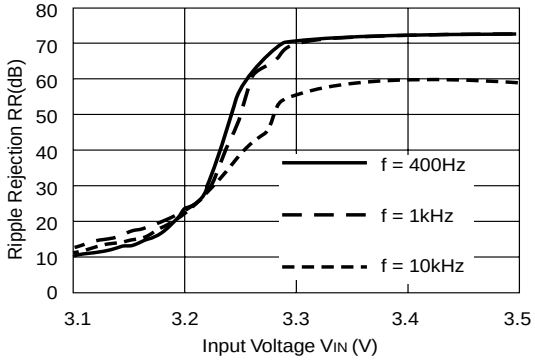
R1111N301B

I_{OUT} = 1mA
C_{OUT} = 2.2µF



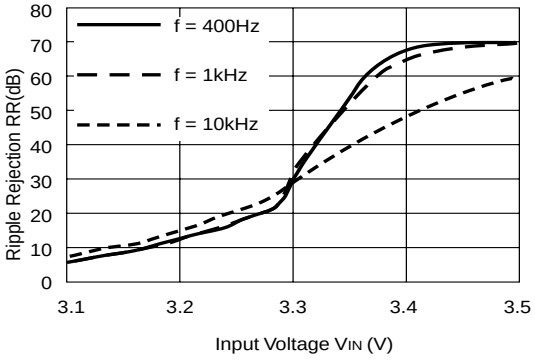
R1111N301B

I_{OUT} = 10mA
C_{OUT} = 2.2µF



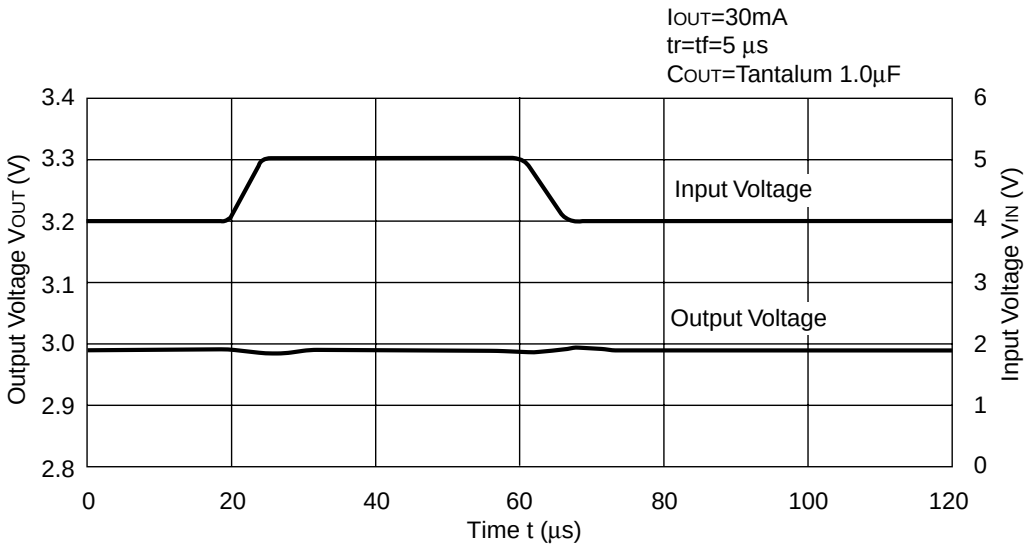
R1111N301B

I_{OUT} = 50mA
C_{OUT} = 2.2µF

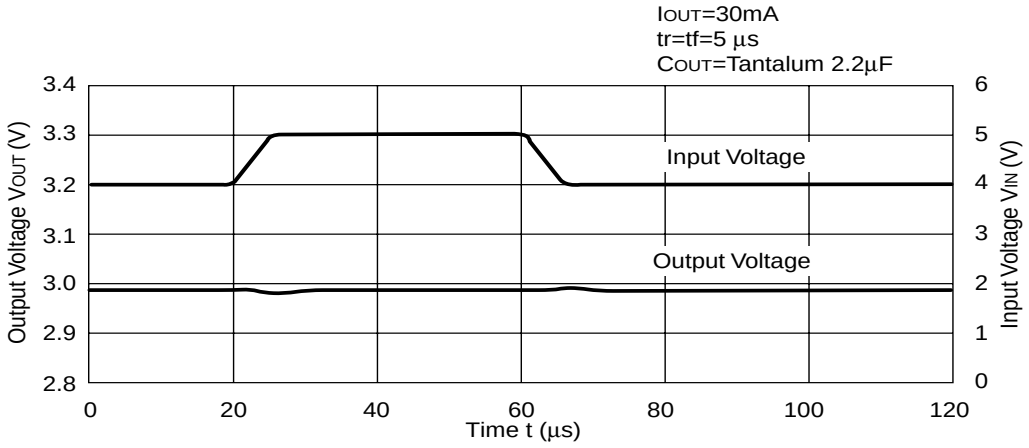


10) Input Transient Response

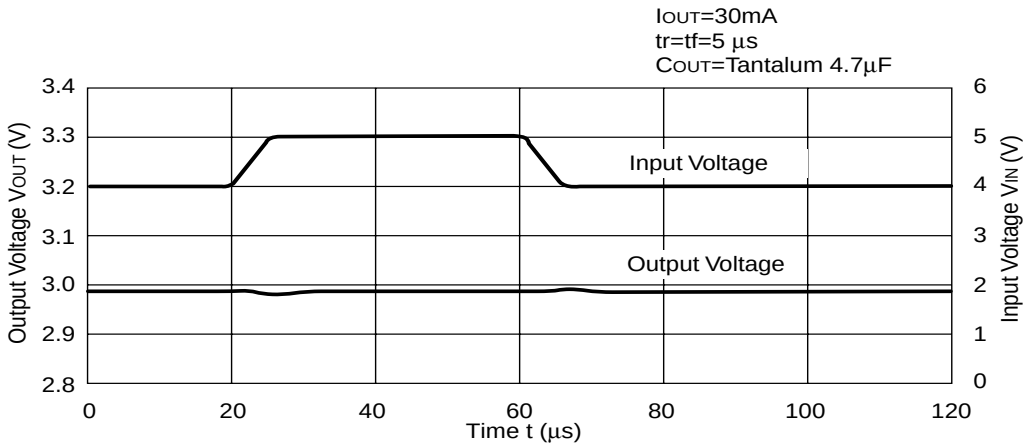
R1111N301B



R1111N301B

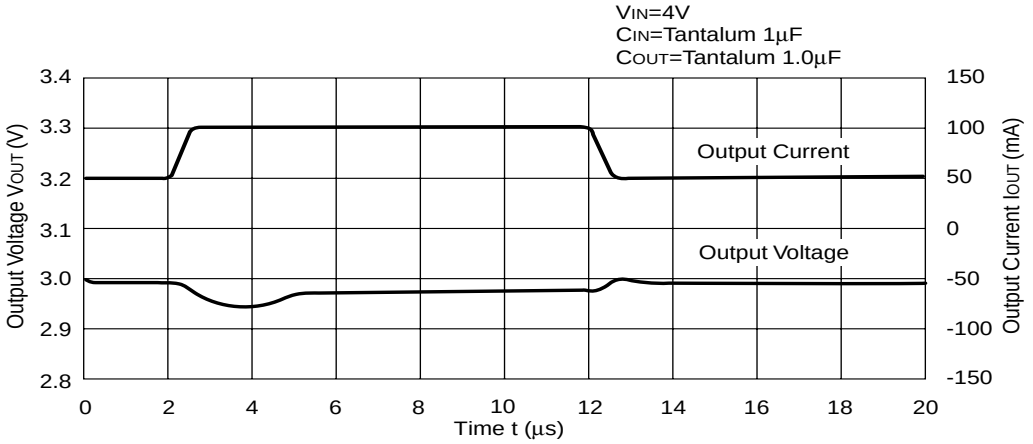


R1111N301B

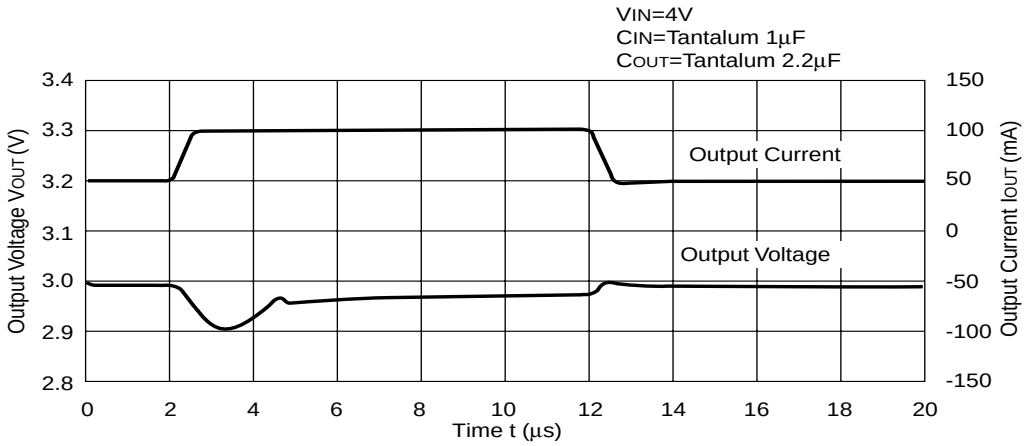


11) Load Transient Response

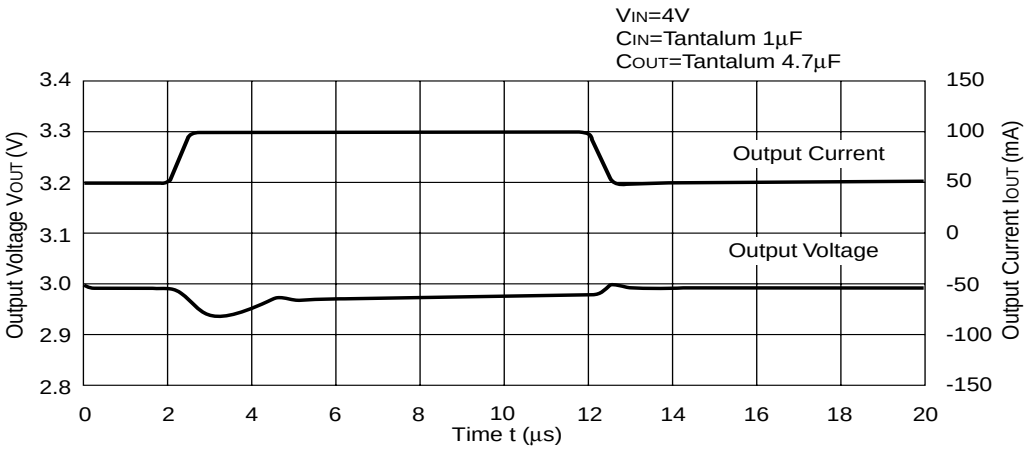
R1111N301B



R1111N301B



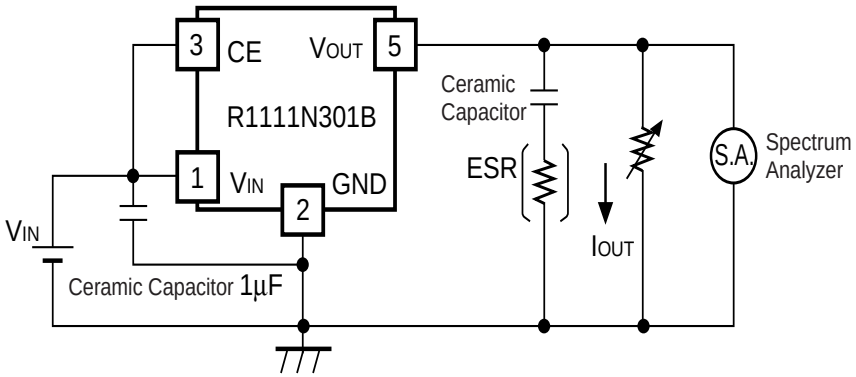
R1111N301B



TECHNICAL NOTES

When using these ICs, consider the following points:

In these ICs, phase compensation is made for securing stable operation even if the load current is varied. For this purpose, be sure to use a capacitor C_{OUT} with good frequency characteristics and ESR (Equivalent Series Resistance) of which is in the range described as follows:



Measuring Circuit for white noise; R1111N301B

The relationship between I_{OUT} (output current) and ESR of output capacitor is shown in the graphs below. The conditions when the white noise level is under 40mV (Avg.) are indicated by the hatched area in the graph.

(note: When the additional ceramic capacitors are connected to the output pin with output capacitor for phase compensation, the operation might be unstable. Because of this, test these ICs with as the same external components as the ones to be used on the PCB.)

<Measurement conditions>

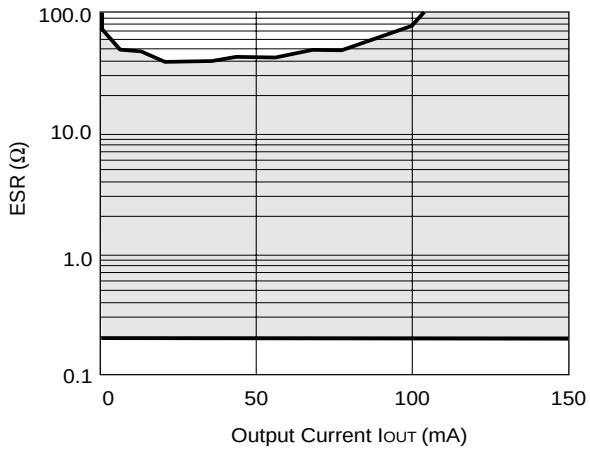
(1) V_{IN}=4V

(2) Frequency Band: 10Hz to 1MHz

(3) Temperature: 25°C

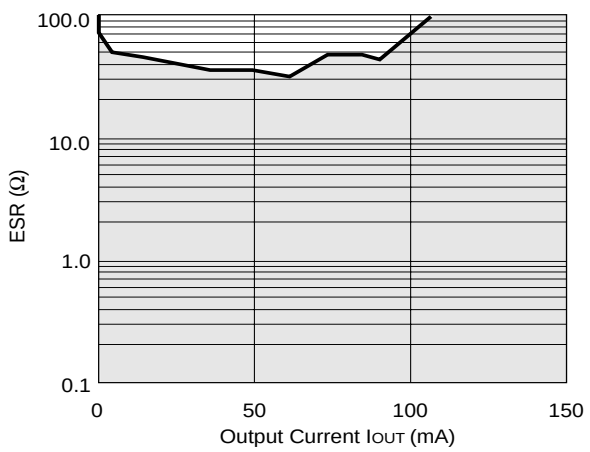
R1111N301B

Ceramic 1.0µF



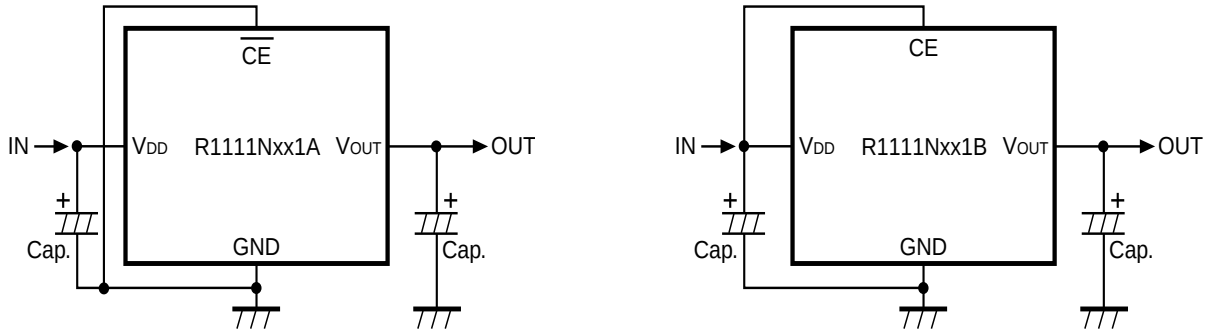
R1111N301B

Ceramic 2.2µF



· Make V_{DD} and GND lines sufficient. If their impedance is high, noise pick up or incorrect operation may result.
· Connect the capacitor with a capacitance of 1μF or more between V_{DD} and GND as close as possible.
· Set external components, especially the output capacitor, as close as possible to the ICs and make wiring as short as possible.

TYPICAL APPLICATION



1. The products and the product specifications described in this document are subject to change or discontinuation of production without notice for reasons such as improvement. Therefore, before deciding to use the products, please refer to our sales representatives for the latest information thereon.
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6. We are making our continuous effort to improve the quality and reliability of our products, but semiconductor products are likely to fail with certain probability. In order to prevent any injury to persons or damages to property resulting from such failure, customers should be careful enough to incorporate safety measures in their design, such as redundancy feature, fire containment feature and fail-safe feature. We do not assume any liability or responsibility for any loss or damage arising from misuse or inappropriate use of the products.
7. Anti-radiation design is not implemented in the products described in this document.
8. The X-ray exposure can influence functions and characteristics of the products. Confirm the product functions and characteristics in the evaluation stage.
9. WLCSP products should be used in light shielded environments. The light exposure can influence functions and characteristics of the products under operation or storage.
10. There can be variation in the marking when different AOI (Automated Optical Inspection) equipment is used. In the case of recognizing the marking characteristic with AOI, please contact our sales or our distributor before attempting to use AOI.
11. Please contact our sales representatives should you have any questions or comments concerning the products or the technical information.

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