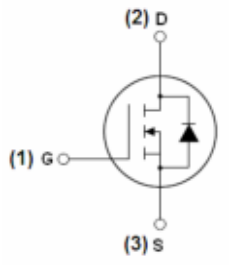
RECTRON SEMICONDUCTOR
TECHNICAL SPECIFICATION

RM50N60T2

N-Channel Enhancement Mode Power MOSFET

Description

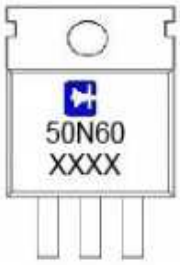
The RM50N60T2 uses advaned trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.



Schematic diagram

General Features

- V_{DS} =60V,I_D =50A
  $R_{DS(ON)}$ <20mΩ @ V_{GS}=10V
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation
- Special process technology for high ESD capability



Marking and pin assignment

Application

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply
- Halogen-free
- P/N suffix V means AEC-Q101 qualified, e.g:RM50N60T2V

100% UIS TESTED!

100% ΔVds TESTED!



TO-220-3L top view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
50N60	RM50N60T2	TO-220-3L	-	-	-

Absolute Maximum Ratings (T_C=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	±20	V
Drain Current-Continuous	I_D	50	A
Drain Current-Continuous(T_C=100℃)	I_D (100℃)	35.4	A
Pulsed Drain Current	I_{DM}	90	A
Maximum Power Dissipation	P_D	85	W
Derating factor		0.3	W/℃
Single pulse avalanche energy (Note 5)	E_{AS}	245	mJ
Operating Junction and Storage Temperature Range	T_J,T_{STG}	-55 To 175	℃

Thermal Characteristic

Thermal Resistance,Junction-to-Case (Note 2)	$R_{\theta JC}$	3.3	℃/W

Electrical Characteristics (T_C=25℃unless otherwise noted)

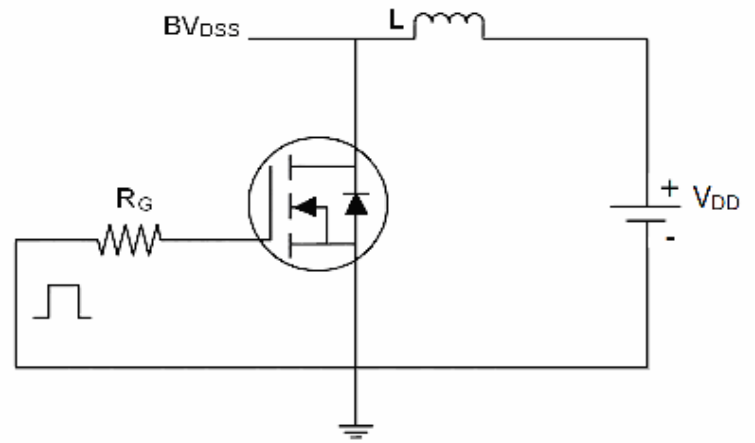
Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V I_D=250μA	60	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=60V,V_{GS}=0V	-	-	1	μA
Gate-Body Leakage Current	I_{GSS}	V_{GS}=±20V,V_{DS}=0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS},I_D=250μA	1.4	1.9	2.5	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	V_{GS}=10V, I_D=20A	-	14	20	mΩ
Forward Transconductance	g_{FS}	V_{DS}=5V,I_D=20A	18	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C_{lss}	V_{DS}=30V,V_{GS}=0V, F=1.0MHz	-	2050	-	PF
Output Capacitance	C_{oss}		-	158	-	PF
Reverse Transfer Capacitance	C_{rss}		-	120	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	$t_{d(on)}$	V_{DD}=30V, R_L=6.7Ω V_{GS}=10V,R_G=3Ω	-	7.4	-	nS
Turn-on Rise Time	t_r		-	5.1	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	28.2	-	nS
Turn-Off Fall Time	t_f		-	5.5	-	nS
Total Gate Charge	Q_g	V_{DS}=30V,I_D=20A, V_{GS}=10V	-	50		nC
Gate-Source Charge	Q_{gs}		-	6		nC
Gate-Drain Charge	Q_{gd}		-	15		nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V_{SD}	V_{GS}=0V,I_S=20A	-		1.2	V
Diode Forward Current (Note 2)	I_S		-	-	50	A
Reverse Recovery Time	t_{rr}	TJ = 25°C, IF =20A di/dt = 100A/μs (Note3)	-	28	-	nS
Reverse Recovery Charge	Qrr		-	40	-	nC
Forward Turn-On Time	t_{on}	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

Notes:

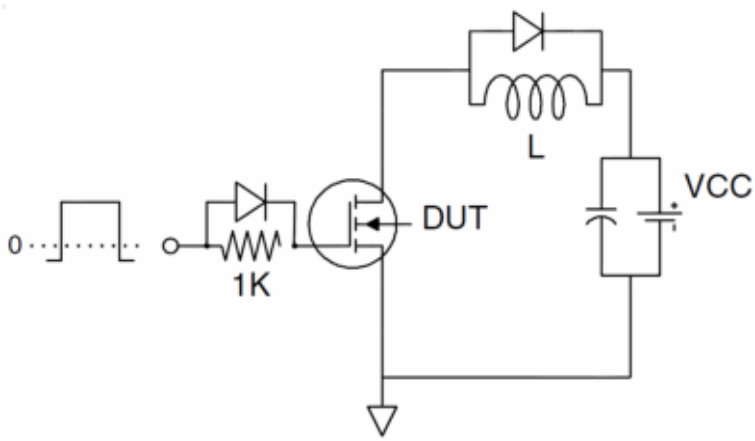
1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, t ≤ 10 sec.
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
4. Guaranteed by design, not subject to production
5. EAS condition : Tj=25℃,VDD=30V,VG=10V,L=0.5mH,Rg=25Ω

Test Circuit

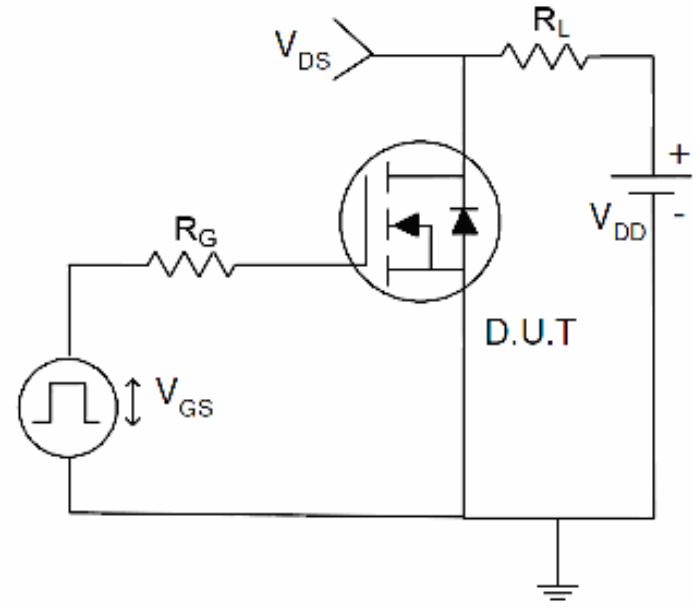
1) E_{AS} test Circuit



2) Gate charge test Circuit



3) Switch Time Test Circuit



RATING AND CHARACTERISTICS CURVES (RM50N60T2)

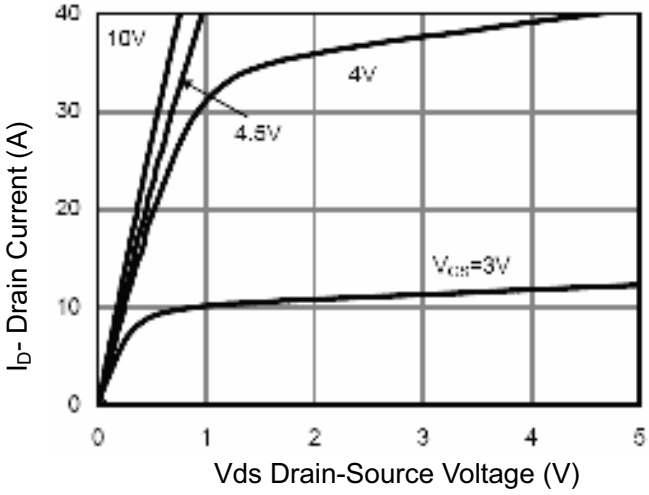


Figure 1 Output Characteristics

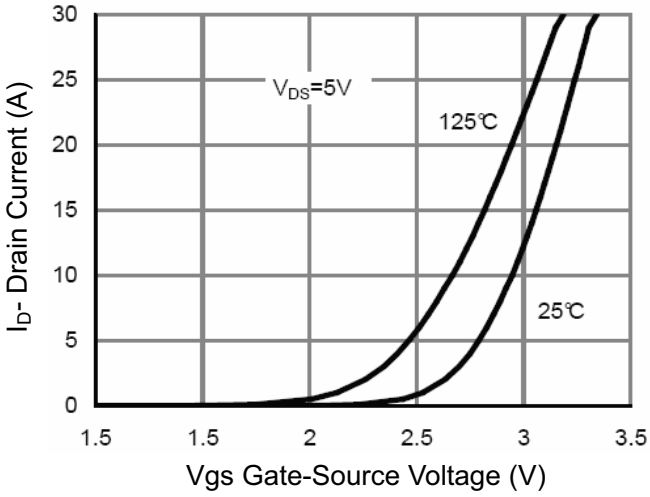


Figure 2 Transfer Characteristics

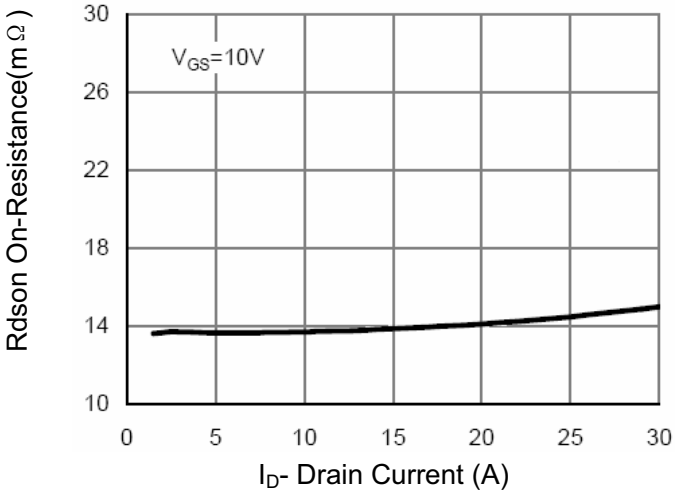


Figure 3 Rdson- Drain Current

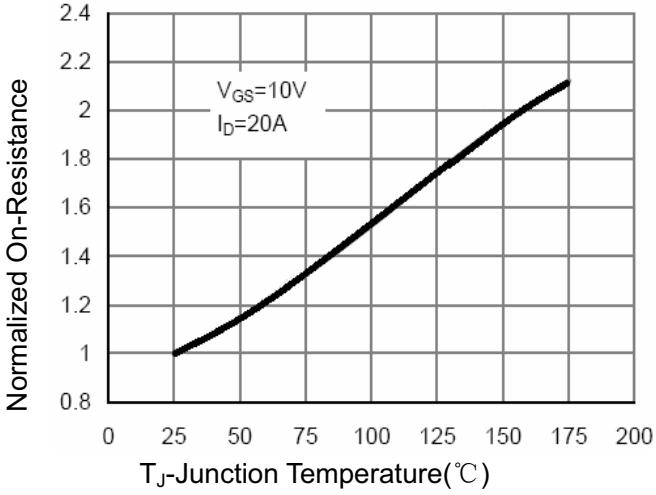


Figure 4 Rdson-Junction Temperature

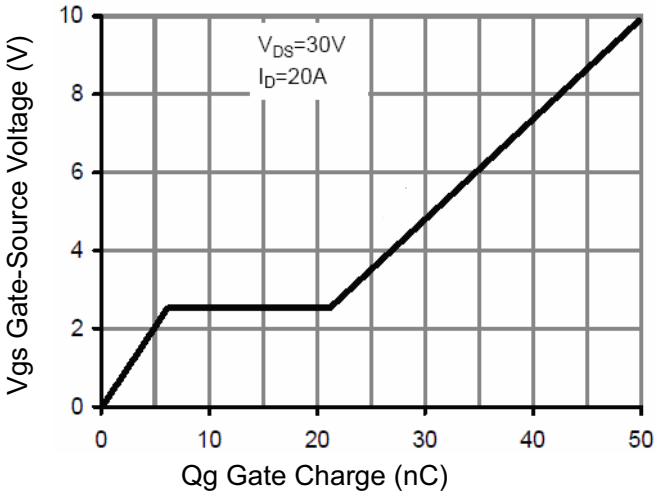


Figure 5 Gate Charge

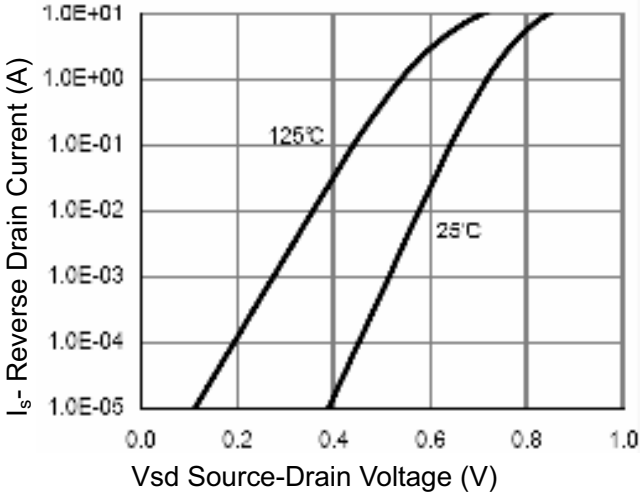


Figure 6 Source- Drain Diode Forward

RATING AND CHARACTERISTICS CURVES (RM50N60T2)

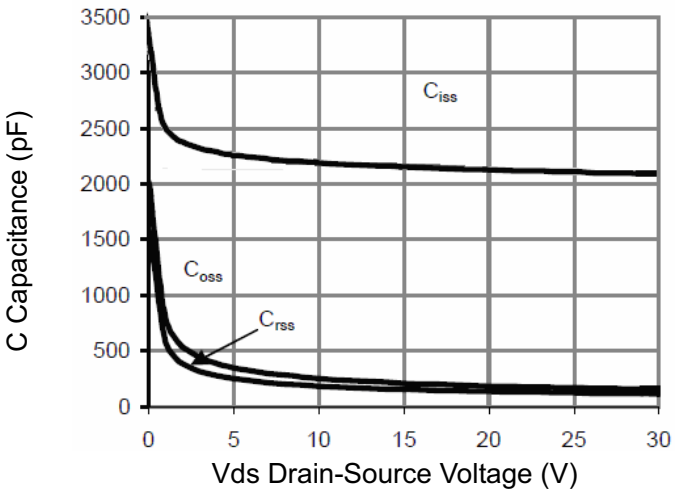


Figure 7 Capacitance vs Vds

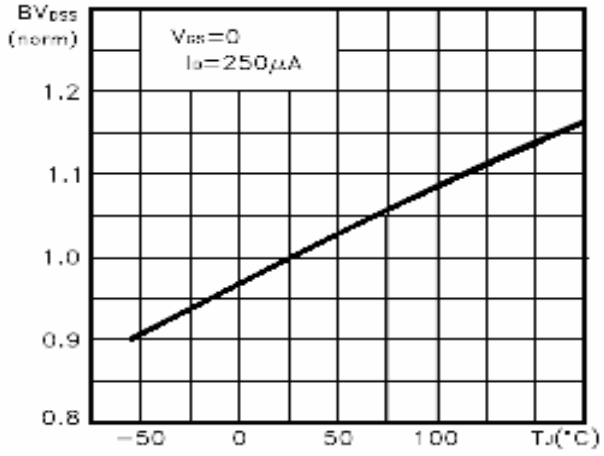


Figure 9 BV_{DSS} vs Junction Temperature

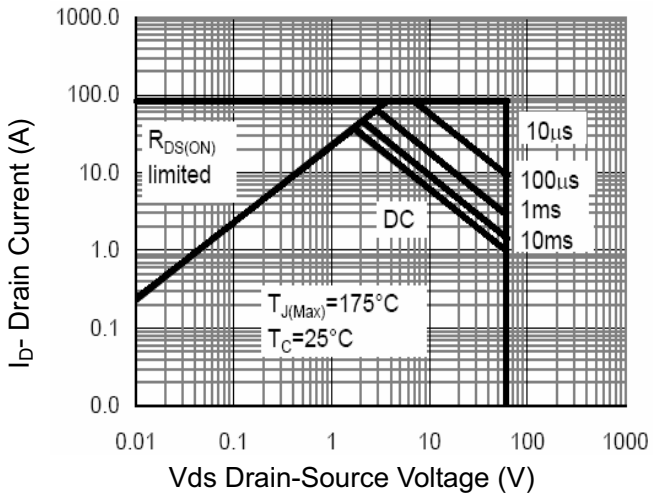


Figure 8 Safe Operation Area

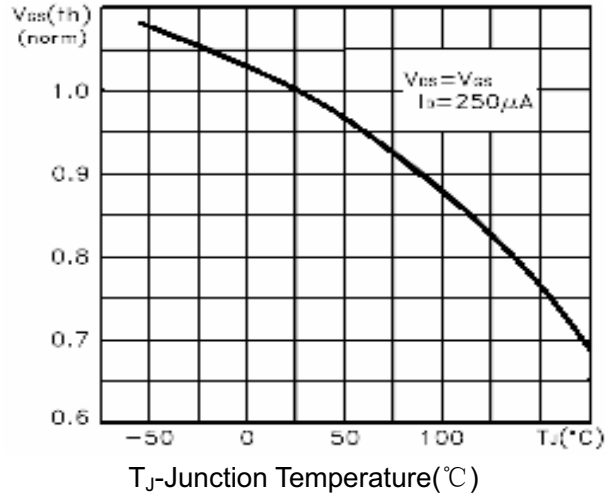


Figure 10 $V_{GS(th)}$ vs Junction Temperature

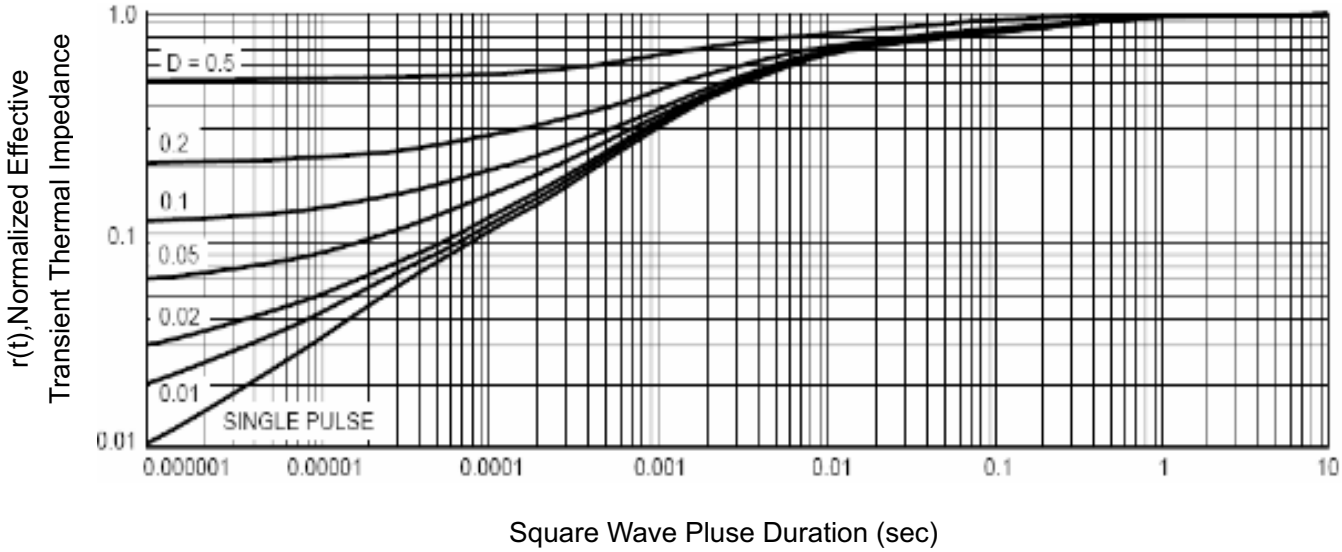
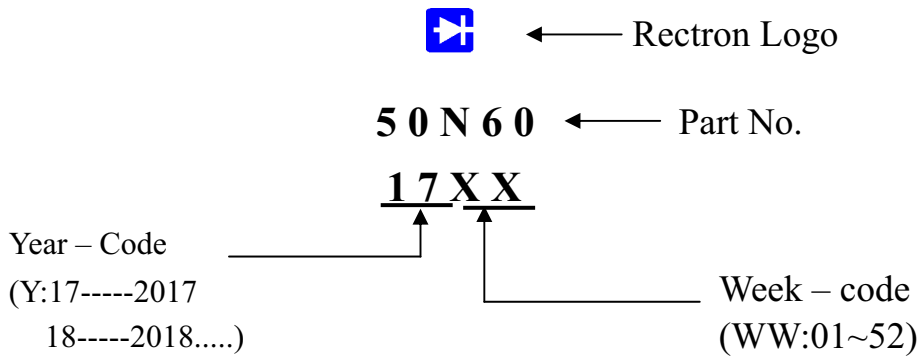


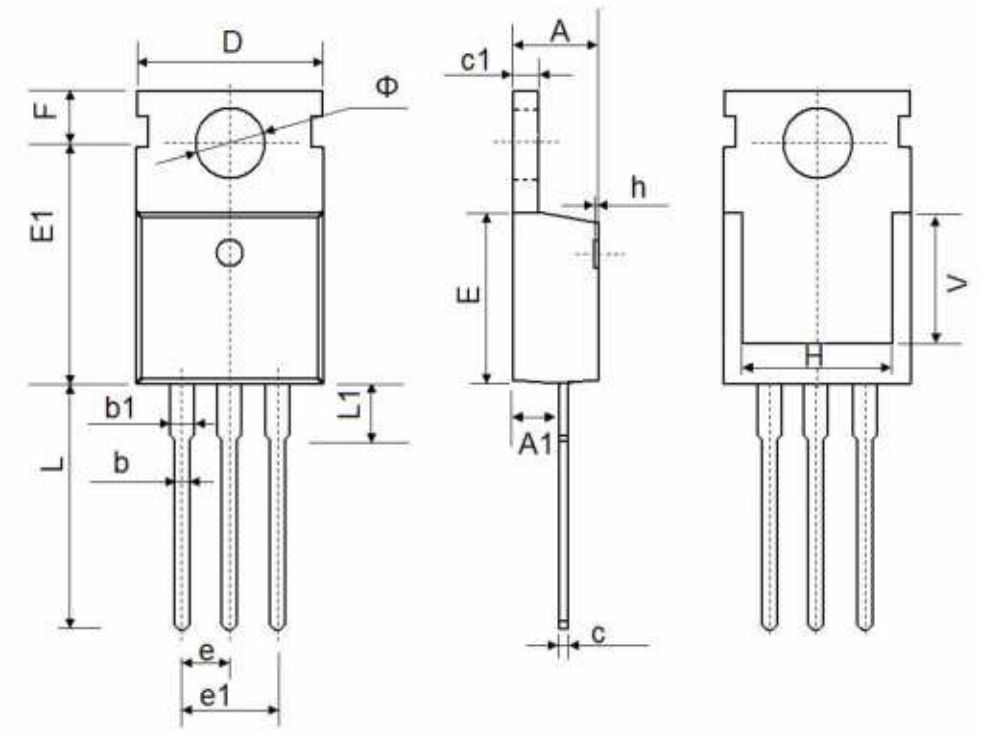
Figure 11 Normalized Maximum Transient Thermal Impedance

RECTRON

Marking on the body



TO-220-3L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.400	4.600	0.173	0.181
A1	2.250	2.550	0.089	0.100
b	0.710	0.910	0.028	0.036
b1	1.170	1.370	0.046	0.054
c	0.330	0.650	0.013	0.026
c1	1.200	1.400	0.047	0.055
D	9.910	10.250	0.390	0.404
E	8.9500	9.750	0.352	0.384
E1	12.650	12.950	0.498	0.510
e	2.540 TYP.		0.100 TYP.	
e1	4.980	5.180	0.196	0.204
F	2.650	2.950	0.104	0.116
H	7.900	8.100	0.311	0.319
h	0.000	0.300	0.000	0.012
L	12.900	13.400	0.508	0.528
L1	2.850	3.250	0.112	0.128
V	7.500 REF.		0.295 REF.	
Φ	3.400	3.800	0.134	0.150

Package	Tube (pcs/tube)	Tube (pcs/inner box)	Tube (pcs/cartoon)	Tape&Reel (pcs/reel)	Tape&Reel (pcs/inner box)	Tape&Reel (pcs/cartoon)
DFN	100	10,000	100,000	2,500	5,000	40,000
SOP-8	100	10,000	100,000	4,000	4,000	20,000
TSSOP-8	100	32,000	128,000	3,000	6,000	48,000
SOT-23-3L	——	——	——	3,000	30,000	120,000
SOT-23-6L	——	——	——	3,000	30,000	120,000
SOT-23(6R)	——	——	——	3,000	30,000	120,000
SOT-363	——	——	——	3,000	30,000	120,000
SOT-523	——	——	——	3,000	30,000	120,000
S0T223	——	——	——	2,500	2,500	20,000
TO-220	50	1,000	5,000	——	——	——
TO-220F	50	1,000	10,000	——	——	——
TO-247	30	300	1,200	——	——	——
TO-251	80	4,000	40,000	——	——	——
TO-251S(4R)	80	4,000	40,000	——	——	——
TO-252-2L(4R)	80	4,000	40,000	2,500	2,500	25,000
TO-263-2L	50	1,000	10,000	800	800	8,000
TO-3P	30	300	3,000	——	——	——
TO-92	——	——	——	1,000(袋装)	10,000	100,000

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