TLV707, TLV707P
200-mA, Low-I_Q, Low-Noise, Low-Dropout Regulator for Portable Devices

1 Features

- Typical accuracy: 0.5%
- Supports 200-mA output
- Low I_Q: 25 µA
- Fixed-output voltage combinations possible from 0.85 V to 5.0 V[(1)]
- High PSRR:
  - 70 dB at 100 Hz
  - 50 dB at 1 MHz
- Stable with effective capacitance of 0.1 µF[(2)]
- Thermal shutdown and overcurrent protection
- Package: 1-mm × 1-mm DQN (X2SON) [1] [2]

2 Applications

- Smart phones and wireless handsets
- Gaming and toys
- WLAN and other PC add-on cards
- TVs and set-top boxes
- Wearable electronics

3 Description

The TLV707 series (TLV707 and TLV707P) of low-dropout linear regulators (LDOs) are low quiescent current devices with excellent line and load transient performance for power-sensitive applications. These devices provide a typical accuracy of 0.5%. All versions have thermal shutdown and overcurrent protection for safety.

Furthermore, these devices are stable with an effective output capacitance of only 0.1 µF. This feature enables the use of cost-effective capacitors that have higher bias voltages and temperature derating. These devices also regulate to the specified accuracy with no output load.

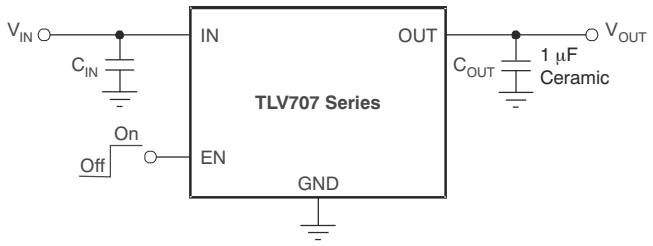
The TLV707P also provides an active pulldown circuit to quickly discharge the outputs.

The TLV707 series of LDOs are available in a 1-mm × 1-mm DQN (X2SON) package that makes them desirable for handheld applications.

Package Information

PART NUMBER	PACKAGE(1)	PACKAGE SIZE(2)
TLV707	DQN (X2SON, 4)	1.00 mm × 1.00 mm
TLV707P		

(1) For all available packages, see the package option addendum at the end of the data sheet.
(2) The package size (length × width) is a nominal value and includes pins, where applicable.



Typical Application Circuit

[1] For all available voltage options, see the package option addendum at the end of the data sheet.
[2] See the *Mechanical, Packaging, and Orderable Information* section for more details.

An IMPORTANT NOTICE at the end of this data sheet addresses availability, warranty, changes, use in safety-critical applications, intellectual property matters and other important disclaimers. PRODUCTION DATA.

Table of Contents

1 Features....1
2 Applications....1
3 Description....1
4 Revision History....2
5 Pin Configuration and Functions....3
6 Specifications....4
6.1 Absolute Maximum Ratings....4
6.2 ESD Ratings....4
6.3 Recommended Operating Conditions....4
6.4 Thermal Information....4
6.5 Electrical Characteristics....5
6.6 Typical Characteristics....6
7 Detailed Description....16
7.1 Overview....16
7.2 Functional Block Diagrams....16
7.3 Feature Description....17
7.4 Device Functional Modes....18
8 Application and Implementation....19
8.1 Application Information....19
8.2 Typical Application....19
8.3 Best Design Practices....22
8.4 Power Supply Recommendations....23
8.5 Layout....23
9 Device and Documentation Support....25
9.1 Device Support....25
9.2 Documentation Support....25
9.3 Receiving Notification of Documentation Updates....25
9.4 Support Resources....25
9.5 Trademarks....25
9.6 Electrostatic Discharge Caution....26
9.7 Glossary....26
10 Mechanical, Packaging, and Orderable Information....26

4 Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

Changes from Revision F (May 2018) to Revision G (June 2023)	Page
• Added TLV707xxA information to document	1
• Added TLV707xxA information to $V_{(DO)}$ and I_{CL} parameters in *Electrical Characteristics* table	5
• Added TLV707xxA information to *Device Nomenclature* section	25

Changes from Revision E (February 2016) to Revision F (May 2018)	Page
• Changed $V_{(ESD)}$ HBM value from ±2000 V to ±4000 V in *ESD Ratings* table	4

Copyright © 2023 Texas Instruments Incorporated

5 Pin Configuration and Functions

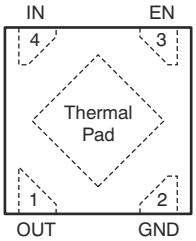


Figure 5-1. DQN Package, 4-Pin X2SON (Top View)

Table 5-1. Pin Functions

PIN		I/O	DESCRIPTION
NAME	NO.		
EN	3	I	Enable pin. Driving EN over 0.9 V turns on the regulator. Driving EN below 0.4 V puts the regulator into shutdown mode. For TLV707P, output voltage is discharged through an internal 120-Ω resistor when device is shut down.
GND	2	—	Ground pin
IN	4	I	Input pin. For good transient performance, place a small 1-µF ceramic capacitor from this pin to ground. See *Input and Output Capacitor Requirements* for more details.
OUT	1	O	Regulated output voltage pin. A small 1-µF ceramic capacitor is required from this pin to ground to assure stability. See *Input and Output Capacitor Requirements* for more details.

Copyright © 2023 Texas Instruments Incorporated

6 Specifications

6.1 Absolute Maximum Ratings

over operating junction temperature range (unless otherwise noted)[1]

		MIN	MAX	UNIT
Voltage[2]	IN	–0.3	6.0	V
	EN	–0.3	6.0	V
	OUT	–0.3	6.0	V
Current (source)	OUT	Internally limited		
Output short-circuit duration		Indefinite		
Temperature	Operating junction, T_J	–55	150	°C
	Storage, T_{stg}	–55	150	°C

(1) Stresses beyond those listed under *Absolute Maximum Ratings* may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under *Recommended Operating Conditions* is not implied. Exposure to absolute-maximum-rated conditions for extended periods my affect device reliability.
(2) All voltages are with respect to network ground pin.

6.2 ESD Ratings

			VALUE	UNIT
$V_{(ESD)}$	Electrostatic discharge	Human body model (HBM) QSS 009-105 (JESD22-A114A)[1]	±4000	V
		Charged device model (CDM) QSS 009-147 (JESD22-C101B.01)[2]	±500	

(1) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.
(2) JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.

6.3 Recommended Operating Conditions

over operating junction temperature range (unless otherwise noted)

		MIN	NOM	MAX	UNIT
V_{IN}	Input voltage	2.0		5.5	V
I_{OUT}	Output current	0		200	mA
T_J	Operating junction temperature	–40		125	°C

6.4 Thermal Information

THERMAL METRIC[1]		TLV707, TLV707P DQN (X2SON) 4 PINS	UNIT
$R_{\theta JA}$	Junction-to-ambient thermal resistance	208.1	°C/W
$R_{\theta JC(top)}$	Junction-to-case (top) thermal resistance	108.8	°C/W
$R_{\theta JB}$	Junction-to-board thermal resistance	159.4	°C/W
ψ_{JT}	Junction-to-top characterization parameter	3.8	°C/W
ψ_{JB}	Junction-to-board characterization parameter	159.4	°C/W
$R_{\theta JC(bot)}$	Junction-to-case (bottom) thermal resistance	110.2	°C/W

(1) For more information about traditional and new thermal metrics, see the *Semiconductor and IC Package Thermal Metrics* application note.

Copyright © 2023 Texas Instruments Incorporated

6.5 Electrical Characteristics

at V_{IN} = $V_{OUT(nom)}$ + 0.5 V or 2.0 V (whichever is greater); I_{OUT} = 1 mA, V_{EN} = V_{IN}, C_{OUT} = 0.47 µF, and T_J = –40°C to +85°C (unless otherwise noted); typical values are at T_J = 25°C

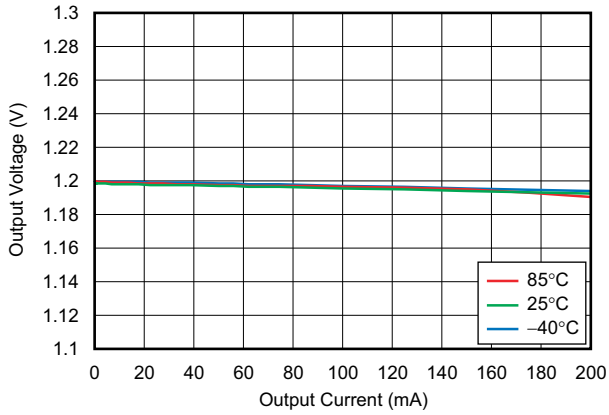
PARAMETER		TEST CONDITIONS			MIN	TYP	MAX	UNIT
V_{OUT}	Output voltage range				0.85		5	V
	DC output accuracy					0.5%		
		V_{OUT} ≥ 0.85 V			–1.5%		1.5%	
$\Delta V_{O\ (\Delta VI)}$	Line regulation					1	5	mV
$\Delta V_{O\ (\Delta IO)}$	Load regulation	0 mA ≤ I_{OUT} ≤ 150 mA				10	20	mV
$V_{(DO)}$	Dropout voltage	V_{IN} = 0.98 x $V_{OUT(nom)}$	2.0 V < V_{OUT} ≤ 2.4 V	I_{OUT} = 30 mA		65		mV
				I_{OUT} = 150 mA		325	360	
			2.4 V < V_{OUT} ≤ 2.8 V	I_{OUT} = 30 mA		50		
				I_{OUT} = 150 mA		250	300	
			2.8 V < V_{OUT} ≤ 3.3 V	I_{OUT} = 30 mA		45		
				I_{OUT} = 150 mA		220	270	
			3.3 V < V_{OUT} ≤ 5.0 V	I_{OUT} = 30 mA		40		
				I_{OUT} = 150 mA		200	250	
$V_{(DO)}$	Dropout voltage, TLV707xxA	V_{IN} = 0.98 × $V_{OUT(nom)}$	2.0 V < VOUT ≤ 2.5 V	I_{OUT} = 30 mA		65		mV
				I_{OUT} = 150 mA		325	360	
			2.5 V < VOUT ≤ 3.1 V	I_{OUT} = 30 mA		50		
				I_{OUT} = 150 mA		250	300	
			3.1 V < VOUT ≤ 3.5 V	I_{OUT} = 30 mA		45		
				I_{OUT} = 150 mA		220	280	
			3.5 V < VOUT ≤ 5.0 V	I_{OUT} = 30 mA		40		
				I_{OUT} = 150 mA		200	260	
I_{CL}	Output current limit	V_{OUT} = 0.9 × $V_{OUT(nom)}$			240	300	450	mA
I_{CL}	Output current limit, TLV707xxA	V_{OUT} = 0.9 × $V_{OUT(nom)}$, $V_{OUT(nom)}$ ≤ 1.5 V			220	300	450	mA
		V_{OUT} = 0.9 × $V_{OUT(nom)}$, $V_{OUT(nom)}$ > 1.5 V			240	300	450	
$I_{(GND)}$	Ground pin current	I_{OUT} = 0 mA				25	50	µA
$I_{(EN)}$	EN pin current	V_{EN} = 5.5 V				0.01		µA
$I_{SHUTDOWN}$	Shutdown current	V_{EN} ≤ 0.4 V, 2.0 V ≤ V_{IN} ≤ 4.5 V				1		µA
$V_{IL(EN)}$	EN pin low-level input voltage (disable device)				0		0.4	V
$V_{IH(EN)}$	EN pin high-level input voltage (enable device)				0.9		V_{IN}	V
PSRR	Power-supply rejection ratio	V_{IN} = 3.3 V, V_{OUT} = 2.8 V, I_{OUT} = 30 mA		f = 100 Hz		70		dB
				f = 10 kHz		55		
				f = 1 MHz		50		
V_n	Output noise voltage	BW = 100 Hz to 100 kHz, V_{IN} = 2.3 V, V_{OUT} = 1.8 V, I_{OUT} = 10 mA				45		μV_{RMS}
t_{STR}	Start-up time(1)	C_{OUT} = 1.0 µF, I_{OUT} = 150 mA				100		µs
$R_{PULLDOWN}$	Pulldown resistance (TLV707P only)					120		Ω

(1) Start-up time = time from EN assertion to 0.98 × V_{OUT}.

Copyright © 2023 Texas Instruments Incorporated

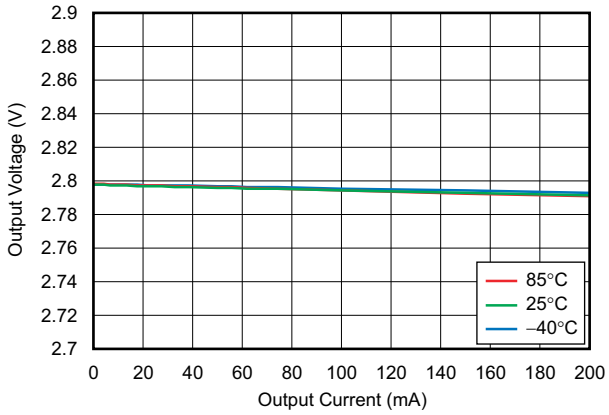
6.6 Typical Characteristics

at T_J = –40°C to +85°C, V_{IN} = $V_{OUT(nom)}$ + 0.5 V or 2.0 V (whichever is greater), I_{OUT} = 10 mA, V_{EN} = V_{IN}, and C_{OUT} = 1 µF (unless otherwise noted); typical values are at T_J = 25°C



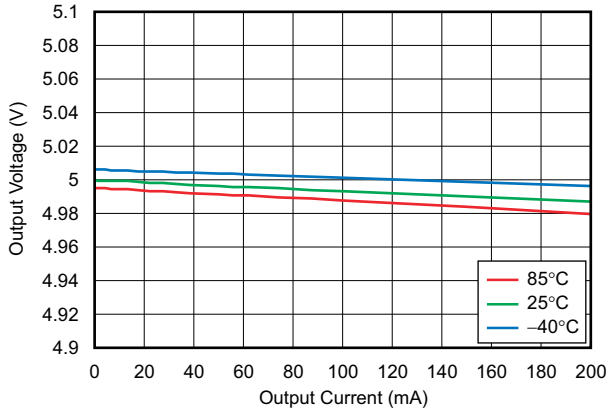
V_{OUT} = 1.2 V

Figure 6-1. Load Regulation



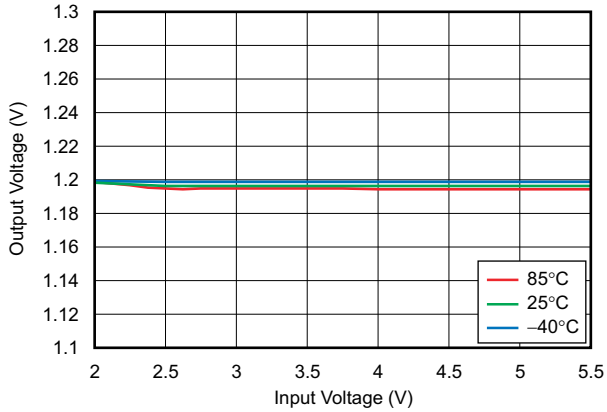
V_{OUT} = 2.8 V

Figure 6-2. Load Regulation



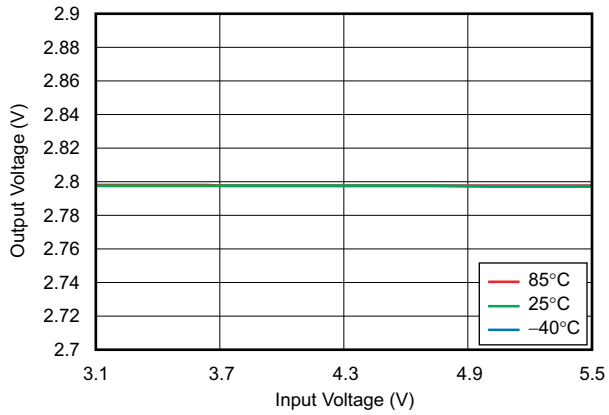
V_{OUT} = 5.0 V

Figure 6-3. Load Regulation



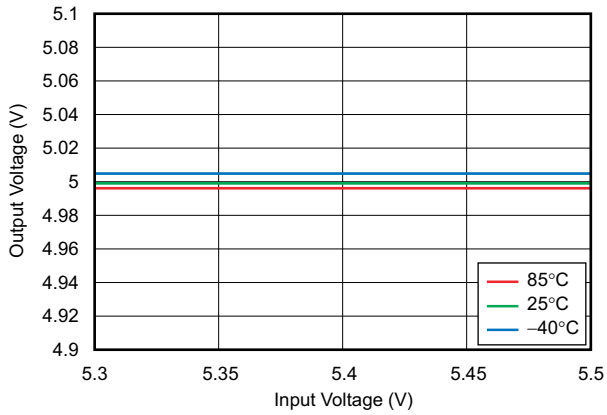
V_{OUT} = 1.2 V, I_{OUT} = 10 mA

Figure 6-4. Line Regulation



V_{OUT} = 2.8 V, I_{OUT} = 10 mA

Figure 6-5. Line Regulation



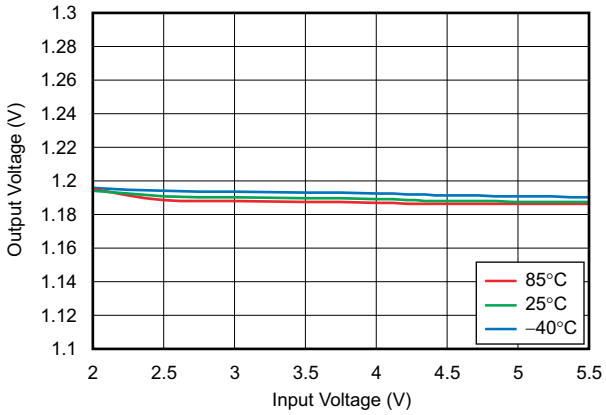
V_{OUT} = 5.0 V, I_{OUT} = 10 mA

Figure 6-6. Line Regulation

Copyright © 2023 Texas Instruments Incorporated

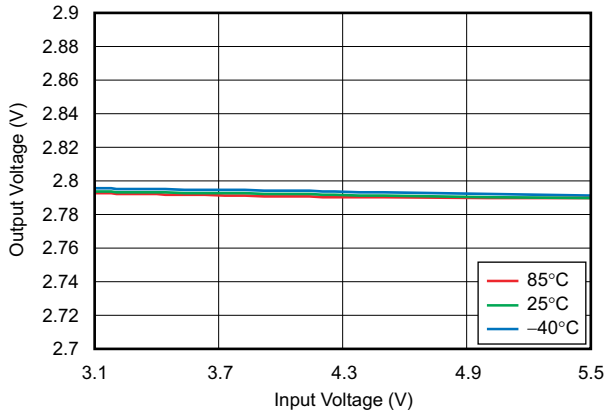
6.6 Typical Characteristics (continued)

at T_J = –40°C to +85°C, V_{IN} = $V_{OUT(nom)}$ + 0.5 V or 2.0 V (whichever is greater), I_{OUT} = 10 mA, V_{EN} = V_{IN}, and C_{OUT} = 1 µF (unless otherwise noted); typical values are at T_J = 25°C



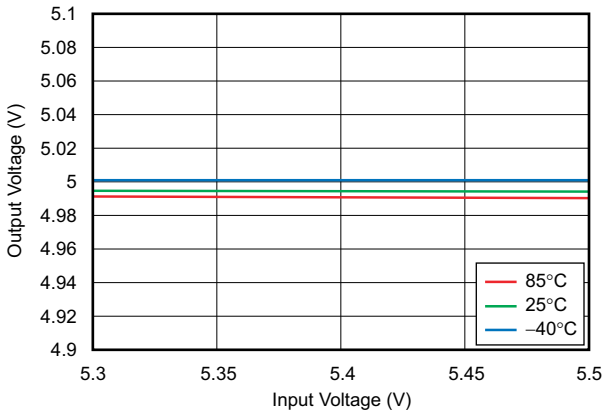
V_{OUT} = 1.2 V, I_{OUT} = 150 mA

Figure 6-7. Line Regulation



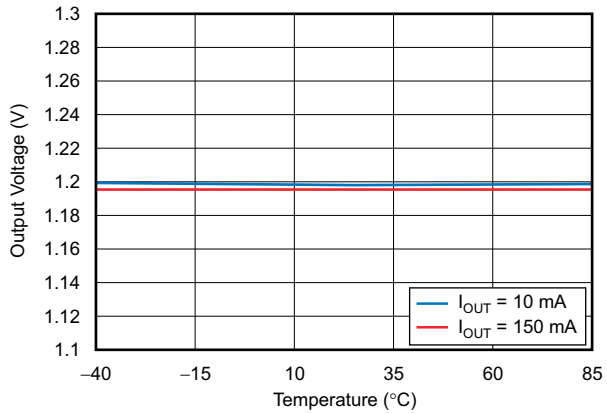
V_{OUT} = 2.8 V, I_{OUT} = 150 mA

Figure 6-8. Line Regulation



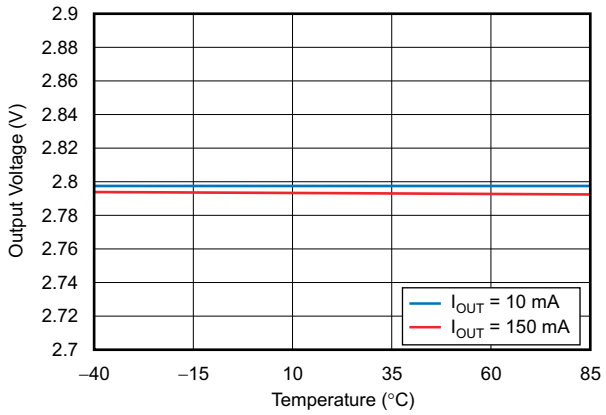
V_{OUT} = 5.0 V, I_{OUT} = 150 mA

Figure 6-9. Line Regulation



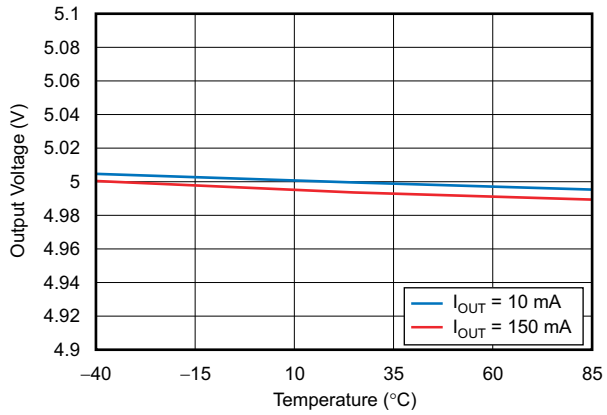
V_{OUT} = 1.2 V

Figure 6-10. Output Voltage vs Temperature



V_{OUT} = 2.8 V

Figure 6-11. Output Voltage vs Temperature

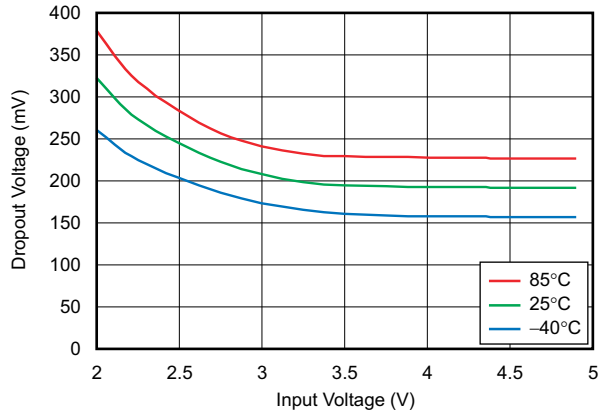


V_{OUT} = 5.0 V

Figure 6-12. Output Voltage vs Temperature

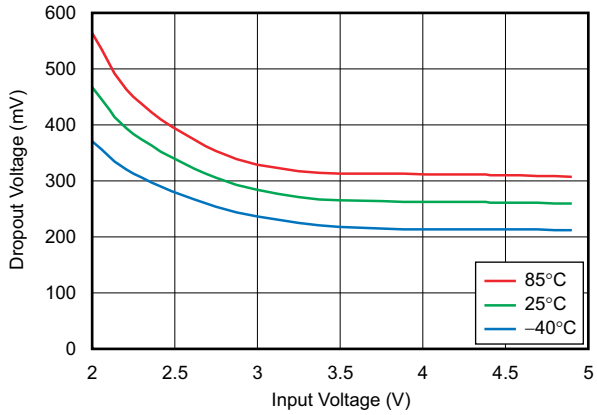
6.6 Typical Characteristics (continued)

at T_J = –40°C to +85°C, V_{IN} = $V_{OUT(nom)}$ + 0.5 V or 2.0 V (whichever is greater), I_{OUT} = 10 mA, V_{EN} = V_{IN}, and C_{OUT} = 1 µF (unless otherwise noted); typical values are at T_J = 25°C



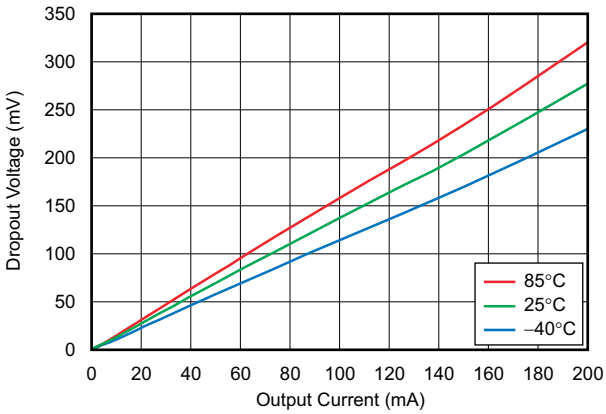
I_{OUT} = 150 mA

Figure 6-13. Dropout Voltage vs Input Voltage



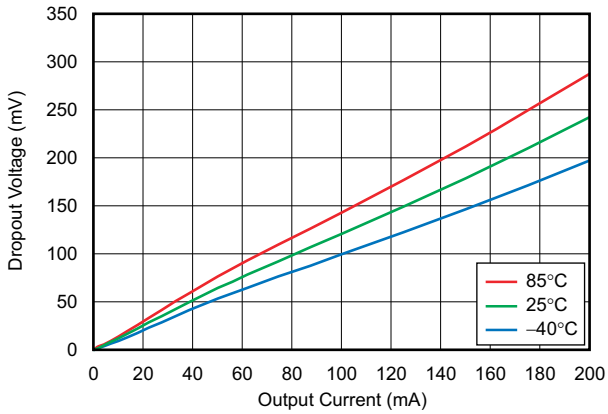
I_{OUT} = 200 mA

Figure 6-14. Dropout Voltage vs Input Voltage



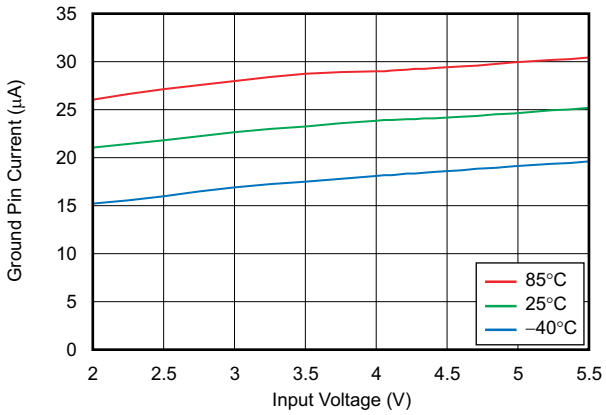
V_{OUT} = 2.8 V

Figure 6-15. Dropout Voltage vs Output Current



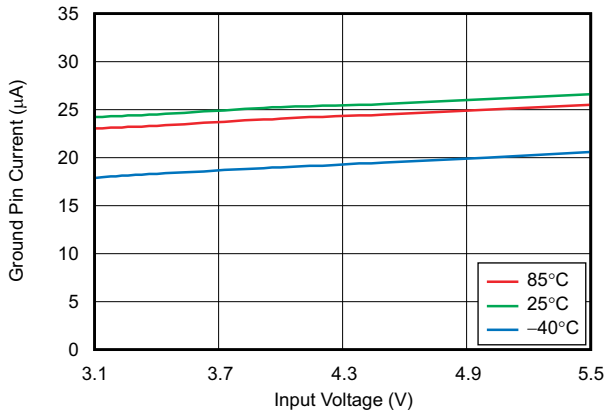
V_{OUT} = 5.0 V

Figure 6-16. Dropout Voltage vs Output Current



V_{OUT} = 1.2 V, I_{OUT} = 0 mA

Figure 6-17. Ground Pin Current vs Input Voltage



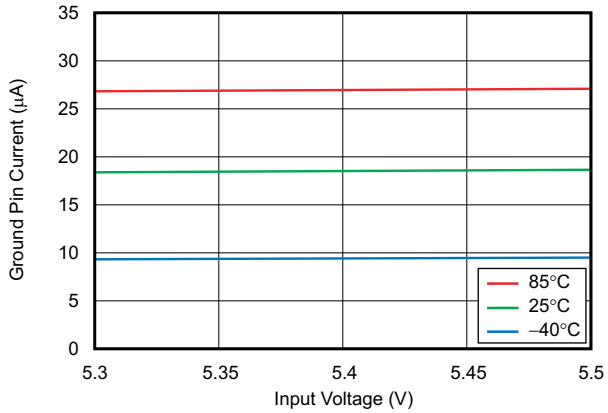
V_{OUT} = 2.8 V, I_{OUT} = 0 mA

Figure 6-18. Ground Pin Current vs Input Voltage

Copyright © 2023 Texas Instruments Incorporated

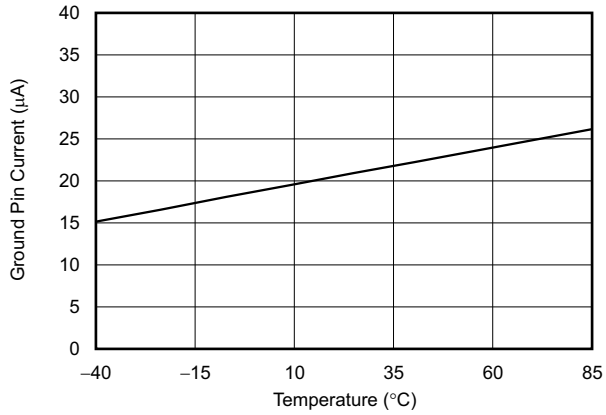
6.6 Typical Characteristics (continued)

at T_J = –40°C to +85°C, V_{IN} = $V_{OUT(nom)}$ + 0.5 V or 2.0 V (whichever is greater), I_{OUT} = 10 mA, V_{EN} = V_{IN}, and C_{OUT} = 1 µF (unless otherwise noted); typical values are at T_J = 25°C



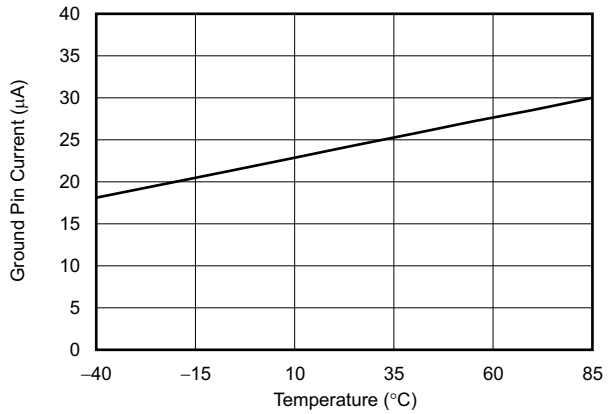
V_{OUT} = 5.0 V, I_{OUT} = 0 mA

Figure 6-19. Ground Pin Current vs Input Voltage



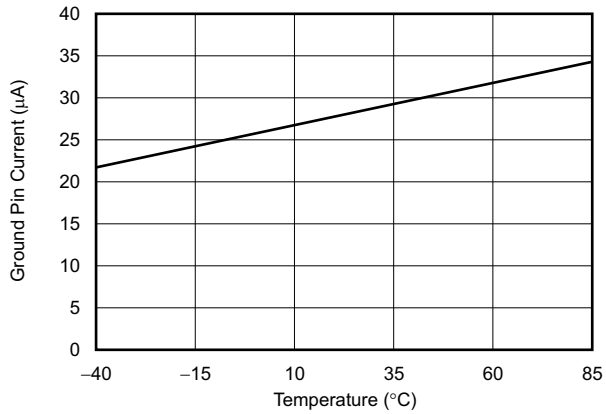
V_{OUT} = 1.2 V, I_{OUT} = 0 mA

Figure 6-20. Ground Pin Current vs Temperature



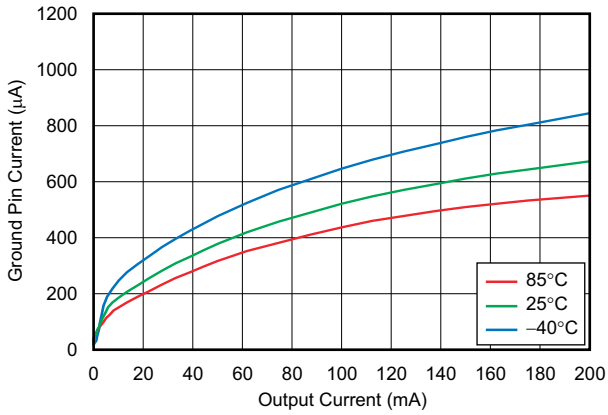
V_{OUT} = 2.8 V, I_{OUT} = 0 mA

Figure 6-21. Ground Pin Current vs Temperature



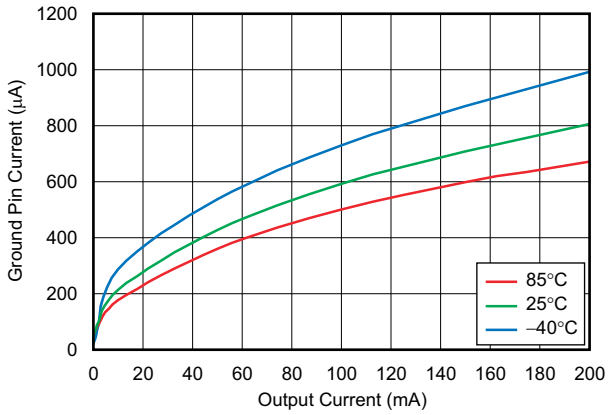
V_{OUT} = 5.0 V, I_{OUT} = 0 mA

Figure 6-22. Ground Pin Current vs Temperature



V_{OUT} = 1.2 V

Figure 6-23. Ground Pin Current vs Output Current



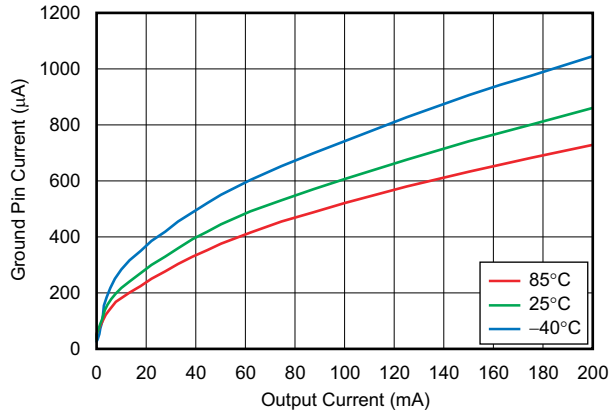
V_{OUT} = 2.8 V

Figure 6-24. Ground Pin Current vs Output Current

Copyright © 2023 Texas Instruments Incorporated

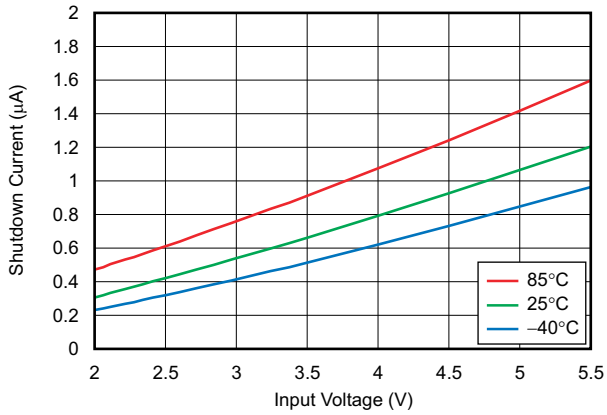
6.6 Typical Characteristics (continued)

at T_J = –40°C to +85°C, V_{IN} = $V_{OUT(nom)}$ + 0.5 V or 2.0 V (whichever is greater), I_{OUT} = 10 mA, V_{EN} = V_{IN}, and C_{OUT} = 1 µF (unless otherwise noted); typical values are at T_J = 25°C



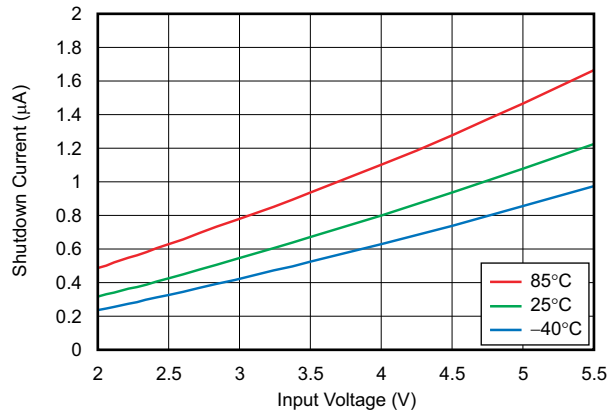
V_{OUT} = 5.0 V

Figure 6-25. Ground Pin Current vs Output Current



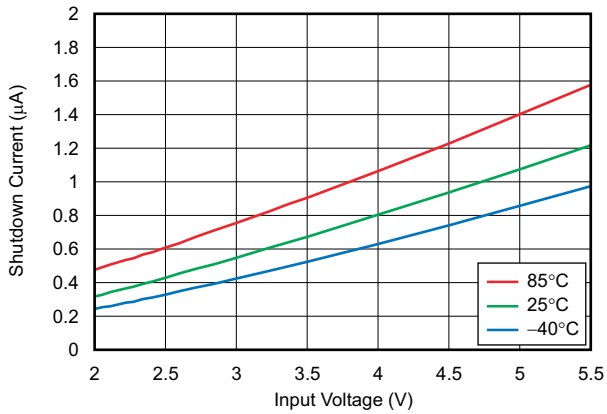
V_{OUT} = 1.2 V

Figure 6-26. Shutdown Current vs Input Voltage



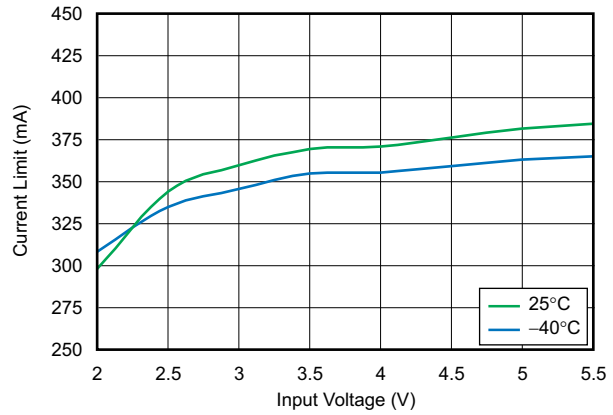
V_{OUT} = 2.8 V

Figure 6-27. Shutdown Current vs Input Voltage



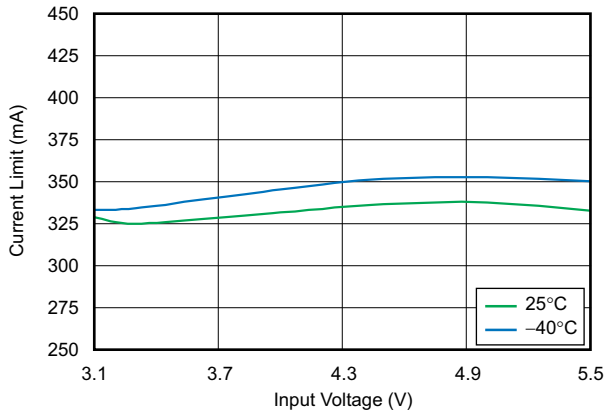
V_{OUT} = 5.0 V

Figure 6-28. Shutdown Current vs Input Voltage



V_{OUT} = 1.2 V

Figure 6-29. Current Limit vs Input Voltage



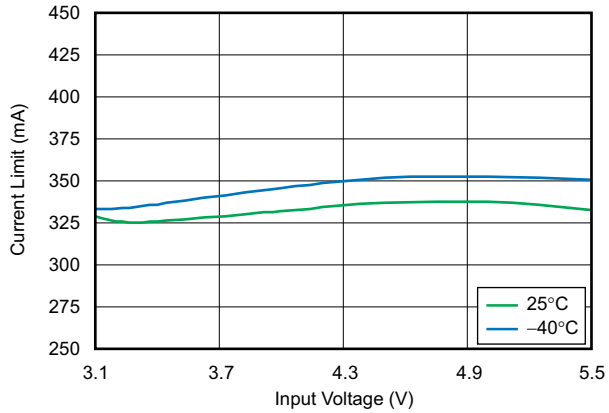
V_{OUT} = 2.8 V

Figure 6-30. Current Limit vs Input Voltage

Copyright © 2023 Texas Instruments Incorporated

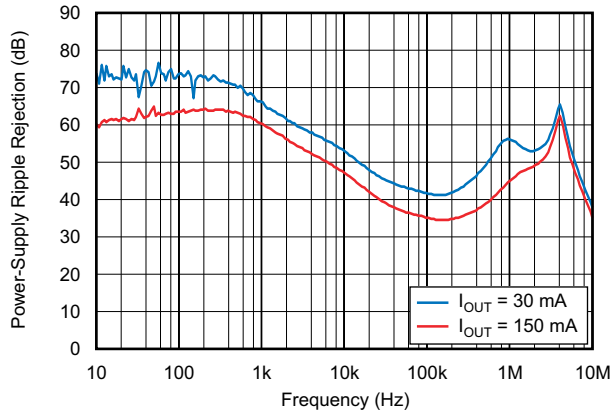
6.6 Typical Characteristics (continued)

at T_J = –40°C to +85°C, V_{IN} = $V_{OUT(nom)}$ + 0.5 V or 2.0 V (whichever is greater), I_{OUT} = 10 mA, V_{EN} = V_{IN}, and C_{OUT} = 1 µF (unless otherwise noted); typical values are at T_J = 25°C



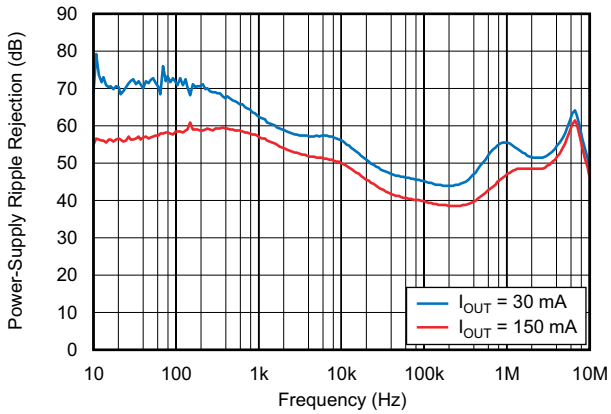
V_{OUT} = 5.0 V

Figure 6-31. Current Limit vs Input Voltage



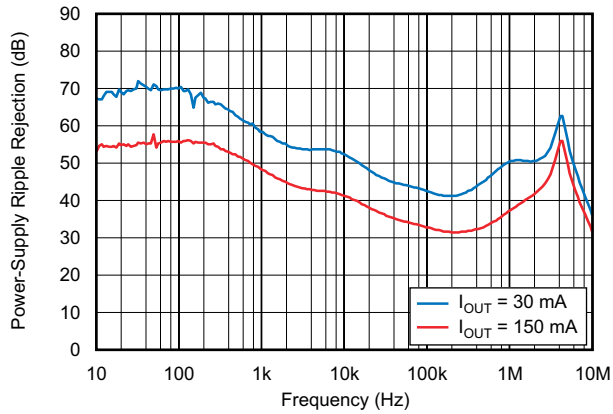
V_{OUT} = 1.2 V

Figure 6-32. Power-Supply Ripple Rejection vs Frequency



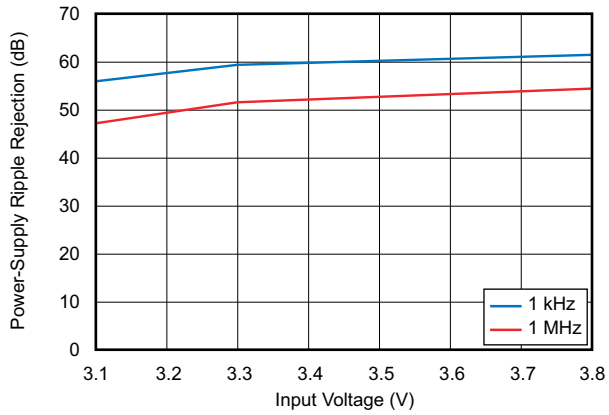
V_{OUT} = 2.8 V

Figure 6-33. Power-Supply Ripple Rejection vs Frequency



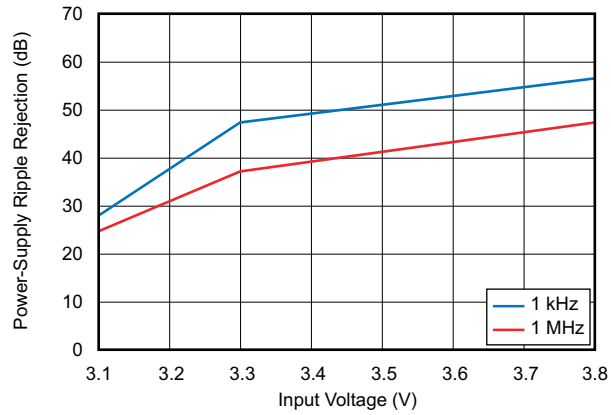
V_{OUT} = 5.0 V

Figure 6-34. Power-Supply Ripple Rejection vs Frequency



V_{OUT} = 2.8 V, I_{OUT} = 30 mA

Figure 6-35. Power-Supply Ripple Rejection vs Input Voltage



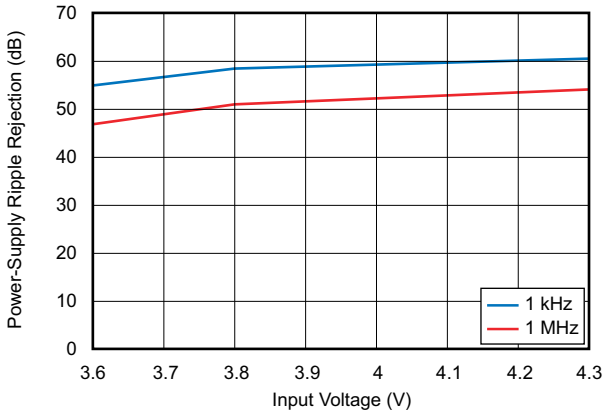
V_{OUT} = 2.8 V, I_{OUT} = 150 mA

Figure 6-36. Power-Supply Ripple Rejection vs Input Voltage

Copyright © 2023 Texas Instruments Incorporated

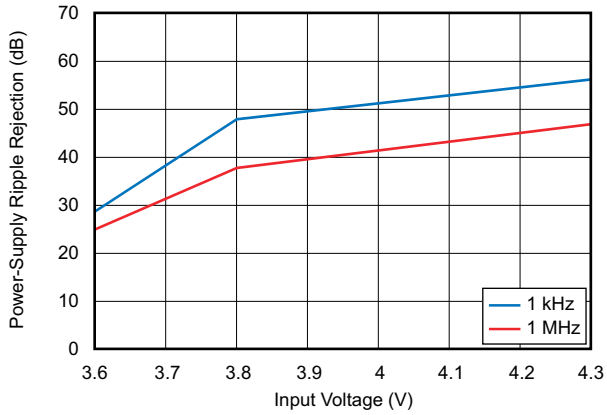
6.6 Typical Characteristics (continued)

at T_J = –40°C to +85°C, V_{IN} = $V_{OUT(nom)}$ + 0.5 V or 2.0 V (whichever is greater), I_{OUT} = 10 mA, V_{EN} = V_{IN}, and C_{OUT} = 1 µF (unless otherwise noted); typical values are at T_J = 25°C



V_{OUT} = 3.3 V, I_{OUT} = 30 mA

Figure 6-37. Power-Supply Ripple Rejection vs Input Voltage



V_{OUT} = 3.3 V, I_{OUT} = 150 mA

Figure 6-38. Power-Supply Ripple Rejection vs Input Voltage

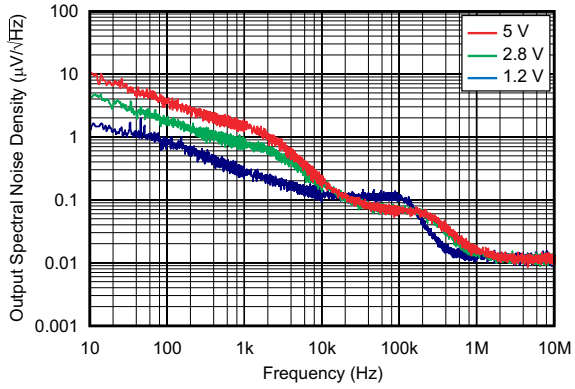
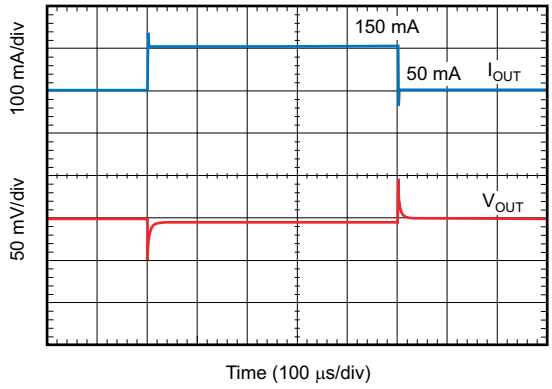
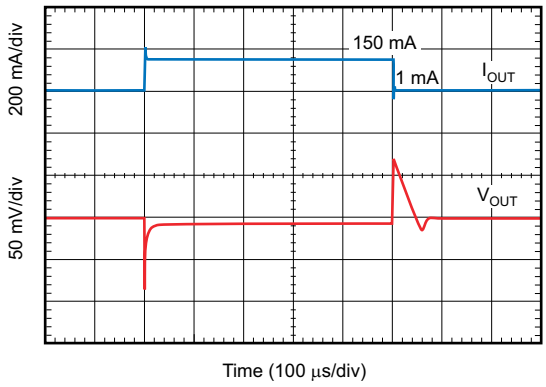


Figure 6-39. Output Spectral Noise Density vs Frequency



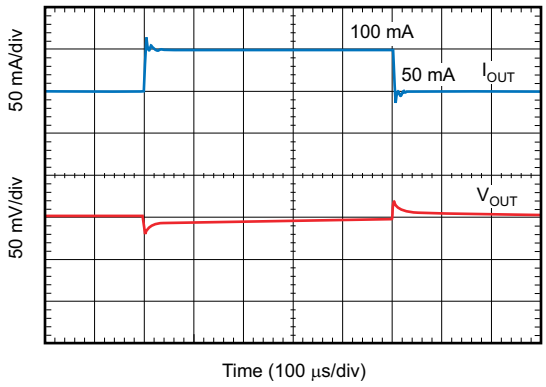
V_{OUT} = 1.2 V

Figure 6-40. Load Transient Response



V_{OUT} = 1.2 V

Figure 6-41. Load Transient Response



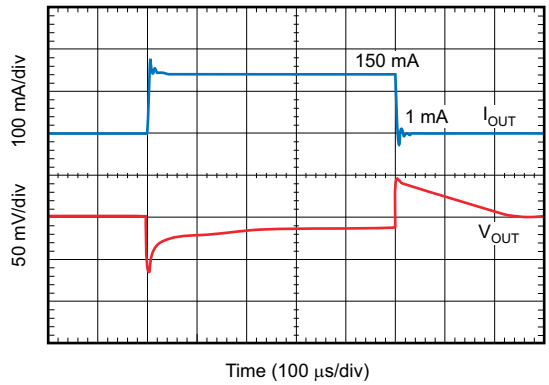
V_{OUT} = 2.8 V

Figure 6-42. Load Transient Response

Copyright © 2023 Texas Instruments Incorporated

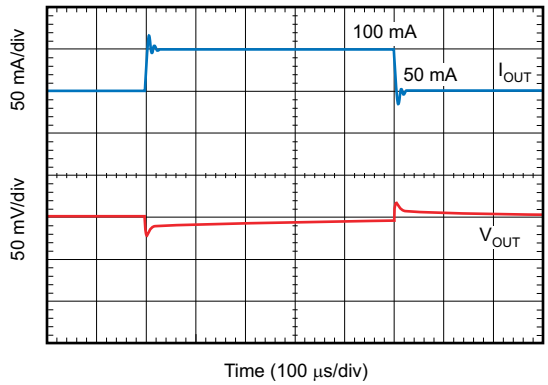
6.6 Typical Characteristics (continued)

at T_J = –40°C to +85°C, V_{IN} = $V_{OUT(nom)}$ + 0.5 V or 2.0 V (whichever is greater), I_{OUT} = 10 mA, V_{EN} = V_{IN}, and C_{OUT} = 1 µF (unless otherwise noted); typical values are at T_J = 25°C



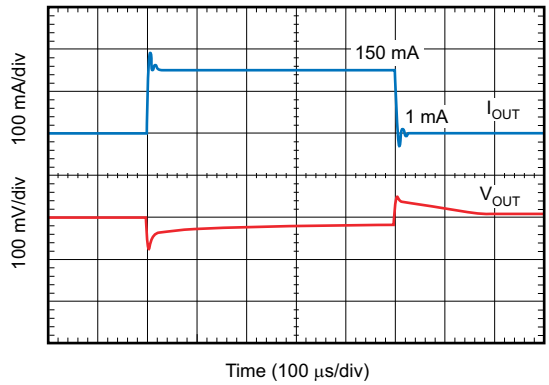
V_{OUT} = 2.8 V

Figure 6-43. Load Transient Response



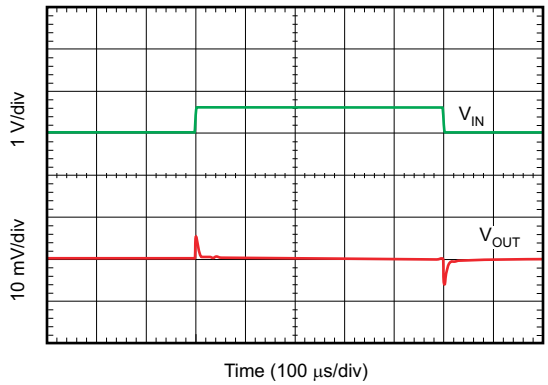
V_{OUT} = 5.0 V

Figure 6-44. Load Transient Response



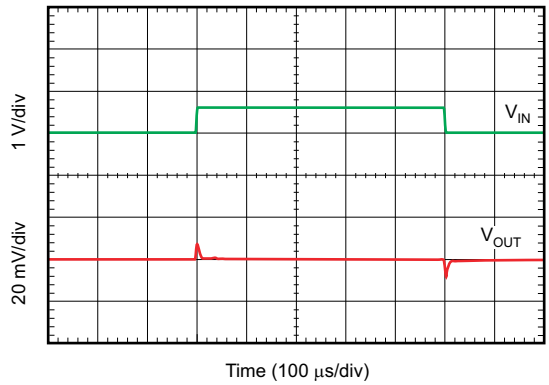
V_{OUT} = 5.0 V

Figure 6-45. Load Transient Response



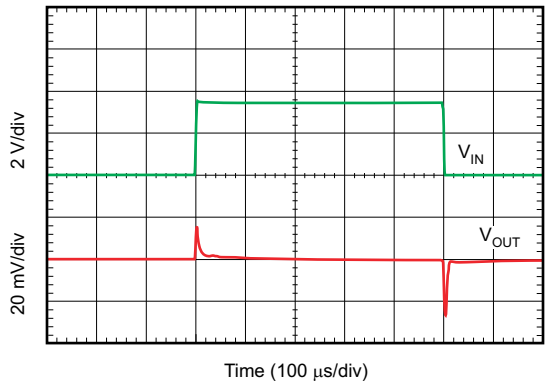
V_{OUT} = 1.2 V, I_{OUT} = 150 mA

Figure 6-46. Line Transient Response



V_{OUT} = 1.2 V, I_{OUT} = 200 mA

Figure 6-47. Line Transient Response



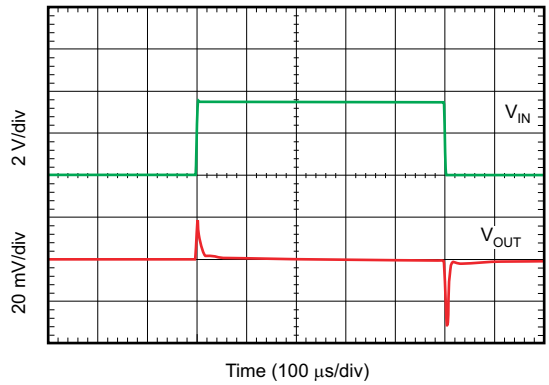
V_{OUT} = 1.2 V, I_{OUT} = 150 mA

Figure 6-48. Line Transient Response

Copyright © 2023 Texas Instruments Incorporated

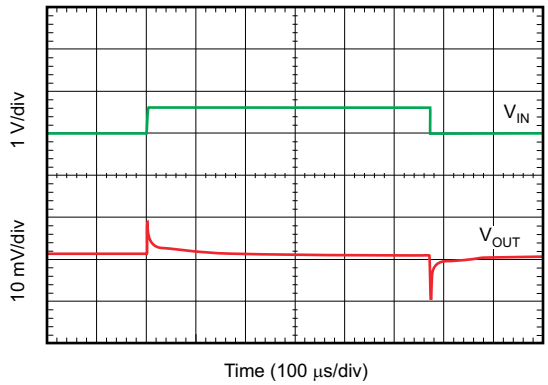
6.6 Typical Characteristics (continued)

at T_J = –40°C to +85°C, V_{IN} = $V_{OUT(nom)}$ + 0.5 V or 2.0 V (whichever is greater), I_{OUT} = 10 mA, V_{EN} = V_{IN}, and C_{OUT} = 1 µF (unless otherwise noted); typical values are at T_J = 25°C



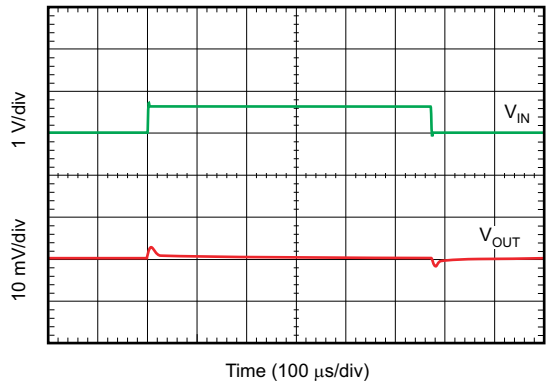
V_{OUT} = 1.2 V, I_{OUT} = 200 mA

Figure 6-49. Line Transient Response



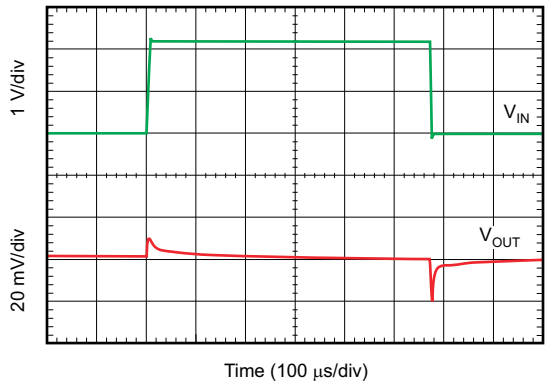
V_{OUT} = 2.8 V, I_{OUT} = 150 mA

Figure 6-50. Line Transient Response



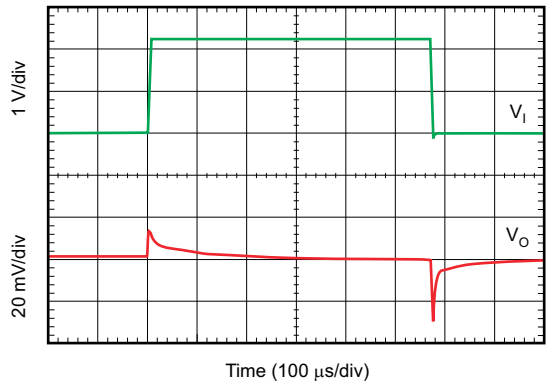
V_{OUT} = 2.8 V, I_{OUT} = 200 mA

Figure 6-51. Line Transient Response



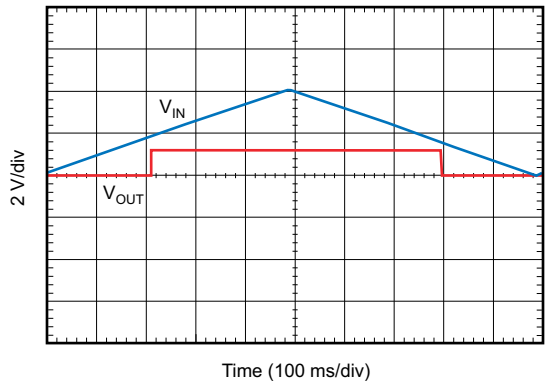
V_{OUT} = 2.8 V, I_{OUT} = 200 mA

Figure 6-52. Line Transient Response



V_{OUT} = 2.8 V, I_{OUT} = 200 mA

Figure 6-53. Line Transient Response



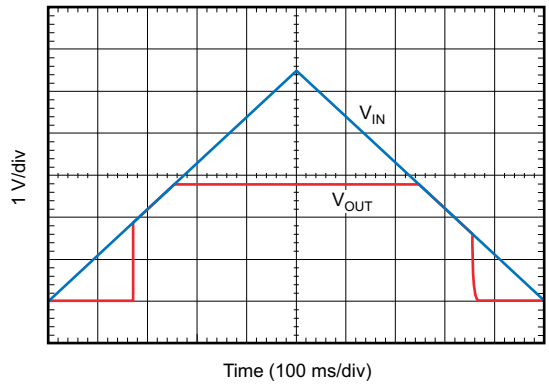
V_{OUT} = 1.2 V, I_{OUT} = 30 mA

Figure 6-54. V_{IN} Ramp Up, Ramp Down Response

Copyright © 2023 Texas Instruments Incorporated

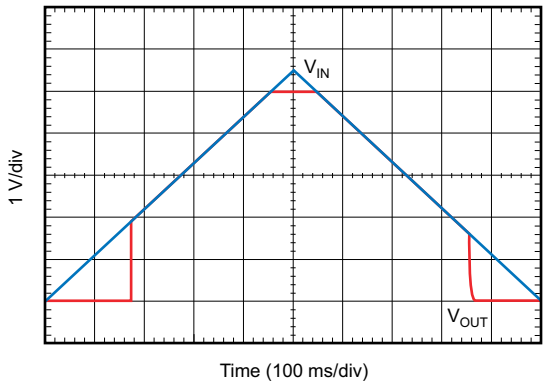
6.6 Typical Characteristics (continued)

at T_J = –40°C to +85°C, V_{IN} = $V_{OUT(nom)}$ + 0.5 V or 2.0 V (whichever is greater), I_{OUT} = 10 mA, V_{EN} = V_{IN}, and C_{OUT} = 1 µF (unless otherwise noted); typical values are at T_J = 25°C



V_{OUT} = 2.8 V, I_{OUT} = 30 mA

Figure 6-55. V_{IN} Ramp Up, Ramp Down Response



V_{OUT} = 5 V, I_{OUT} = 30 mA

Figure 6-56. V_{IN} Ramp Up, Ramp Down Response

7 Detailed Description

7.1 Overview

The TLV707 series (TLV707 and TLV707P) belongs to a family of low-dropout regulators (LDOs). This device consumes low quiescent current and delivers excellent line and load transient performance. These characteristics, combined with low noise and very good PSRR with little (V_{IN} – V_{OUT}) headroom, make this device designed for portable RF applications.

7.2 Functional Block Diagrams

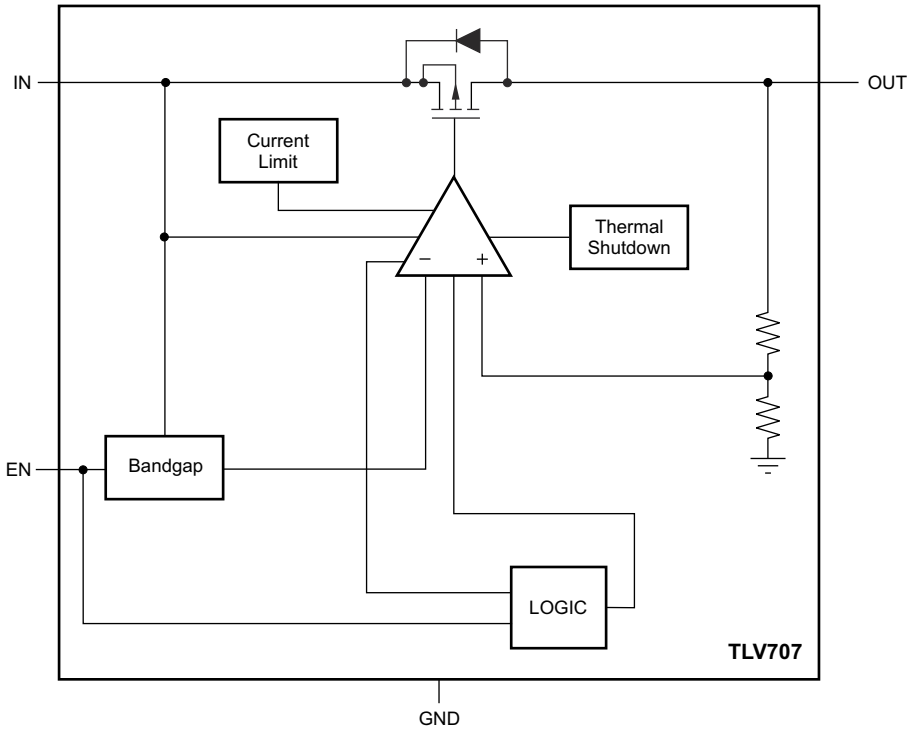


Figure 7-1. TLV707 Block Diagram

Copyright © 2023 Texas Instruments Incorporated

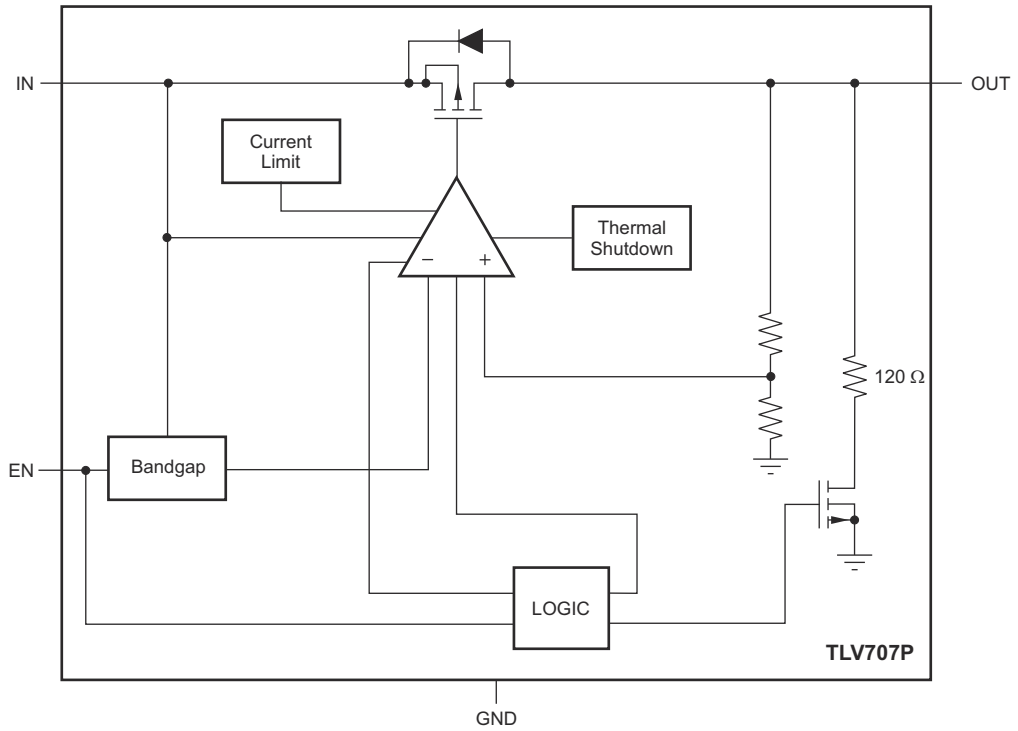


Figure 7-2. TLV707P Block Diagram

7.3 Feature Description

This LDO regulator offers current limit and thermal protection. The operating junction temperature of this device is –40°C to +125°C.

7.3.1 Internal Current Limit

The internal current limit helps protect the regulator during fault conditions. During current limit, the output sources a fixed amount of current that is largely independent of the output voltage. In such a case, the output voltage is not regulated, and is $V_{OUT} = I_{LIMIT} \times R_{LOAD}$. The PMOS pass transistor dissipates $(V_{IN} – V_{OUT}) \times I_{LIMIT}$ until thermal shutdown is triggered and the device turns off. When the device cools, the internal thermal shutdown circuit turns the device back on. If the fault condition continues, the device cycles between current limit and thermal shutdown; see the *Thermal Information* table for more details.

The PMOS pass transistor has a built-in body diode that conducts current when the voltage at OUT exceeds the voltage at IN. This current is not limited, so if extended reverse voltage operation is anticipated, external limiting to 5% of the rated output current is recommended.

Copyright © 2023 Texas Instruments Incorporated

7.3.2 Shutdown

The enable pin (EN) is active high. The device is enabled when voltage at the EN pin goes above 0.9 V. This relatively lower voltage value required to turn on the LDO can also be used to power the device when connected to a GPIO of a newer processor, where the GPIO logic 1 voltage level is lower than that of traditional microcontrollers. The device is turned off when the EN pin is held at less than 0.4 V. When shutdown capability is not required, EN can be connected to the IN pin.

The TLV707P version has internal active pulldown circuitry that discharges the output with a time constant as given by Equation 1:

$$\tau = \frac{(120 \bullet R_L)}{(120 + R_L)} \bullet C_{OUT} \tag{1}$$

where:

- R_L = Load resistance
- C_{OUT} = Output capacitor

7.4 Device Functional Modes

The TLV707 series is specified over the recommended operating conditions (see the *Recommended Operating Conditions* table). The specifications can possibly not be met when exposed to conditions outside of the recommended operating range.

In order to turn on the regulator, the EN pin must be driven over 0.9 V. Driving the EN pin below 0.4 V causes the regulator to enter shutdown mode.

In shutdown, the current consumption of the device is reduced to 1 µA, typically.

Copyright © 2023 Texas Instruments Incorporated

8 Application and Implementation

Note

Information in the following applications sections is not part of the TI component specification, and TI does not warrant its accuracy or completeness. TI's customers are responsible for determining suitability of components for their purposes, as well as validating and testing their design implementation to confirm system functionality.

8.1 Application Information

The TLV707 series is a low-dropout regulator (LDO) with low quiescent current that delivers excellent line and load transient performance. This LDO regulator offers current limit and thermal protection. The operating junction temperature of this device series is –40°C to +125°C.

8.2 Typical Application

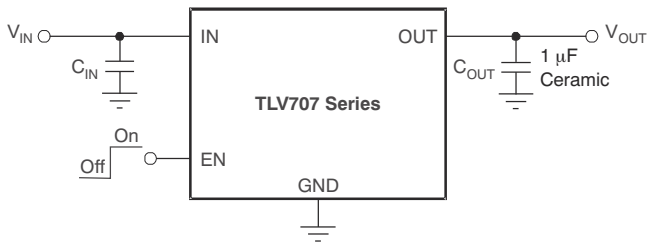


Figure 8-1. Typical Application Circuit

8.2.1 Design Requirements

Provide an input supply with adequate headroom to meet minimum VIN requirements (as shown in Table 8-1), compensate for the GND pin current, and to power the load.

Table 8-1. Design Parameters

PARAMETER	DESIGN REQUIREMENT
Input voltage	1.8 V–3.6 V
Output voltage	1.2 V
Output current	100 mA

8.2.2 Detailed Design Procedure

8.2.2.1 Input and Output Capacitor Requirements

Generally, 1.0-µF X5R- and X7R-type ceramic capacitors are recommended because these capacitors have minimal variation in value and equivalent series resistance (ESR) over temperature.

However, the TLV707 is designed to be stable with an effective capacitance of 0.1 µF or larger at the output. Thus, the device is stable with capacitors of other dielectric types as well, as long as the effective capacitance under operating bias voltage and temperature is greater than 0.1 µF. This effective capacitance refers to the capacitance that the LDO detects under operating bias voltage and temperature conditions; that is, the capacitance after taking both bias voltage and temperature derating into consideration. In addition to allowing the use of less expensive dielectrics, this capability of being stable with 0.1-µF effective capacitance also enables the use of smaller footprint capacitors that have higher derating in size- and space-constrained applications.

Using a 0.1-µF rated capacitor at the output of the LDO does not ensure stability because the effective capacitance under the specified operating conditions is less than 0.1 µF. Maximum ESR must be less than 200 mΩ.

Although an input capacitor is not required for stability, good analog design practice is to connect a 0.1-µF to 1.0-µF, low ESR capacitor across the IN pin and GND pin of the regulator. This capacitor counteracts reactive

Copyright © 2023 Texas Instruments Incorporated

input sources and improves transient response, noise rejection, and ripple rejection. A higher-value capacitor can be necessary if large, fast rise-time load transients are anticipated, or if the device is not located close to the power source. If source impedance is more than 2-Ω, a 0.1-μF input capacitor can be necessary to provide stability.

8.2.2.2 Dropout Voltage

The TLV707 series of LDOs use a PMOS pass transistor to achieve low dropout. When (V_{IN} – V_{OUT}) is less than the dropout voltage (V_{DO}), the PMOS pass transistor is in the linear region of operation and the input-to-output resistance is the $R_{DS(ON)}$ of the PMOS pass transistor. V_{DO} scales approximately with output current because the PMOS transistor functions similar to a resistor in dropout.

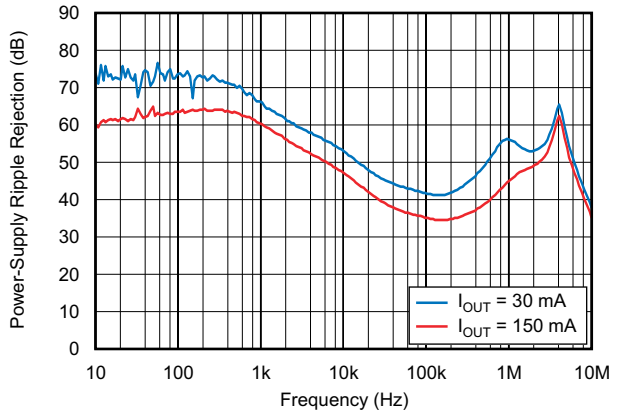
As with any linear regulator, PSRR and transient response are degraded when (V_{IN} – V_{OUT}) approaches dropout.

8.2.2.3 Transient Response

As with any regulator, increasing the size of the output capacitor reduces over- and undershoot magnitude but increases the duration of the transient response.

Copyright © 2023 Texas Instruments Incorporated

8.2.3 Application Curves



V_{OUT} = 1.2 V

Figure 8-2. Power-Supply Ripple Rejection vs Frequency

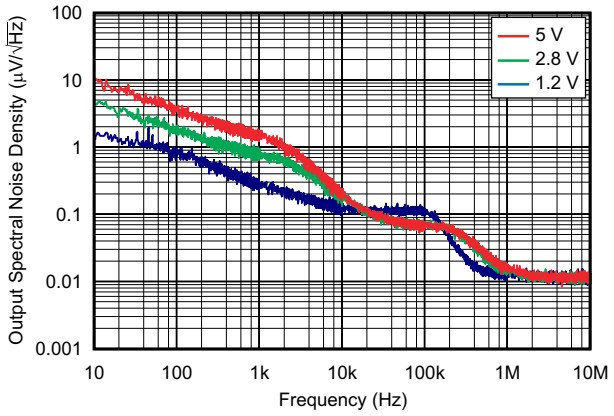
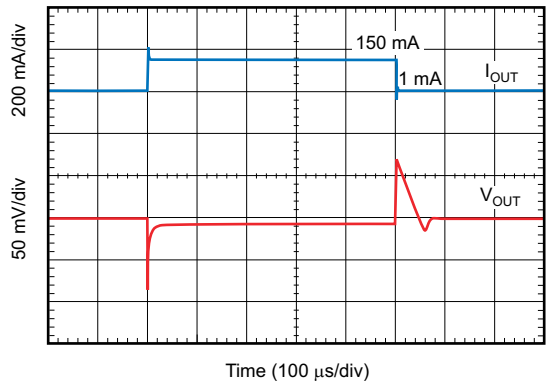
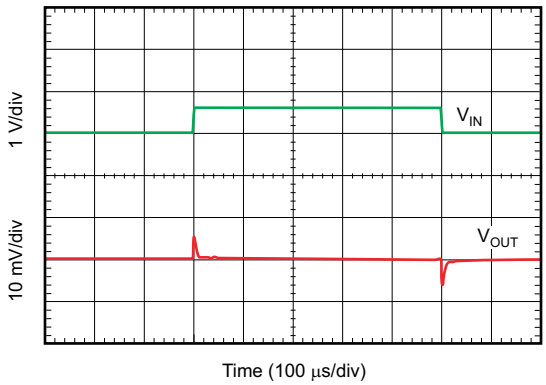


Figure 8-3. Output Spectral Noise Density vs Frequency



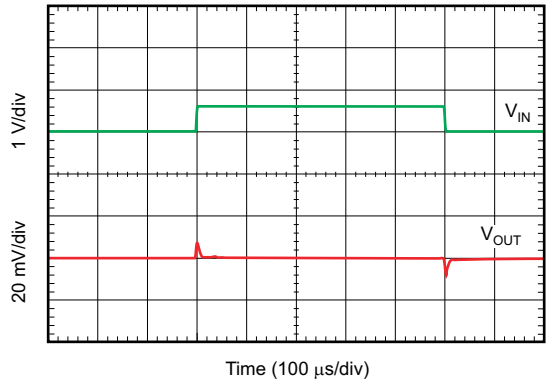
V_{OUT} = 1.2 V

Figure 8-4. Load Transient Response



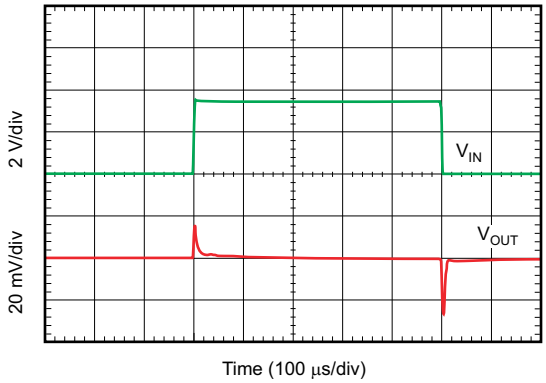
V_{OUT} = 1.2 V, I_{OUT} = 150 mA

Figure 8-5. Line Transient Response



V_{OUT} = 1.2 V, I_{OUT} = 200 mA

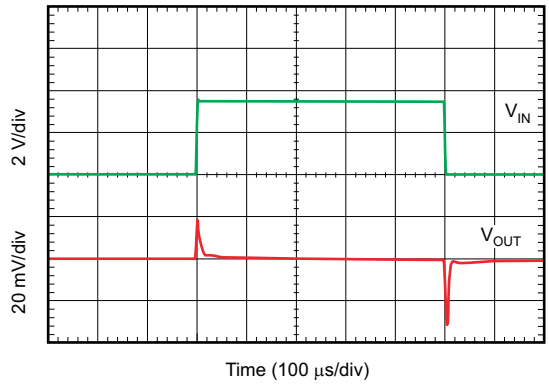
Figure 8-6. Line Transient Response



V_{OUT} = 1.2 V, I_{OUT} = 150 mA

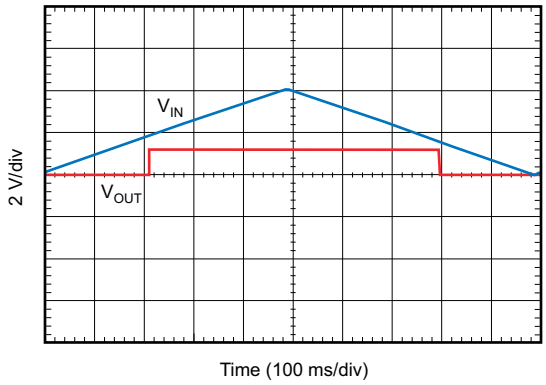
Figure 8-7. Line Transient Response

Copyright © 2023 Texas Instruments Incorporated



V_{OUT} = 1.2 V, I_{OUT} = 200 mA

Figure 8-8. Line Transient Response



V_{OUT} = 1.2 V, I_{OUT} = 30 mA

Figure 8-9. V_{IN} Ramp Up, Ramp Down Response

8.3 Best Design Practices

Place at least one 1.0-µF ceramic capacitor as close as possible to the OUT pin of the regulator.

Do not place the output capacitor more than 10 mm away from the regulator.

Connect a 1.0-µF low equivalent series resistance (ESR) capacitor across the IN pin and GND input of the regulator for improved transient performance.

Do not exceed the absolute maximum ratings.

Copyright © 2023 Texas Instruments Incorporated

8.4 Power Supply Recommendations

The device is designed to operate from an input voltage supply range between 2.0 V and 5.5 V. The input voltage range provides adequate headroom in order for the device to have a regulated output. This input supply must be well regulated (see Figure 6-46 through Figure 6-53). If the input supply is noisy, additional input capacitors with low ESR help improve the output noise performance.

8.5 Layout

8.5.1 Layout Guidelines

8.5.1.1 Board Layout Recommendations to Improve PSRR and Noise Performance

Place input and output capacitors as close to the device pins as possible. To improve ac performance (such as PSRR, output noise, and transient response), design the board with separate ground planes for V_{IN} and V_{OUT}, with the ground plane connected only at the GND pin of the device, as shown in Figure 8-10. In addition, connect the ground connection for the output capacitor directly to the GND pin of the device. High ESR capacitors can degrade PSRR performance.

8.5.1.2 Package Mounting

Solder pad footprint recommendations are available from TI's website at www.ti.com. The recommended land pattern for the DQN (X2SON-4) package is provided in the *Mechanical, Packaging, and Orderable Information* section.

8.5.1.3 Thermal Considerations

Thermal protection disables the output when the junction temperature rises to approximately 160°C, allowing the device to cool. When the junction temperature cools to approximately 140°C, the output circuitry is again enabled. Depending on power dissipation, thermal resistance, and ambient temperature, the thermal protection circuit can cycle on and off. This cycling limits the dissipation of the regulator, thus protecting the regulator from damage as a result of overheating.

Any tendency to activate the thermal protection circuit indicates excessive power dissipation or an inadequate heat sink. For reliable operation, limit junction temperature to 125°C (maximum). To estimate the margin of safety in a complete design (including heat sink), increase the ambient temperature until the thermal protection is triggered; use worst-case loads and signal conditions.

For good reliability, thermal protection triggers at least 35°C above the maximum expected ambient condition of the particular application. This configuration produces a worst-case junction temperature of 125°C at the highest expected ambient temperature and worst-case load.

The internal protection circuitry of the LDO is designed to protect against overload conditions. This circuitry is not intended to replace proper heat sinking. Continuously running the LDO into thermal shutdown degrades device reliability.

8.5.1.4 Power Dissipation

The ability to remove heat from the die is different for each package type, presenting different considerations in the printed-circuit-board (PCB) layout. The PCB area around the device that is free of other components moves the heat from the device to the ambient air.

Performance data for JEDEC low- and high-K boards are given in the *Thermal Information* table. Using heavier copper increases the effectiveness in removing heat from the device. The addition of plated through-holes to heat-dissipating layers also improves heat sink effectiveness.

Power dissipation depends on input voltage and load conditions. Power dissipation (P_D) is equal to the product of the output current and the voltage drop across the output pass element, as shown in Equation 2.

$$P_D = (V_{IN} - V_{OUT}) \times I_{OUT} \tag{2}$$

8.5.2 Layout Example

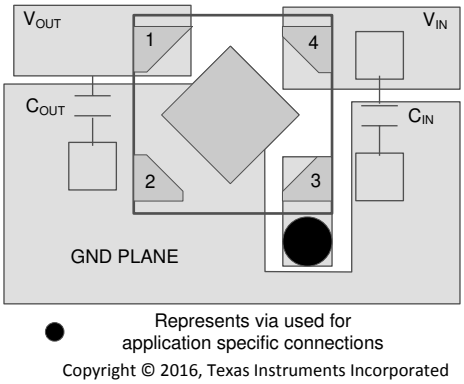


Figure 8-10. Recommended Layout Example

Copyright © 2023 Texas Instruments Incorporated

9 Device and Documentation Support

9.1 Device Support

9.1.1 Development Support

9.1.1.1 Evaluation Modules

An evaluation module (EVM) is available to assist in the initial circuit performance evaluation using the TLV707 and TLV707P. SLVU416 details the design kits and evaluation modules for TLV70728EVM-612.

The EVM can be requested at the Texas Instruments web site through the TLV707 and TLV707P product folders, or purchased directly from the TI eStore.

9.1.1.2 Spice Models

Computer simulation of circuit performance using SPICE is often useful when analyzing the performance of analog circuits and systems. A SPICE model for the TLV707 and TLV707P is available through the respective device product folders under *Simulation Models*.

9.1.2 Device Nomenclature

Table 9-1. Ordering Information(1)

PRODUCT	V_{OUT} (2)
TLV707**xx(x)(A)(*P*)yyyz**	**xx(x)** is the nominal output voltage. For output voltages with a resolution of 100 mV, two digits are used in the ordering number; otherwise, three digits are used (for example, 18 = 1.8 V, 285 = 2.85 V). **A** indicates a more recent design revision. **P** is optional; devices with P have an LDO regulator with an active output discharge. **yyy** is the package designator. **z** is the package quantity. Use ***R*** for reel (3000 pieces), and ***T*** for tape (250 pieces).

(1) For the most current package and ordering information see the Package Option Addendum at the end of this document, or visit the device product folder at www.ti.com.
(2) Output voltages from 0.85 V to 5.0 V in 50-mV increments are available. Contact factory for details and availability.

9.2 Documentation Support

9.2.1 Related Documentation

For related documentation see the following:

- Texas Instruments, *TLV70728EVM-612 Evaluation Module* user guide

9.3 Receiving Notification of Documentation Updates

To receive notification of documentation updates, navigate to the device product folder on ti.com. Click on *Subscribe to updates* to register and receive a weekly digest of any product information that has changed. For change details, review the revision history included in any revised document.

9.4 Support Resources

TI E2E™ support forums are an engineer's go-to source for fast, verified answers and design help — straight from the experts. Search existing answers or ask your own question to get the quick design help you need.

Linked content is provided "AS IS" by the respective contributors. They do not constitute TI specifications and do not necessarily reflect TI's views; see TI's Terms of Use.

9.5 Trademarks

TI E2E™ is a trademark of Texas Instruments.
All trademarks are the property of their respective owners.

Copyright © 2023 Texas Instruments Incorporated

9.6 Electrostatic Discharge Caution

This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

9.7 Glossary

TI Glossary This glossary lists and explains terms, acronyms, and definitions.

10 Mechanical, Packaging, and Orderable Information

The following pages include mechanical, packaging, and orderable information. This information is the most current data available for the designated family of devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the left-hand navigation.

Copyright © 2023 Texas Instruments Incorporated

PACKAGING INFORMATION

Orderable part number	Status (1)	Material type (2)	Package | Pins	Package qty | Carrier	RoHS (3)	Lead finish/ Ball material (4)	MSL rating/ Peak reflow (5)	Op temp (°C)	Part marking (6)
TLV707085DQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	BY
TLV707085DQNR.B	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	BY
TLV707085DQNT	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	BY
TLV707085DQNT.B	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	BY
TLV70710DQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	BB
TLV70710DQNR.B	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	BB
TLV70710DQNT	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	BB
TLV70710DQNT.B	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	BB
TLV70710PDQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	BC
TLV70710PDQNR.B	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	BC
TLV70710PDQNT	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	BC
TLV70710PDQNT.B	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	BC
TLV707115DQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	B3
TLV707115DQNR.B	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	B3
TLV707115DQNT	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	B3
TLV707115DQNT.B	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	B3
TLV70711PDQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	C3
TLV70711PDQNR.B	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	C3
TLV70711PDQNT	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	C3
TLV70711PDQNT.B	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	C3
TLV70712APDQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	N6
TLV70712APDQNR.A	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	N6
TLV70712PDQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	WJ
TLV70712PDQNR.B	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	WJ
TLV70712PDQNT	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	WJ
TLV70712PDQNT.B	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	WJ
TLV707135DQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	BD
TLV707135DQNR.B	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	BD
TLV707135DQNT	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	BD

Orderable part number	Status (1)	Material type (2)	Package | Pins	Package qty | Carrier	RoHS (3)	Lead finish/ Ball material (4)	MSL rating/ Peak reflow (5)	Op temp (°C)	Part marking (6)
TLV707135DQNT.B	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	BD
TLV70715PDQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	WI
TLV70715PDQNR.B	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	WI
TLV70715PDQNT	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	WI
TLV70715PDQNT.B	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	WI
TLV70717DQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	GD
TLV70717DQNR.B	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	GD
TLV70717DQNT	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	GD
TLV70717DQNT.B	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	GD
TLV707185ADQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	MZ
TLV707185ADQNR.A	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	MZ
TLV707185DQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	ZN
TLV707185DQNR.B	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	ZN
TLV707185DQNT	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	ZN
TLV707185DQNT.B	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	ZN
TLV707185PDQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	B1
TLV707185PDQNR.B	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	B1
TLV707185PDQNT	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	B1
TLV707185PDQNT.B	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	B1
TLV70718ADQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	MX
TLV70718ADQNR.A	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	MX
TLV70718APDQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	N7
TLV70718APDQNR.A	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	N7
TLV70718DQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	ZC
TLV70718DQNR.B	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	ZC
TLV70718DQNT	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	ZC
TLV70718DQNT.B	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	ZC
TLV70718PDQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	SB
TLV70718PDQNR.B	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	SB
TLV70718PDQNRG4	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	SB
TLV70718PDQNRG4.B	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	SB

Orderable part number	Status (1)	Material type (2)	Package | Pins	Package qty | Carrier	RoHS (3)	Lead finish/ Ball material (4)	MSL rating/ Peak reflow (5)	Op temp (°C)	Part marking (6)
TLV70718PDQNT	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	SB
TLV70718PDQNT.B	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	SB
TLV70719PDQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	ZM
TLV70719PDQNR.B	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	ZM
TLV70719PDQNT	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	ZM
TLV70719PDQNT.B	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	ZM
TLV70725DQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	BM
TLV70725DQNR.B	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	BM
TLV70725DQNT	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	BM
TLV70725DQNT.B	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	BM
TLV70725PDQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	AT
TLV70725PDQNR.B	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	AT
TLV70725PDQNT	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	AT
TLV70725PDQNT.B	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	AT
TLV70726DQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	RF
TLV70726DQNR.B	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	RF
TLV70726DQNT	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	RF
TLV70726DQNT.B	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	RF
TLV70726PDQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	SC
TLV70726PDQNR.B	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	SC
TLV70726PDQNT	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	SC
TLV70726PDQNT.B	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	SC
TLV70727PDQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	C6
TLV70727PDQNR.B	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	C6
TLV70727PDQNT	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	C6
TLV70727PDQNT.B	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	C6
TLV707285DQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	RZ
TLV707285DQNR.B	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	RZ
TLV707285DQNT	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	RZ
TLV707285DQNT.B	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	RZ
TLV707285PDQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	XE

Orderable part number	Status (1)	Material type (2)	Package | Pins	Package qty | Carrier	RoHS (3)	Lead finish/ Ball material (4)	MSL rating/ Peak reflow (5)	Op temp (°C)	Part marking (6)
TLV707285PDQNR.B	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	XE
TLV707285PDQNT	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	XE
TLV707285PDQNT.B	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	XE
TLV70728APDQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	N8
TLV70728APDQNR.A	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	N8
TLV70728PDQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	SD
TLV70728PDQNR.B	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	SD
TLV70728PDQNT	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	SD
TLV70728PDQNT.B	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	SD
TLV70729DQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	BF
TLV70729DQNR.B	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	BF
TLV70729DQNT	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	BF
TLV70729DQNT.B	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	BF
TLV70729PDQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	BG
TLV70729PDQNR.B	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	BG
TLV70729PDQNT	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	BG
TLV70729PDQNT.B	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	BG
TLV70730ADQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	N1
TLV70730ADQNR.A	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	N1
TLV70730APDQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	N2
TLV70730APDQNR.A	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	N2
TLV70730DQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	HJ
TLV70730DQNR.B	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	HJ
TLV70730DQNT	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	HJ
TLV70730DQNT.B	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	HJ
TLV70730PDQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	SE
TLV70730PDQNR.B	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	SE
TLV70730PDQNRG4	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	SE
TLV70730PDQNRG4.B	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	SE
TLV70730PDQNT	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	SE
TLV70730PDQNT.B	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	SE

Orderable part number	Status (1)	Material type (2)	Package | Pins	Package qty | Carrier	RoHS (3)	Lead finish/ Ball material (4)	MSL rating/ Peak reflow (5)	Op temp (°C)	Part marking (6)
TLV70731DQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	DI
TLV70731DQNR.B	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	DI
TLV70731DQNT	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	DI
TLV70731DQNT.B	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	DI
TLV70732DQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	C8
TLV70732DQNR.B	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	C8
TLV70732DQNT	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	C8
TLV70732DQNT.B	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	C8
TLV707335ADQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	N5
TLV707335ADQNR.A	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	N5
TLV707335DQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	F6
TLV707335DQNR.B	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	F6
TLV707335DQNT	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	F6
TLV707335DQNT.B	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	F6
TLV70733ADQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	N3
TLV70733ADQNR.A	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	N3
TLV70733APDQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	N4
TLV70733APDQNR.A	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	N4
TLV70733DQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	YH
TLV70733DQNR.B	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	YH
TLV70733DQNR1	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	BN
TLV70733DQNR1.B	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	BN
TLV70733DQNT	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	YH
TLV70733DQNT.B	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	YH
TLV70733PDQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	TI
TLV70733PDQNR.B	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	TI
TLV70733PDQNT	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	TI
TLV70733PDQNT.B	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	TI
TLV70734DQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	AQ
TLV70734DQNR.B	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	AQ
TLV70734DQNT	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	AQ

Orderable part number	Status (1)	Material type (2)	Package | Pins	Package qty | Carrier	RoHS (3)	Lead finish/ Ball material (4)	MSL rating/ Peak reflow (5)	Op temp (°C)	Part marking (6)
TLV70734DQNT.B	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	AQ
TLV70734PDQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	AP
TLV70734PDQNR.B	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	AP
TLV70734PDQNT	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	AP
TLV70734PDQNT.B	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	AP
TLV70736DQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	CC
TLV70736DQNR.B	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	CC
TLV70736DQNT	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	CC
TLV70736DQNT.B	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	CC
TLV70736PDQNR	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	ZO
TLV70736PDQNR.B	Active	Production	X2SON (DQN) | 4	3000 | LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	ZO
TLV70736PDQNT	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	ZO
TLV70736PDQNT.B	Active	Production	X2SON (DQN) | 4	250 | SMALL T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	ZO

(1) **Status:** For more details on status, see our product life cycle.

(2) **Material type:** When designated, preproduction parts are prototypes/experimental devices, and are not yet approved or released for full production. Testing and final process, including without limitation quality assurance, reliability performance testing, and/or process qualification, may not yet be complete, and this item is subject to further changes or possible discontinuation. If available for ordering, purchases will be subject to an additional waiver at checkout, and are intended for early internal evaluation purposes only. These items are sold without warranties of any kind.

(3) **RoHS values:** Yes, No, RoHS Exempt. See the TI RoHS Statement for additional information and value definition.

(4) **Lead finish/Ball material:** Parts may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

(5) **MSL rating/Peak reflow:** The moisture sensitivity level ratings and peak solder (reflow) temperatures. In the event that a part has multiple moisture sensitivity ratings, only the lowest level per JEDEC standards is shown. Refer to the shipping label for the actual reflow temperature that will be used to mount the part to the printed circuit board.

(6) **Part marking:** There may be an additional marking, which relates to the logo, the lot trace code information, or the environmental category of the part.

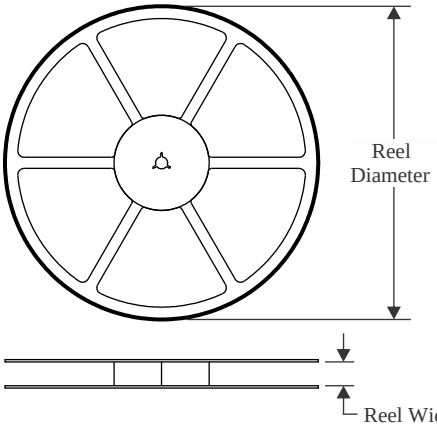
Multiple part markings will be inside parentheses. Only one part marking contained in parentheses and separated by a "~" will appear on a part. If a line is indented then it is a continuation of the previous line and the two combined represent the entire part marking for that device.

Important Information and Disclaimer: The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

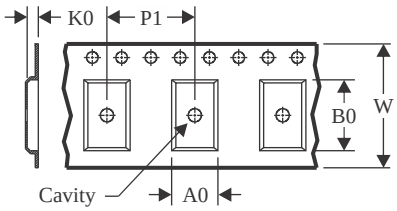
In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

TAPE AND REEL INFORMATION

REEL DIMENSIONS

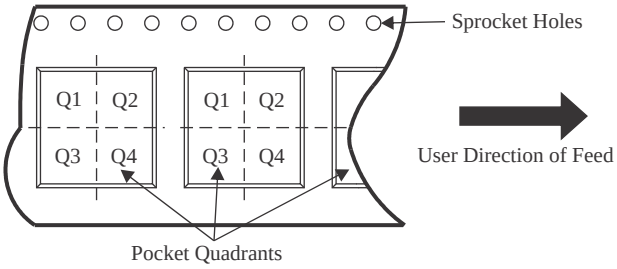


TAPE DIMENSIONS



A0	Dimension designed to accommodate the component width
B0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*All dimensions are nominal

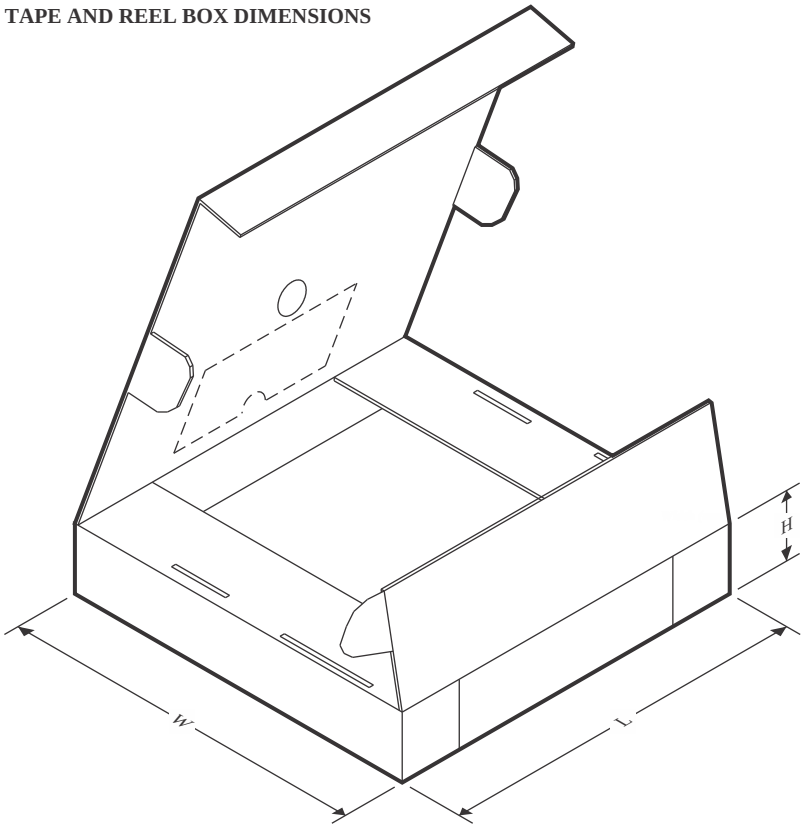
Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TLV707085DQNR	X2SON	DQN	4	3000	180.0	9.5	1.16	1.16	0.5	4.0	8.0	Q2
TLV707085DQNT	X2SON	DQN	4	250	180.0	9.5	1.16	1.16	0.5	4.0	8.0	Q2
TLV70710DQNR	X2SON	DQN	4	3000	180.0	9.5	1.16	1.16	0.5	4.0	8.0	Q2
TLV70710DQNR	X2SON	DQN	4	3000	180.0	8.4	1.16	1.16	0.63	4.0	8.0	Q2
TLV70710DQNT	X2SON	DQN	4	250	180.0	9.5	1.16	1.16	0.5	4.0	8.0	Q2
TLV70710DQNT	X2SON	DQN	4	250	180.0	8.4	1.16	1.16	0.63	4.0	8.0	Q2
TLV70710PDQNR	X2SON	DQN	4	3000	180.0	9.5	1.16	1.16	0.5	4.0	8.0	Q2
TLV70710PDQNR	X2SON	DQN	4	3000	180.0	8.4	1.16	1.16	0.63	4.0	8.0	Q2
TLV70710PDQNT	X2SON	DQN	4	250	180.0	8.4	1.16	1.16	0.63	4.0	8.0	Q2
TLV70710PDQNT	X2SON	DQN	4	250	180.0	9.5	1.16	1.16	0.5	4.0	8.0	Q2
TLV707115DQNR	X2SON	DQN	4	3000	180.0	8.4	1.16	1.16	0.63	4.0	8.0	Q2
TLV707115DQNR	X2SON	DQN	4	3000	180.0	9.5	1.16	1.16	0.5	4.0	8.0	Q2
TLV707115DQNT	X2SON	DQN	4	250	180.0	9.5	1.16	1.16	0.5	4.0	8.0	Q2
TLV707115DQNT	X2SON	DQN	4	250	180.0	8.4	1.16	1.16	0.63	4.0	8.0	Q2
TLV70711PDQNR	X2SON	DQN	4	3000	180.0	9.5	1.16	1.16	0.5	4.0	8.0	Q2
TLV70711PDQNT	X2SON	DQN	4	250	180.0	9.5	1.16	1.16	0.5	4.0	8.0	Q2

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TLV70712APDQNR	X2SON	DQN	4	3000	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV70712PDQNR	X2SON	DQN	4	3000	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV70712PDQNT	X2SON	DQN	4	250	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV707135DQNR	X2SON	DQN	4	3000	180.0	9.5	1.16	1.16	0.5	4.0	8.0	Q2
TLV707135DQNT	X2SON	DQN	4	250	180.0	9.5	1.16	1.16	0.5	4.0	8.0	Q2
TLV70715PDQNR	X2SON	DQN	4	3000	180.0	8.4	1.16	1.16	0.63	4.0	8.0	Q2
TLV70715PDQNR	X2SON	DQN	4	3000	180.0	9.5	1.16	1.16	0.5	4.0	8.0	Q2
TLV70715PDQNT	X2SON	DQN	4	250	180.0	8.4	1.16	1.16	0.63	4.0	8.0	Q2
TLV70715PDQNT	X2SON	DQN	4	250	180.0	9.5	1.16	1.16	0.5	4.0	8.0	Q2
TLV70717DQNR	X2SON	DQN	4	3000	180.0	9.5	1.16	1.16	0.5	4.0	8.0	Q2
TLV70717DQNT	X2SON	DQN	4	250	180.0	9.5	1.16	1.16	0.5	4.0	8.0	Q2
TLV707185ADQNR	X2SON	DQN	4	3000	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV707185DQNR	X2SON	DQN	4	3000	180.0	9.5	1.16	1.16	0.5	4.0	8.0	Q2
TLV707185DQNT	X2SON	DQN	4	250	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV707185PDQNR	X2SON	DQN	4	3000	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV707185PDQNT	X2SON	DQN	4	250	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV70718ADQNR	X2SON	DQN	4	3000	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV70718APDQNR	X2SON	DQN	4	3000	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV70718DQNR	X2SON	DQN	4	3000	180.0	9.5	1.16	1.16	0.5	4.0	8.0	Q2
TLV70718DQNR	X2SON	DQN	4	3000	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV70718DQNT	X2SON	DQN	4	250	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV70718PDQNR	X2SON	DQN	4	3000	180.0	9.5	1.16	1.16	0.5	4.0	8.0	Q2
TLV70718PDQNR	X2SON	DQN	4	3000	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV70718PDQNRG4	X2SON	DQN	4	3000	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV70718PDQNT	X2SON	DQN	4	250	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV70719PDQNR	X2SON	DQN	4	3000	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV70719PDQNT	X2SON	DQN	4	250	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV70725DQNR	X2SON	DQN	4	3000	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV70725DQNT	X2SON	DQN	4	250	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV70725PDQNR	X2SON	DQN	4	3000	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV70725PDQNT	X2SON	DQN	4	250	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV70726DQNR	X2SON	DQN	4	3000	180.0	9.5	1.16	1.16	0.5	4.0	8.0	Q2
TLV70726DQNR	X2SON	DQN	4	3000	180.0	8.4	1.16	1.16	0.63	4.0	8.0	Q2
TLV70726DQNT	X2SON	DQN	4	250	180.0	9.5	1.16	1.16	0.5	4.0	8.0	Q2
TLV70726DQNT	X2SON	DQN	4	250	180.0	8.4	1.16	1.16	0.63	4.0	8.0	Q2
TLV70726PDQNR	X2SON	DQN	4	3000	180.0	9.5	1.16	1.16	0.5	4.0	8.0	Q2
TLV70726PDQNR	X2SON	DQN	4	3000	180.0	8.4	1.16	1.16	0.63	4.0	8.0	Q2
TLV70726PDQNT	X2SON	DQN	4	250	180.0	8.4	1.16	1.16	0.63	4.0	8.0	Q2
TLV70726PDQNT	X2SON	DQN	4	250	180.0	9.5	1.16	1.16	0.5	4.0	8.0	Q2
TLV70727PDQNR	X2SON	DQN	4	3000	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV70727PDQNT	X2SON	DQN	4	250	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TLV707285DQNR	X2SON	DQN	4	3000	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV707285DQNT	X2SON	DQN	4	250	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV707285PDQNR	X2SON	DQN	4	3000	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV707285PDQNT	X2SON	DQN	4	250	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV70728APDQNR	X2SON	DQN	4	3000	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV70728PDQNR	X2SON	DQN	4	3000	180.0	9.5	1.16	1.16	0.5	4.0	8.0	Q2
TLV70728PDQNT	X2SON	DQN	4	250	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV70729DQNR	X2SON	DQN	4	3000	180.0	9.5	1.16	1.16	0.5	4.0	8.0	Q2
TLV70729DQNR	X2SON	DQN	4	3000	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV70729DQNT	X2SON	DQN	4	250	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV70729PDQNR	X2SON	DQN	4	3000	180.0	9.5	1.16	1.16	0.5	4.0	8.0	Q2
TLV70729PDQNR	X2SON	DQN	4	3000	180.0	8.4	1.16	1.16	0.63	4.0	8.0	Q2
TLV70729PDQNT	X2SON	DQN	4	250	180.0	8.4	1.16	1.16	0.63	4.0	8.0	Q2
TLV70729PDQNT	X2SON	DQN	4	250	180.0	9.5	1.16	1.16	0.5	4.0	8.0	Q2
TLV70730ADQNR	X2SON	DQN	4	3000	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV70730APDQNR	X2SON	DQN	4	3000	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV70730DQNR	X2SON	DQN	4	3000	180.0	9.5	1.16	1.16	0.5	4.0	8.0	Q2
TLV70730DQNT	X2SON	DQN	4	250	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV70730PDQNR	X2SON	DQN	4	3000	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV70730PDQNR	X2SON	DQN	4	3000	180.0	9.5	1.16	1.16	0.5	4.0	8.0	Q2
TLV70730PDQNRG4	X2SON	DQN	4	3000	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV70730PDQNT	X2SON	DQN	4	250	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV70731DQNR	X2SON	DQN	4	3000	180.0	9.5	1.16	1.16	0.5	4.0	8.0	Q2
TLV70731DQNT	X2SON	DQN	4	250	180.0	9.5	1.16	1.16	0.5	4.0	8.0	Q2
TLV70732DQNR	X2SON	DQN	4	3000	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV70732DQNR	X2SON	DQN	4	3000	180.0	9.5	1.16	1.16	0.5	4.0	8.0	Q2
TLV70732DQNT	X2SON	DQN	4	250	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV707335ADQNR	X2SON	DQN	4	3000	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV707335DQNR	X2SON	DQN	4	3000	180.0	9.5	1.16	1.16	0.5	4.0	8.0	Q2
TLV707335DQNT	X2SON	DQN	4	250	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV70733ADQNR	X2SON	DQN	4	3000	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV70733APDQNR	X2SON	DQN	4	3000	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV70733DQNR	X2SON	DQN	4	3000	180.0	9.5	1.16	1.16	0.5	4.0	8.0	Q2
TLV70733DQNR	X2SON	DQN	4	3000	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV70733DQNR1	X2SON	DQN	4	3000	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q1
TLV70733DQNT	X2SON	DQN	4	250	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV70733PDQNR	X2SON	DQN	4	3000	180.0	9.5	1.16	1.16	0.5	4.0	8.0	Q2
TLV70733PDQNT	X2SON	DQN	4	250	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV70734DQNR	X2SON	DQN	4	3000	180.0	9.5	1.16	1.16	0.5	4.0	8.0	Q2
TLV70734DQNR	X2SON	DQN	4	3000	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV70734DQNT	X2SON	DQN	4	250	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TLV70734PDQNR	X2SON	DQN	4	3000	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV70734PDQNT	X2SON	DQN	4	250	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV70736DQNR	X2SON	DQN	4	3000	180.0	8.4	1.16	1.16	0.63	4.0	8.0	Q2
TLV70736DQNR	X2SON	DQN	4	3000	180.0	9.5	1.16	1.16	0.5	4.0	8.0	Q2
TLV70736DQNT	X2SON	DQN	4	250	180.0	9.5	1.16	1.16	0.5	4.0	8.0	Q2
TLV70736DQNT	X2SON	DQN	4	250	180.0	8.4	1.16	1.16	0.63	4.0	8.0	Q2
TLV70736PDQNR	X2SON	DQN	4	3000	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2
TLV70736PDQNT	X2SON	DQN	4	250	180.0	8.4	1.16	1.16	0.5	4.0	8.0	Q2

TAPE AND REEL BOX DIMENSIONS



*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
TLV707085DQNR	X2SON	DQN	4	3000	184.0	184.0	19.0
TLV707085DQNT	X2SON	DQN	4	250	184.0	184.0	19.0
TLV70710DQNR	X2SON	DQN	4	3000	184.0	184.0	19.0
TLV70710DQNR	X2SON	DQN	4	3000	183.0	183.0	20.0
TLV70710DQNT	X2SON	DQN	4	250	184.0	184.0	19.0
TLV70710DQNT	X2SON	DQN	4	250	183.0	183.0	20.0
TLV70710PDQNR	X2SON	DQN	4	3000	184.0	184.0	19.0
TLV70710PDQNR	X2SON	DQN	4	3000	183.0	183.0	20.0
TLV70710PDQNT	X2SON	DQN	4	250	183.0	183.0	20.0
TLV70710PDQNT	X2SON	DQN	4	250	184.0	184.0	19.0
TLV707115DQNR	X2SON	DQN	4	3000	183.0	183.0	20.0
TLV707115DQNR	X2SON	DQN	4	3000	184.0	184.0	19.0
TLV707115DQNT	X2SON	DQN	4	250	184.0	184.0	19.0
TLV707115DQNT	X2SON	DQN	4	250	183.0	183.0	20.0
TLV70711PDQNR	X2SON	DQN	4	3000	184.0	184.0	19.0
TLV70711PDQNT	X2SON	DQN	4	250	184.0	184.0	19.0
TLV70712APDQNR	X2SON	DQN	4	3000	210.0	185.0	35.0
TLV70712PDQNR	X2SON	DQN	4	3000	210.0	185.0	35.0

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
TLV70712PDQNT	X2SON	DQN	4	250	210.0	185.0	35.0
TLV707135DQNR	X2SON	DQN	4	3000	184.0	184.0	19.0
TLV707135DQNT	X2SON	DQN	4	250	184.0	184.0	19.0
TLV70715PDQNR	X2SON	DQN	4	3000	183.0	183.0	20.0
TLV70715PDQNR	X2SON	DQN	4	3000	184.0	184.0	19.0
TLV70715PDQNT	X2SON	DQN	4	250	183.0	183.0	20.0
TLV70715PDQNT	X2SON	DQN	4	250	184.0	184.0	19.0
TLV70717DQNR	X2SON	DQN	4	3000	184.0	184.0	19.0
TLV70717DQNT	X2SON	DQN	4	250	184.0	184.0	19.0
TLV707185ADQNR	X2SON	DQN	4	3000	210.0	185.0	35.0
TLV707185DQNR	X2SON	DQN	4	3000	184.0	184.0	19.0
TLV707185DQNT	X2SON	DQN	4	250	210.0	185.0	35.0
TLV707185PDQNR	X2SON	DQN	4	3000	210.0	185.0	35.0
TLV707185PDQNT	X2SON	DQN	4	250	210.0	185.0	35.0
TLV70718ADQNR	X2SON	DQN	4	3000	210.0	185.0	35.0
TLV70718APDQNR	X2SON	DQN	4	3000	210.0	185.0	35.0
TLV70718DQNR	X2SON	DQN	4	3000	184.0	184.0	19.0
TLV70718DQNR	X2SON	DQN	4	3000	210.0	185.0	35.0
TLV70718DQNT	X2SON	DQN	4	250	210.0	185.0	35.0
TLV70718PDQNR	X2SON	DQN	4	3000	184.0	184.0	19.0
TLV70718PDQNR	X2SON	DQN	4	3000	210.0	185.0	35.0
TLV70718PDQNRG4	X2SON	DQN	4	3000	210.0	185.0	35.0
TLV70718PDQNT	X2SON	DQN	4	250	210.0	185.0	35.0
TLV70719PDQNR	X2SON	DQN	4	3000	210.0	185.0	35.0
TLV70719PDQNT	X2SON	DQN	4	250	210.0	185.0	35.0
TLV70725DQNR	X2SON	DQN	4	3000	210.0	185.0	35.0
TLV70725DQNT	X2SON	DQN	4	250	210.0	185.0	35.0
TLV70725PDQNR	X2SON	DQN	4	3000	210.0	185.0	35.0
TLV70725PDQNT	X2SON	DQN	4	250	210.0	185.0	35.0
TLV70726DQNR	X2SON	DQN	4	3000	184.0	184.0	19.0
TLV70726DQNR	X2SON	DQN	4	3000	183.0	183.0	20.0
TLV70726DQNT	X2SON	DQN	4	250	184.0	184.0	19.0
TLV70726DQNT	X2SON	DQN	4	250	183.0	183.0	20.0
TLV70726PDQNR	X2SON	DQN	4	3000	184.0	184.0	19.0
TLV70726PDQNR	X2SON	DQN	4	3000	183.0	183.0	20.0
TLV70726PDQNT	X2SON	DQN	4	250	183.0	183.0	20.0
TLV70726PDQNT	X2SON	DQN	4	250	184.0	184.0	19.0
TLV70727PDQNR	X2SON	DQN	4	3000	210.0	185.0	35.0
TLV70727PDQNT	X2SON	DQN	4	250	210.0	185.0	35.0
TLV707285DQNR	X2SON	DQN	4	3000	210.0	185.0	35.0
TLV707285DQNT	X2SON	DQN	4	250	210.0	185.0	35.0
TLV707285PDQNR	X2SON	DQN	4	3000	210.0	185.0	35.0
TLV707285PDQNT	X2SON	DQN	4	250	210.0	185.0	35.0

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
TLV70728APDQNR	X2SON	DQN	4	3000	210.0	185.0	35.0
TLV70728PDQNR	X2SON	DQN	4	3000	184.0	184.0	19.0
TLV70728PDQNT	X2SON	DQN	4	250	210.0	185.0	35.0
TLV70729DQNR	X2SON	DQN	4	3000	184.0	184.0	19.0
TLV70729DQNR	X2SON	DQN	4	3000	210.0	185.0	35.0
TLV70729DQNT	X2SON	DQN	4	250	210.0	185.0	35.0
TLV70729PDQNR	X2SON	DQN	4	3000	184.0	184.0	19.0
TLV70729PDQNR	X2SON	DQN	4	3000	183.0	183.0	20.0
TLV70729PDQNT	X2SON	DQN	4	250	183.0	183.0	20.0
TLV70729PDQNT	X2SON	DQN	4	250	184.0	184.0	19.0
TLV70730ADQNR	X2SON	DQN	4	3000	210.0	185.0	35.0
TLV70730APDQNR	X2SON	DQN	4	3000	210.0	185.0	35.0
TLV70730DQNR	X2SON	DQN	4	3000	184.0	184.0	19.0
TLV70730DQNT	X2SON	DQN	4	250	210.0	185.0	35.0
TLV70730PDQNR	X2SON	DQN	4	3000	210.0	185.0	35.0
TLV70730PDQNR	X2SON	DQN	4	3000	184.0	184.0	19.0
TLV70730PDQNRG4	X2SON	DQN	4	3000	210.0	185.0	35.0
TLV70730PDQNT	X2SON	DQN	4	250	210.0	185.0	35.0
TLV70731DQNR	X2SON	DQN	4	3000	184.0	184.0	19.0
TLV70731DQNT	X2SON	DQN	4	250	184.0	184.0	19.0
TLV70732DQNR	X2SON	DQN	4	3000	210.0	185.0	35.0
TLV70732DQNR	X2SON	DQN	4	3000	184.0	184.0	19.0
TLV70732DQNT	X2SON	DQN	4	250	210.0	185.0	35.0
TLV707335ADQNR	X2SON	DQN	4	3000	210.0	185.0	35.0
TLV707335DQNR	X2SON	DQN	4	3000	184.0	184.0	19.0
TLV707335DQNT	X2SON	DQN	4	250	210.0	185.0	35.0
TLV70733ADQNR	X2SON	DQN	4	3000	210.0	185.0	35.0
TLV70733APDQNR	X2SON	DQN	4	3000	210.0	185.0	35.0
TLV70733DQNR	X2SON	DQN	4	3000	184.0	184.0	19.0
TLV70733DQNR	X2SON	DQN	4	3000	210.0	185.0	35.0
TLV70733DQNR1	X2SON	DQN	4	3000	210.0	185.0	35.0
TLV70733DQNT	X2SON	DQN	4	250	210.0	185.0	35.0
TLV70733PDQNR	X2SON	DQN	4	3000	184.0	184.0	19.0
TLV70733PDQNT	X2SON	DQN	4	250	210.0	185.0	35.0
TLV70734DQNR	X2SON	DQN	4	3000	184.0	184.0	19.0
TLV70734DQNR	X2SON	DQN	4	3000	210.0	185.0	35.0
TLV70734DQNT	X2SON	DQN	4	250	210.0	185.0	35.0
TLV70734PDQNR	X2SON	DQN	4	3000	210.0	185.0	35.0
TLV70734PDQNT	X2SON	DQN	4	250	210.0	185.0	35.0
TLV70736DQNR	X2SON	DQN	4	3000	183.0	183.0	20.0
TLV70736DQNR	X2SON	DQN	4	3000	184.0	184.0	19.0
TLV70736DQNT	X2SON	DQN	4	250	184.0	184.0	19.0
TLV70736DQNT	X2SON	DQN	4	250	183.0	183.0	20.0

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
TLV70736PDQNR	X2SON	DQN	4	3000	210.0	185.0	35.0
TLV70736PDQNT	X2SON	DQN	4	250	210.0	185.0	35.0

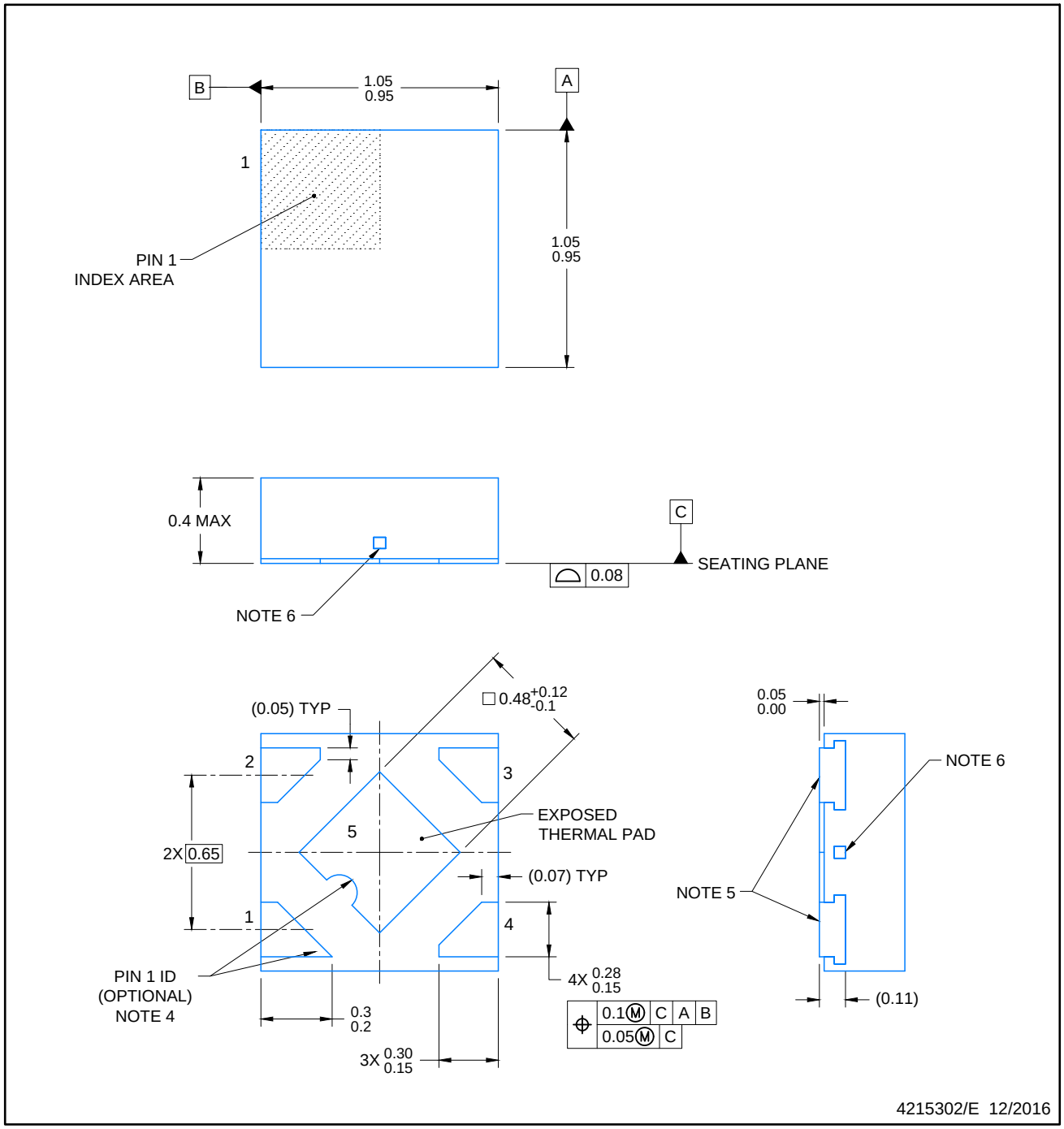
DQN 4

X2SON - 0.4 mm max height

PLASTIC SMALL OUTLINE - NO LEAD

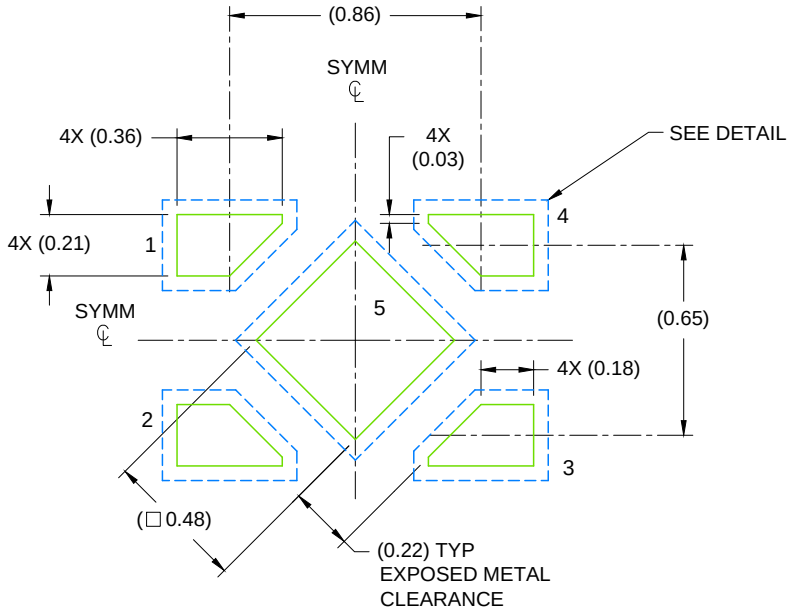
Images above are just a representation of the package family, actual package may vary.
Refer to the product data sheet for package details.

TEXAS INSTRUMENTS

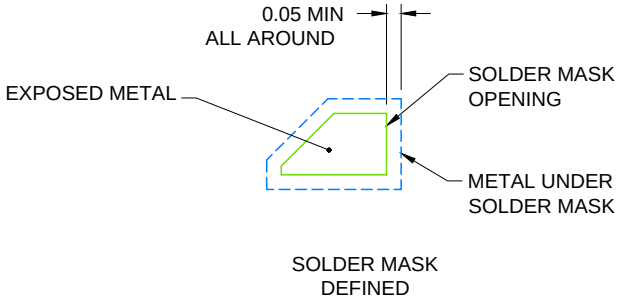


NOTES:

1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. The package thermal pad must be soldered to the printed circuit board for optimal thermal and mechanical performance.
4. Features may not exist. Recommend use of pin 1 marking on top of package for orientation purposes.
5. Shape of exposed side leads may differ.
6. Number and location of exposed tie bars may vary.



LAND PATTERN EXAMPLE
SCALE: 40X

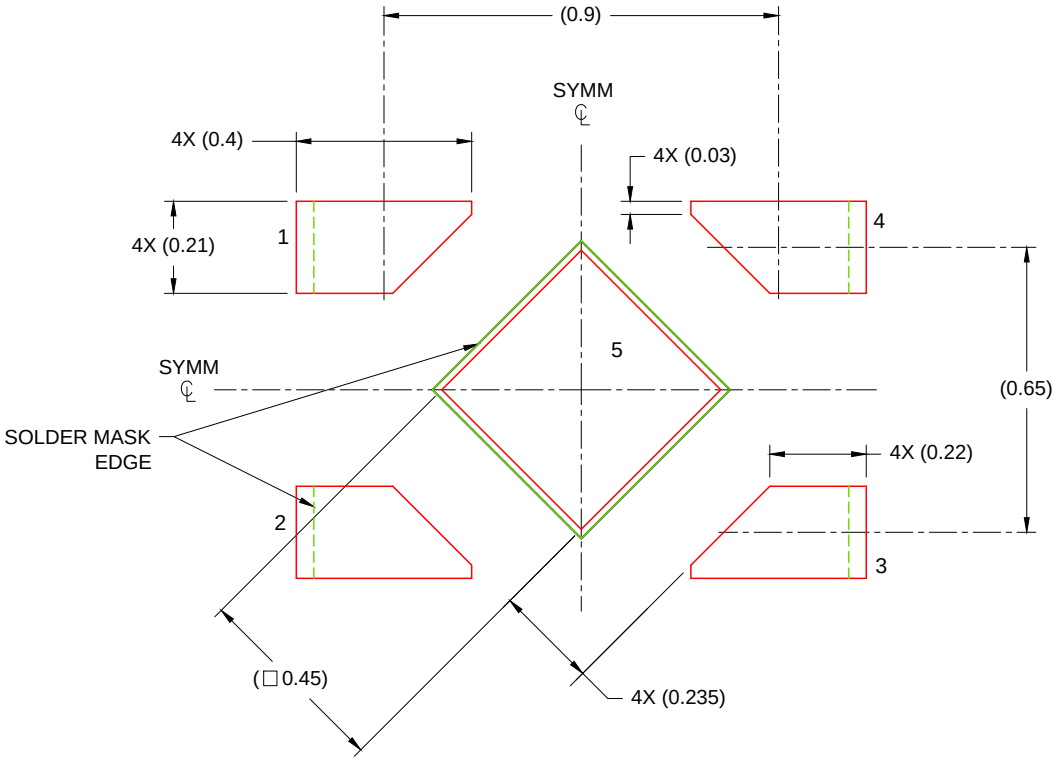


SOLDER MASK DETAIL

4215302/E 12/2016

NOTES: (continued)

7. This package is designed to be soldered to a thermal pad on the board. For more information, see Texas Instruments literature number SLUA271 (www.ti.com/lit/slua271).
8. If any vias are implemented, it is recommended that vias under paste be filled, plugged or tented.



SOLDER PASTE EXAMPLE
BASED ON 0.075 - 0.1mm THICK STENCIL

EXPOSED PAD
88% PRINTED SOLDER COVERAGE BY AREA
SCALE: 60X

4215302/E 12/2016

NOTES: (continued)

9. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.

IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATASHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES “AS IS” AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, regulatory or other requirements.

These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you fully indemnify TI and its representatives against any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI’s products are provided subject to TI’s Terms of Sale, TI’s General Quality Guidelines, or other applicable terms available either on ti.com or provided in conjunction with such TI products. TI’s provision of these resources does not expand or otherwise alter TI’s applicable warranties or warranty disclaimers for TI products. Unless TI explicitly designates a product as custom or customer-specified, TI products are standard, catalog, general purpose devices.

TI objects to and rejects any additional or different terms you may propose.

Copyright © 2026, Texas Instruments Incorporated

Last updated 10/2025